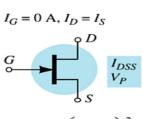
# **Electronic Circuits Elektronik Devreler**

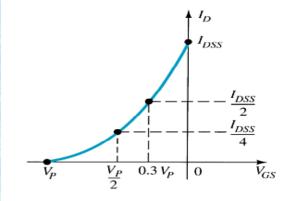
Dr. Gökhan Bilgin

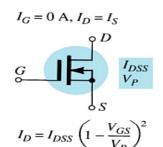
gokhanb@ce.yildiz.edu.tr

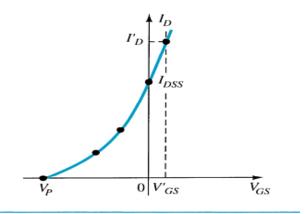
# **Electronic Circuits Questions**

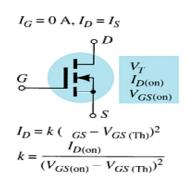


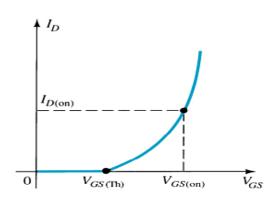
$$I_D = I_{DSS} \left( 1 - \frac{V_{GS}}{V_P} \right)^2$$











Sketch the transfer curve defined by  $I_{DSS} = 12 \text{ mA}$  and  $V_P = -6 \text{ V}$ .

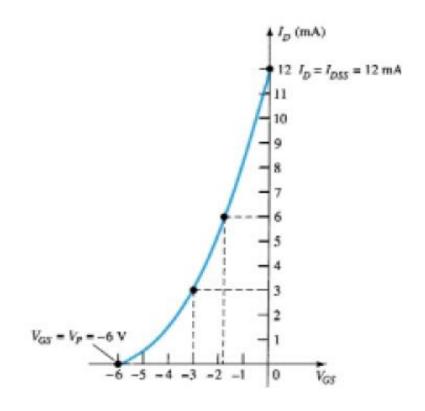
#### Solution

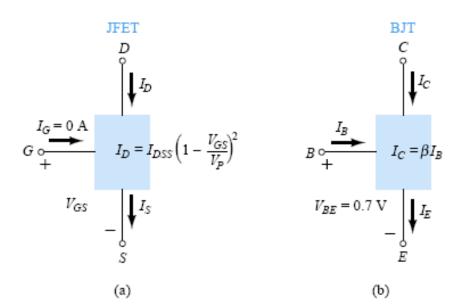
and

Two plot points are defined by

$$I_{DSS} = 12 \text{ mA}$$
 and  $V_{GS} = 0 \text{ V}$   
 $I_D = 0 \text{ mA}$  and  $V_{GS} = V_P$ 

At  $V_{GS} = V_P/2 = -6$  V/2 = -3 V the drain current will be determined by  $I_D = I_{DSS}/4 = 12$  mA/4 = 3 mA. At  $I_D = I_{DSS}/2 = 12$  mA/2 = 6 mA the gate-to-source voltage is determined by  $V_{GS} \cong 0.3V_P = 0.3(-6 \text{ V}) = -1.8 \text{ V}$ . All four plot points are well defined on Fig. 5.16 with the complete transfer curve.





$$JFET \qquad BJT$$

$$I_D = I_{DSS} \left( 1 - \frac{V_{GS}}{V_P} \right)^2 \iff I_C = \beta I_B$$

$$I_D = I_S \qquad \Leftrightarrow \qquad I_C \cong I_E$$

$$I_G \cong 0 \text{ A} \qquad \Leftrightarrow \qquad V_{BE} \cong 0.7 \text{ V}$$

$$(5.10)$$

Sketch the transfer characteristics for an *n*-channel depletion-type MOSFET with  $I_{DSS} = 10$  mA and  $V_P = -4$  V.

#### Solution

At 
$$V_{GS} = 0 \text{ V}$$
,  $I_D = I_{DSS} = 10 \text{ mA}$ 

$$V_{GS} = V_P = -4 \text{ V}$$
,  $I_D = 0 \text{ mA}$ 

$$V_{GS} = \frac{V_P}{2} = \frac{-4 \text{ V}}{2} = -2 \text{ V}$$
,  $I_D = \frac{I_{DSS}}{4} = \frac{10 \text{ mA}}{4} = 2.5 \text{ mA}$ 

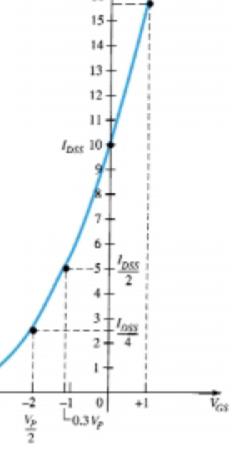
and at 
$$I_D = \frac{I_{DSS}}{2}$$
,  $V_{GS} = 0.3V_P = 0.3(-4 \text{ V}) = -1.2 \text{ V}$ 

all of which appear in Fig. 5.27.

Before plotting the positive region of  $V_{GS}$ , keep in mind that  $I_D$  increases very rapidly with increasing positive values of  $V_{GS}$ . In other words, be conservative with the choice of values to be substituted into Shockley's equation. In this case, we will try +1 V as follows:

$$I_D = I_{DSS} \left( 1 - \frac{V_{GS}}{V_P} \right)^2$$
  
= 10 mA $\left( 1 - \frac{+1 \text{ V}}{-4 \text{ V}} \right)^2$  = 10 mA(1 + 0.25)<sup>2</sup> = 10 mA(1.5625)  
 $\approx$  15.63 mA

which is sufficiently high to finish the plot.



 $AI_D(mA)$ 

Using the data provided on the specification sheet of Fig. 5.39 and an average threshold voltage of  $V_{GS(Th)} = 3$  V, determine:

- (a) The resulting value of k for the MOSFET.
- (b) The transfer characteristics.

#### Solution

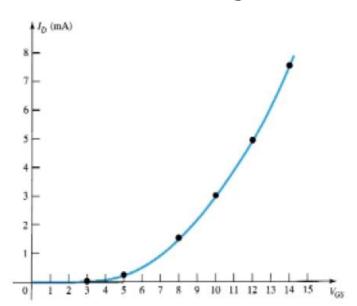
(a) Eq. (5.14): 
$$k = \frac{I_{D(\text{on})}}{(V_{GS(\text{on})} - V_{GS(\text{Th})})^2}$$
  
 $= \frac{3 \text{ mA}}{(10 \text{ V} - 3 \text{ V})^2} = \frac{3 \text{ mA}}{(7 \text{ V})^2} = \frac{3 \times 10^{-3}}{49} \text{ A/V}^2$   
 $= \mathbf{0.061} \times \mathbf{10}^{-3} \text{ A/V}^2$ 

(b) Eq. (5.13): 
$$I_D = k(V_{GS} - V_T)^2$$
  
= 0.061 × 10<sup>-3</sup> $(V_{GS} - 3 \text{ V})^2$ 

For  $V_{GS} = 5 \text{ V}$ ,

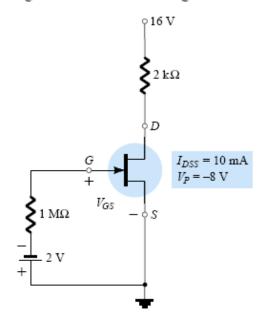
$$I_D = 0.061 \times 10^{-3} (5 \text{ V} - 3 \text{ V})^2 = 0.061 \times 10^{-3} (2)^2$$
  
= 0.061 × 10<sup>-3</sup>(4) = 0.244 mA

For  $V_{GS} = 8$ , 10, 12, and 14 V,  $I_D$  will be 1.525, 3 (as defined), 4.94, and 7.38 mA, respectively. The transfer characteristics are sketched in Fig. 5.40.



Determine the following for the network of Fig. 6.6.

- (a)  $V_{GSQ}$ .
- (b)  $I_{DQ}$ .
- (c) V<sub>DS</sub>.
- (d) V<sub>D</sub>.
- (e) V<sub>G</sub>.
- (f) V<sub>S</sub>.



# Solution

# Mathematical Approach:

(a) 
$$V_{GS_Q} = -V_{GG} = -2 \text{ V}$$

(b) 
$$I_{DQ} = I_{DSS} \left( 1 - \frac{V_{GS}}{V_P} \right)^2 = 10 \text{ mA} \left( 1 - \frac{-2 \text{ V}}{-8 \text{ V}} \right)^2$$
  
= 10 mA(1 - 0.25)<sup>2</sup> = 10 mA(0.75)<sup>2</sup> = 10 mA(0.5625)  
= **5.625 mA**

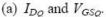
(c) 
$$V_{DS} = V_{DD} - I_D R_D = 16 \text{ V} - (5.625 \text{ mA})(2 \text{ k}\Omega)$$
  
= 16 V - 11.25 V = **4.75 V**

(d) 
$$V_D = V_{DS} = 4.75 \text{ V}$$

(e) 
$$V_G = V_{GS} = -2 \text{ V}$$

(f) 
$$V_S = 0 \text{ V}$$

Determine the following for the network of Fig. 6.24.





- (c) V<sub>S</sub>.
- (d) V<sub>DS</sub>.
- (e) V<sub>DG</sub>.

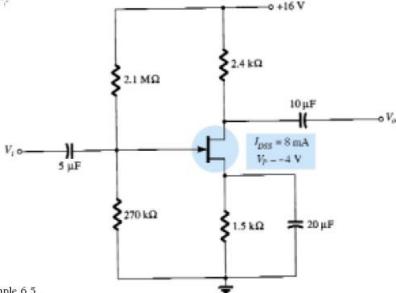


Figure 6.24 Example 6.5.

(a) For the transfer characteristics, if  $I_D = I_{DSS}/4 = 8$  mA/4 = 2 mA, then  $V_{GS} = V_P/2 = -4$  V/2 = -2 V. The resulting curve representing Shockley's equation appears in Fig. 6.25. The network equation is defined by

$$V_G = \frac{R_2 V_{DD}}{R_1 + R_2}$$

$$= \frac{(270 \text{ k}\Omega)(16 \text{ V})}{2.1 \text{ M}\Omega + 0.27 \text{ M}\Omega}$$

$$= 1.82 \text{ V}$$

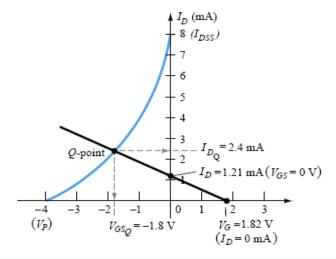
$$V_{GS} = V_G - I_D R_S$$

$$= 1.82 \text{ V} - I_D (1.5 \text{ k}\Omega)$$

and

When  $I_D = 0$  mA:

$$V_{GS} = +1.82 \text{ V}$$



When  $V_{GS} = 0$  V:

$$I_D = \frac{1.82 \text{ V}}{1.5 \text{ k}\Omega} = 1.21 \text{ mA}$$

The resulting bias line appears on Fig. 6.25 with quiescent values of

$$I_{DO} = 2.4 \text{ mA}$$

and

$$V_{GS_Q} = -1.8 \; \mathrm{V}$$

(b) 
$$V_D = V_{DD} - I_D R_D$$
  
= 16 V - (2.4 mA)(2.4 k $\Omega$ )  
= 10.24 V

(c) 
$$V_S = I_D R_S = (2.4 \text{ mA})(1.5 \text{ k}\Omega)$$
  
= 3.6 V

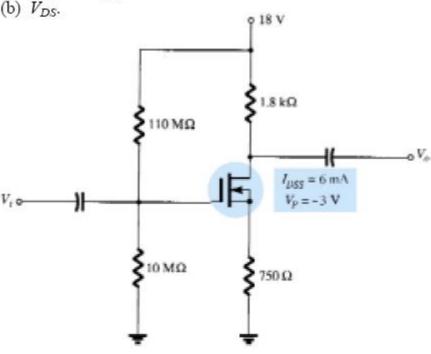
(d) 
$$V_{DS} = V_{DD} - I_D(R_D + R_S)$$
  
= 16 V - (2.4 mA)(2.4 k $\Omega$  + 1.5 k $\Omega$ )  
= **6.64** V  
or  $V_{DS} = V_D - V_S = 10.24$  V - 3.6 V  
= **6.64** V

(e) Although seldom requested, the voltage  $V_{DG}$  can easily be determined using

$$V_{DG} = V_D - V_G$$
  
= 10.24 V - 1.82 V  
= **8.42** V

For the *n*-channel depletion-type MOSFET of Fig. 6.29, determine:

 $\begin{array}{ll} \text{(a)} \ \ I_{D\mathcal{Q}} \ \text{and} \ \ V_{GS\mathcal{Q}}. \\ \text{(b)} \ \ V_{DS}. \end{array}$ 



(a) For the transfer characteristics, a plot point is defined by  $I_D = I_{DSS}/4 = 6 \text{ mA}/4 = 1.5 \text{ mA}$  and  $V_{GS} = V_P/2 = -3 \text{ V}/2 = -1.5 \text{ V}$ . Considering the level of  $V_P$  and the fact that Shockley's equation defines a curve that rises more rapidly as  $V_{GS}$  becomes more positive, a plot point will be defined at  $V_{GS} = +1 \text{ V}$ . Substituting into Shockley's equation yields

$$I_D = I_{DSS} \left( 1 - \frac{V_{GS}}{V_P} \right)^2$$

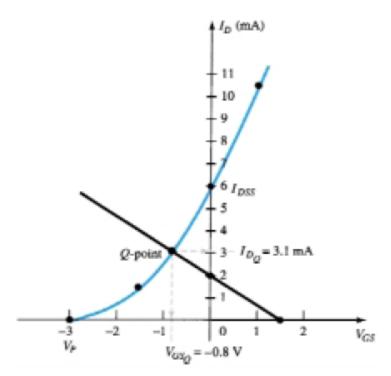
$$= 6 \text{ mA} \left( 1 - \frac{+1 \text{ V}}{-3 \text{ V}} \right)^2 = 6 \text{ mA} \left( 1 + \frac{1}{3} \right)^2 = 6 \text{ mA} (1.778)$$

$$= 10.67 \text{ mA}$$

The resulting transfer curve appears in Fig. 6.30. Proceeding as described for JFETs, we have:

Eq. (6.15): 
$$V_G = \frac{10 \text{ M}\Omega(18 \text{ V})}{10 \text{ M}\Omega + 110 \text{ M}\Omega} = 1.5 \text{ V}$$

Eq. (6.16): 
$$V_{GS} = V_G - I_D R_S = 1.5 \text{ V} - I_D (750 \Omega)$$



**Figure 6.30** Determining the *Q*-point for the network of Fig. 6.29.

Setting  $I_D = 0$  mA results in

$$V_{GS} = V_G = 1.5 \text{ V}$$

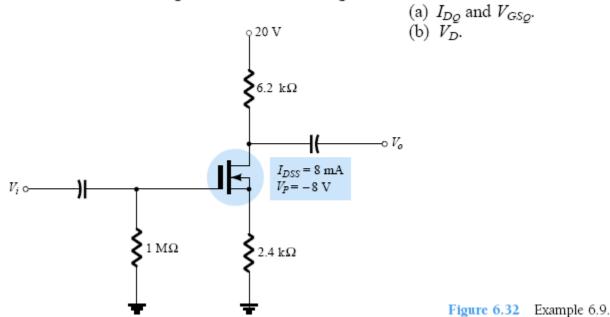
Setting  $V_{GS} = 0$  V yields

$$I_D = \frac{V_G}{R_S} = \frac{1.5 \text{ V}}{750 \Omega} = 2 \text{ mA}$$

The plot points and resulting bias line appear in Fig. 6.30. The resulting operating point:

$$I_{DQ} = 3.1 \text{ mA}$$
 (b) Eq. (6.19):  $V_{DS} = V_{DD} - I_D(R_D + R_S)$  
$$= 18 \text{ V} - (3.1 \text{ mA})(1.8 \text{ k}\Omega + 750 \text{ }\Omega)$$
 
$$\cong 10.1 \text{ V}$$

Determine the following for the network of Fig. 6.32.



#### Solution

(a) The self-bias configuration results in

$$V_{GS} = -I_D R_S$$

as obtained for the JFET configuration, establishing the fact that  $V_{GS}$  must be less than zero volts. There is therefore no requirement to plot the transfer curve for positive values of  $V_{GS}$ , although it was done on this occasion to complete the transfer characteristics. A plot point for the transfer characteristics for  $V_{GS} < 0$  V is

$$I_D = \frac{I_{DSS}}{4} = \frac{8 \text{ mA}}{4} = 2 \text{ mA}$$

and

$$V_{GS} = \frac{V_P}{2} = \frac{-8 \text{ V}}{2} = -4 \text{ V}$$

and

$$I_D = I_{DSS} \left( 1 - \frac{V_{GS}}{V_P} \right)^2 = 8 \text{ mA} \left( 1 - \frac{+2 \text{ V}}{-8 \text{ V}} \right)^2$$
  
= 12.5 mA

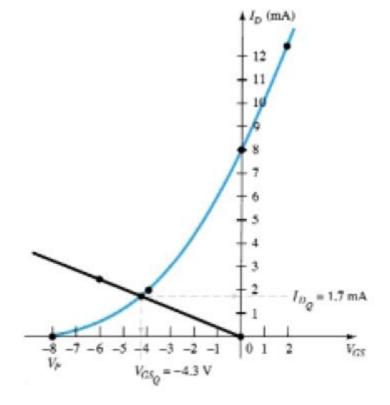
The resulting transfer curve appears in Fig. 6.33. For the network bias line, at  $V_{GS} = 0 \text{ V}$ ,  $I_D = 0 \text{ mA}$ . Choosing  $V_{GS} = -6 \text{ V}$  gives

$$I_D = -\frac{V_{GS}}{R_S} = -\frac{-6 \text{ V}}{2.4 \text{ k}\Omega} = 2.5 \text{ mA}$$

The resulting Q-point:

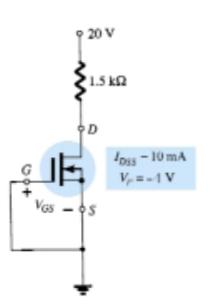
$$I_{D_Q} = 1.7 \text{ mA}$$
$$V_{GS_Q} = -4.3 \text{ V}$$

(b) 
$$V_D = V_{DD} - I_D R_D$$
  
= 20 V - (1.7 mA)(6.2 k $\Omega$ )  
= **9.46** V



# EXAMPLE 6.10

Determine  $V_{DS}$  for the network of Fig. 6.34.



#### Solution

The direct connection between the gate and source terminals requires that

$$V_{GS} = 0 \text{ V}$$

Since  $V_{GS}$  is fixed at 0 V, the drain current must be  $I_{DSS}$  (by definition). In other words,

$$V_{GS_O} = 0 \text{ V}$$

and

$$I_{DQ} = 10 \text{ mA}$$

There is therefore no need to draw the transfer curve and

$$V_D = V_{DD} - I_D R_D = 20 \text{ V} - (10 \text{ mA})(1.5 \text{ k}\Omega)$$
  
= 20 V - 15 V  
= 5 V

Determine  $I_{DQ}$  and  $V_{DSQ}$  for the enhancement-type MOSFET of Fig. 6.39.

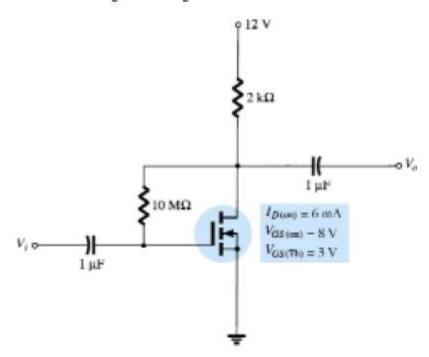


Figure 6.39 Example 6.11.

#### Plotting the Transfer Curve:

Two points are defined immediately as shown in Fig. 6.40. Solving for k:

Eq. (6.26): 
$$k = \frac{I_{D(\text{on})}}{(V_{GS(\text{on})} - V_{GS(\text{Th})})^2}$$
$$= \frac{6 \text{ mA}}{(8 \text{ V} - 3 \text{ V})^2} = \frac{6 \times 10^{-3}}{25} \text{A/v}^2$$
$$= 0.24 \times 10^{-3} \text{ A/V}^2$$

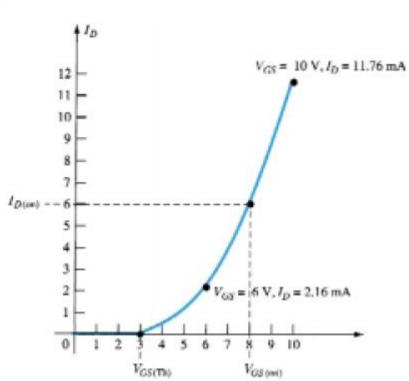
For  $V_{GS} = 6 \text{ V}$  (between 3 and 8 V):

$$I_D = 0.24 \times 10^{-3} (6 \text{ V} - 3 \text{ V})^2 = 0.24 \times 10^{-3} (9)$$
  
= 2.16 mA

as shown on Fig. 6.40. For  $V_{GS} = 10 \text{ V}$  (slightly greater than  $V_{GS(Th)}$ ):

$$I_D = 0.24 \times 10^{-3} (10 \text{ V} - 3 \text{ V})^2 = 0.24 \times 10^{-3} (49)$$
  
= 11.76 mA

as also appearing on Fig. 6.40. The four points are sufficient to plot the full curve for the range of interest as shown in Fig. 6.40.



#### For the Network Bias Line:

$$\begin{split} V_{GS} &= V_{DD} - I_D R_D \\ &= 12 \text{ V} - I_D (2 \text{ k}\Omega) \\ \text{Eq. (6.29):} \quad V_{GS} &= V_{DD} = 12 \text{ V} \big|_{I_D \,=\, 0 \text{ mA}} \\ \text{Eq. (6.30):} \quad I_D &= \frac{V_{DD}}{R_D} = \frac{12 \text{ V}}{2 \text{ k}\Omega} = 6 \text{ mA} \big|_{V_{GS} \,=\, 0 \text{ V}} \end{split}$$

The resulting bias line appears in Fig. 6.41.

At the operating point:

$$I_{DQ} = 2.75 \text{ mA}$$
  
 $V_{GSQ} = 6.4 \text{ V}$   
 $V_{DSQ} = V_{GSQ} = 6.4 \text{ V}$ 

and with

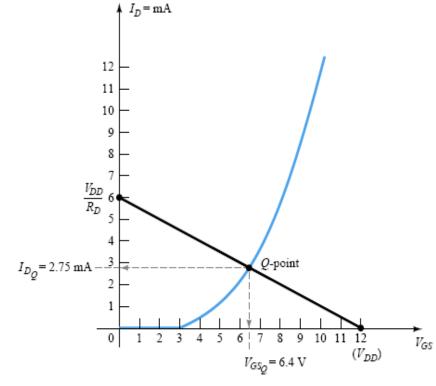


Figure 6.41 Determining the Q-point for the network of Fig. 6.39.

Determine  $I_{DQ}$ ,  $V_{GSQ}$ , and  $V_{DS}$  for the network of Fig. 6.43.

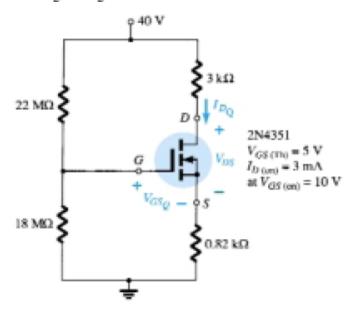


Figure 6.43 Example 6.12.

# Solution

Network:

Eq. (6.31): 
$$V_G = \frac{R_2 V_{DD}}{R_1 + R_2} = \frac{(18 \text{ M}\Omega)(40 \text{ V})}{22 \text{ M}\Omega + 18 \text{ M}\Omega} = 18 \text{ V}$$

Eq. (6.32): 
$$V_{GS} = V_G - I_D R_S = 18 \text{ V} - I_D (0.82 \text{ k}\Omega)$$

When  $I_D = 0$  mA,

$$V_{GS} = 18 \text{ V} - (0 \text{ mA})(0.82 \text{ k}\Omega) = 18 \text{ V}$$

as appearing on Fig. 6.44. When  $V_{GS} = 0 \text{ V}$ ,

$$V_{GS} = 18 \text{ V} - I_D(0.82 \text{ k}\Omega)$$

$$0 = 18 \text{ V} - I_D(0.82 \text{ k}\Omega)$$

$$I_D = \frac{18 \text{ V}}{0.82 \text{ k}\Omega} = 21.95 \text{ mA}$$

as appearing on Fig. 6.44.

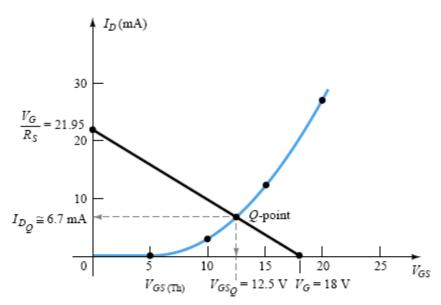


Figure 6.44 Determining the Q-point for the network of Example 6.12.

$$V_{GS(Th)} = 5 \text{ V},$$
  $I_{D(on)} = 3 \text{ mA with } V_{GS(on)} = 10 \text{ V}$   
Eq. (6.26):  $k = \frac{I_{D(on)}}{(V_{GS(on)} - V_{GS(Th)})^2}$   
 $= \frac{3 \text{ mA}}{(10 \text{ V} - 5 \text{ V})^2} = 0.12 \times 10^{-3} \text{ A/V}^2$   
 $I_D = k(V_{GS} - V_{GS(Th)})^2$   
 $= 0.12 \times 10^{-3} (V_{GS} - 5)^2$ 

and

which is plotted on the same graph (Fig. 6.44). From Fig. 6.44,

$$I_{DQ} \cong$$
 **6.7 mA**

$$V_{GSQ} = \mathbf{12.5 V}$$
Eq. (6.33):  $V_{DS} = V_{DD} - I_D(R_S + R_D)$ 

$$= 40 \text{ V} - (6.7 \text{ mA})(0.82 \text{ k}\Omega + 3.0 \text{ k}\Omega)$$

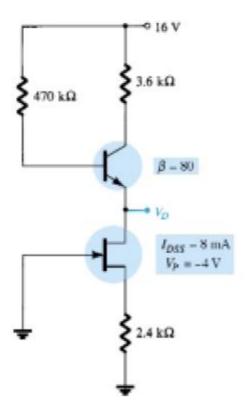
$$= 40 \text{ V} - 25.6 \text{ V}$$

$$= \mathbf{14.4 V}$$

TABLE 6.1 FET Bias Configurations Graphical Solution Configuration Туре Pertinent Equations  $qV_{DD}$  $\xi R_D$ JFET  $V_{GS_O} = -V_{GG}$  $V_{DS} = V_{DD} - I_D R_S$ Fixed-bias O-point  $\tilde{V}_P V_{GG} 0$  $V_{GS}$  $I_{DSS}$  ${\stackrel{\circ}{\xi}} V_{DD} {\stackrel{\circ}{\xi}} R_D$ JFET  $V_{GS} = -I_D R_S$  $V_{DS} = V_{DD} - I_D(R_D + R_S)$ Self-bias  $\S R_S$  $V_{GS}$  $\neg V_{DD}$  $\langle R_D$  $V_G = \frac{R_2 V_{DD}}{R_1 + R_2}$ JFET ξR, Voltage-divider  $V_{GS} = V_G - I_D R_S$ Q-point bias  $V_{DS} = V_{DD} - I_D(R_D + R_S)$  $V_G$   $V_{GS}$  ${\stackrel{\circ}{\xi}} V_{DD}$  $I_{DSS}$ **JFET**  $V_{GS} = V_{SS} - I_D R_S$ Q-point  $V_{DS} = V_{DD} + V_{SS} - I_D(R_D + R_S)$ Common-gate  $R_S = 1_{-V_{SS}}$  $V_{SS}$   $V_{GS}$ Q-point  $I_{DSS}$  $_{\mathbb{Q}}V_{DD}$  $V_{GSQ} = 0 \text{ V}$ **JFET**  $V_{GS_Q} = 0 \text{ V}$  $I_{DQ} = I_{DSS}$  $(V_{GSQ} = 0 \text{ V})$  $V_{GS}$ 

		I	
JFET $(R_D = 0 \ \Omega)$	$R_G \stackrel{V_{DD}}{\underset{=}{\swarrow}} R_S$	$V_{GS} = -I_D R_S$ $V_D = V_{DD}$ $V_S = I_D R_S$ $V_{DS} = V_{DD} - I_S R_S$	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$
Depletion-type MOSFET Fixed-bias	$V_{DD}$ $R_{D}$ $R_{D}$ $V_{GG}$	$V_{GS_Q} = +V_{GG}$ $V_{DS} = V_{DD} - I_DR_S$	$I_{DSS} \stackrel{I_D}{\not \! \! \! \! \! \! \! \! \! \! \! \! \! \! \! \! \! \! \!$
Depletion-type MOSFET Voltage-divider bias	$R_1$ $R_D$ $R_D$ $R_S$	$V_G = \frac{R_2 V_{DD}}{R_1 + R_2}$ $V_{GS} = V_G - I_S R_S$ $V_{DS} = V_{DD} - I_D (R_D + R_S)$	$V_G R_S$ $I_{DSS}$ $I_D$ $Q$ -point $V_P$ $V_P$ $V_P$ $V_D$ $V_G$ $V_G$ $V_G$
Enhancement type MOSFET Feedback configuration	$R_{G} = \begin{bmatrix} V_{DD} \\ \vdots \\ R_{D} \end{bmatrix}$	$V_{GS} = V_{DS}$ $V_{GS} = V_{DD} - I_D R_D$	$I_{D(\mathtt{on})} = I_{D}$ $Q\text{-point}$ $0 \qquad V_{GS(\mathtt{Th})}  V_{GS(\mathtt{on})}  V_{GS}$
Enhancement- type MOSFET Voltage-divider bias	$R_1$ $R_D$ $R_2$ $R_S$	$V_G = \frac{R_2 V_{DD}}{R_1 + R_2}$ $V_{GS} = V_G - I_D R_S$	$\begin{array}{c c} V_G \\ \hline R_S \end{array} \hspace{-0.2cm} I_D \\ \hline 0 \hspace{0.2cm} V_{GS(\text{Th})} \hspace{0.2cm} V_G \hspace{0.2cm} V_{GS} \end{array}$

Determine  $V_D$  for the network of Fig. 6.47.



In this case, there is no obvious path to determine a voltage or current level for the transistor configuration. However, turning to the self-biased JFET, an equation for  $V_{GS}$  can be derived and the resulting quiescent point determined using graphical techniques. That is,

$$V_{GS} = -I_D R_S = -I_D (2.4 \text{ k}\Omega)$$

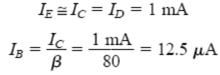
resulting in the self-bias line appearing in Fig. 6.48 that establishes a quiescent point at

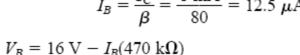
$$V_{GSQ} = -2.6 \text{ V}$$
  
 $I_{DO} = 1 \text{ mA}$ 

For the transistor,

and

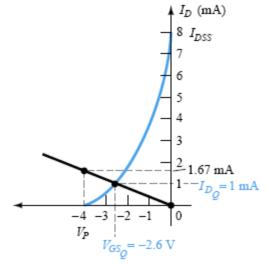
and





$$= 16 \text{ V} - (12.5 \text{ } \mu\text{A})(470 \text{ k}\Omega) = 16 \text{ V} - 5.875 \text{ V}$$
$$= 10.125 \text{ V}$$

$$V_E = V_D = V_B - V_{BE}$$
  
= 10.125 V - 0.7 V  
= 9.425 V



**Figure 6.48** Determining the *Q*-point for the network of Fig. 6.47.

# EXAMPLE 6.16

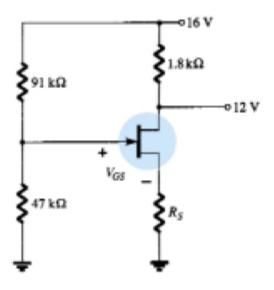


Figure 6.52 Example 6.16.

For the voltage-divider bias configuration of Fig. 6.52, if  $V_D = 12$  V and  $V_{GS_Q} = -2$  V, determine the value of  $R_S$ .

#### Solution

The level of  $V_G$  is determined as follows:

$$V_G = \frac{47 \text{ k}\Omega(16 \text{ V})}{47 \text{ k}\Omega + 91 \text{ k}\Omega} = 5.44 \text{ V}$$

$$I_D = \frac{V_{DD} - V_D}{R_D}$$

$$= \frac{16 \text{ V} - 12 \text{ V}}{18 \text{ k}\Omega} = 2.22 \text{ mA}$$

with

The equation for  $V_{GS}$  is then written and the known values substituted:

$$V_{GS} = V_G - I_D R_S$$
  
 $-2 \text{ V} = 5.44 \text{ V} - (2.22 \text{ mA}) R_S$   
 $-7.44 \text{ V} = -(2.22 \text{ mA}) R_S$   
 $R_S = \frac{7.44 \text{ V}}{2.22 \text{ mA}} = 3.35 \text{ k}\Omega$ 

and

The nearest standard commercial value is 3.3 k $\Omega$ .

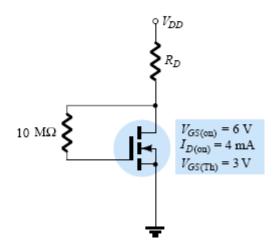


Figure 6.53 Example 6.17.

Given  $I_D = I_{D(on)} = 4$  mA and  $V_{GS} = V_{GS(on)} = 6$  V, for this configuration,

$$V_{DS} = V_{GS} = \frac{1}{2}V_{DD}$$

and

$$6 \text{ V} = \frac{1}{2} V_{DD}$$

so that

$$V_{DD} = 12 \text{ V}$$

Applying Eq. (6.34) yields

$$R_D = \frac{V_{R_D}}{I_D} = \frac{V_{DD} - V_{DS}}{I_{D(\text{on})}} = \frac{V_{DD} - \frac{1}{2}V_{DD}}{I_{D(\text{on})}} = \frac{\frac{1}{2}V_{DD}}{I_{D(\text{on})}}$$

and

$$R_D = \frac{6 \text{ V}}{4 \text{ mA}} = 1.5 \text{ k}\Omega$$

which is a standard commercial value.

Determine  $I_{DQ}$ ,  $V_{GSQ}$ , and  $V_{DS}$  for the p-channel JFET of Fig. 6.56.

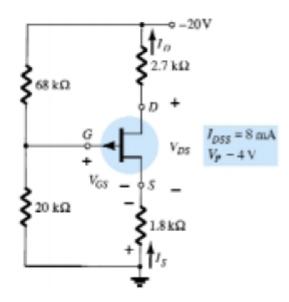


Figure 6.56 Example 6.18.

$$V_G = \frac{20 \text{ k}\Omega(-20 \text{ V})}{20 \text{ k}\Omega + 68 \text{ k}\Omega} = -4.55 \text{ V}$$

Applying Kirchhoff's voltage law gives

$$V_G - V_{GS} + I_D R_S = 0$$

and

$$V_{GS} = V_G + I_D R_S$$

Choosing  $I_D = 0$  mA yields

$$V_{GS} = V_G = -4.55 \text{ V}$$

as appearing in Fig. 6.57.

Choosing  $V_{GS} = 0$  V, we obtain

$$I_D = -\frac{V_G}{R_S} = -\frac{-4.55 \text{ V}}{1.8 \text{ k}\Omega} = 2.53 \text{ mA}$$

as also appearing in Fig. 6.57.

The resulting quiescent point from Fig. 6.57:

$$I_{DO} = 3.4 \text{ mA}$$

$$V_{GSQ} = 1.4 \text{ V}$$

For  $V_{DS}$ , Kirchhoff's voltage law will result in

$$-I_{D}R_{S} + V_{DS} - I_{D}R_{D} + V_{DD} = 0$$

and

$$V_{DS} = -V_{DD} + I_D(R_D + R_S)$$
  
= -20 V + (3.4 mA)(2.7 k $\Omega$  + 1.8 k $\Omega$ )  
= -20 V + 15.3 V  
= -4.7 V

