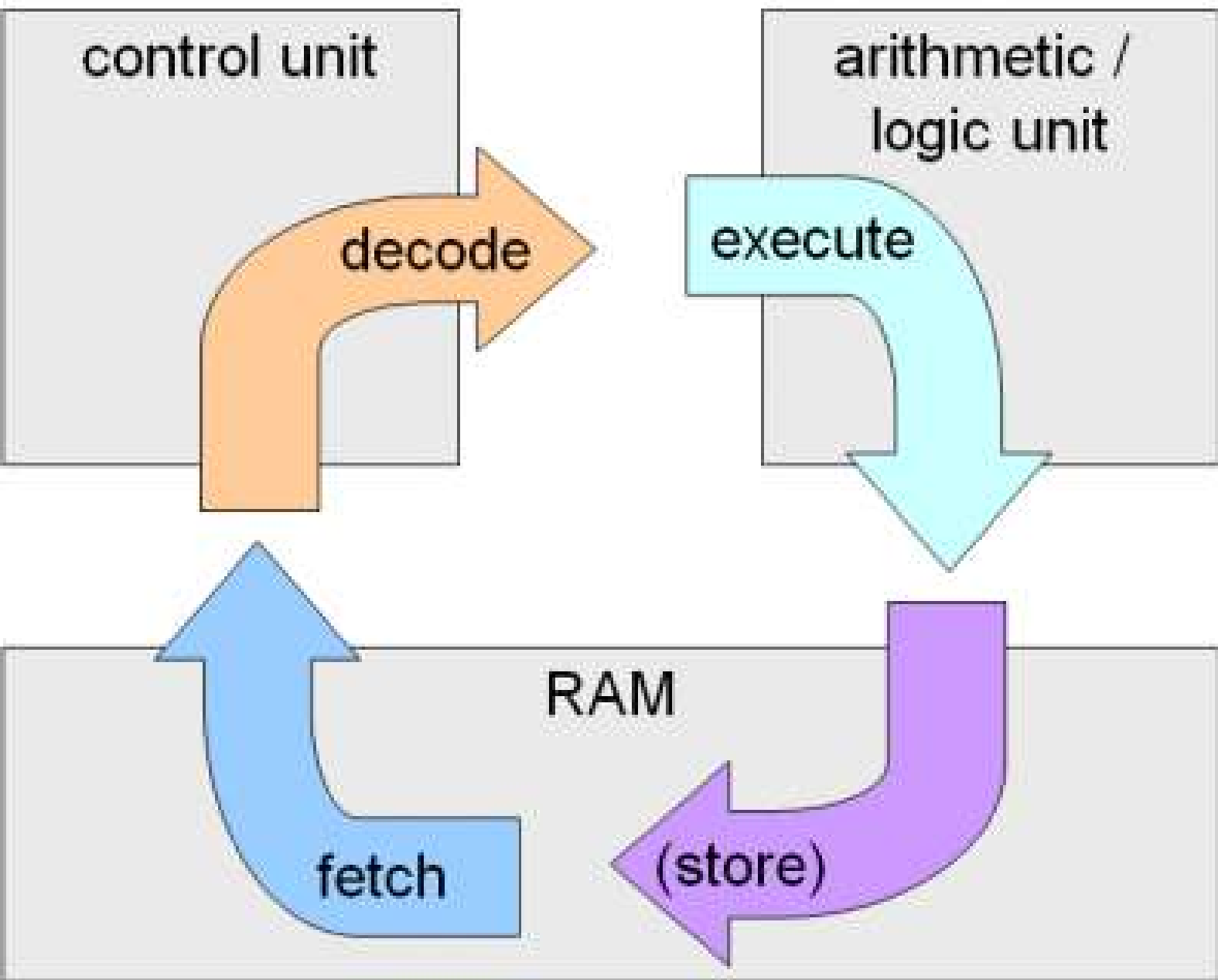


```
graph TD; A[Fetch the instruction] --> B[Decode the instruction]; B --> C[Execute the instruction]; C --> A;
```

Fetch the
instruction

Execute the
instruction

Decode the
instruction



Central Processing Unit (CPU) - Machine Cycle OR Instruction Cycle

