

Cryptography Engineering

“Blowfish Algorithm”

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Introduction:

Symmetric algorithms, sometimes called conventional algorithms, are algorithms where the encryption key can be calculated from the decryption key and vice versa. In most symmetric algorithms, the encryption key and the decryption key are the same. These algorithms, also called secret-key algorithms, single-key algorithms, or one-key algorithms, require that the sender and receiver agree on a key before they can communicate securely. The security of a symmetric algorithm rests in the key; divulging the key means that anyone could encrypt and decrypt messages. As long as the communication needs to remain secret, the key must remain secret. Blowfish is a symmetric cryptographic algorithm.

Blowfish Algorithm:

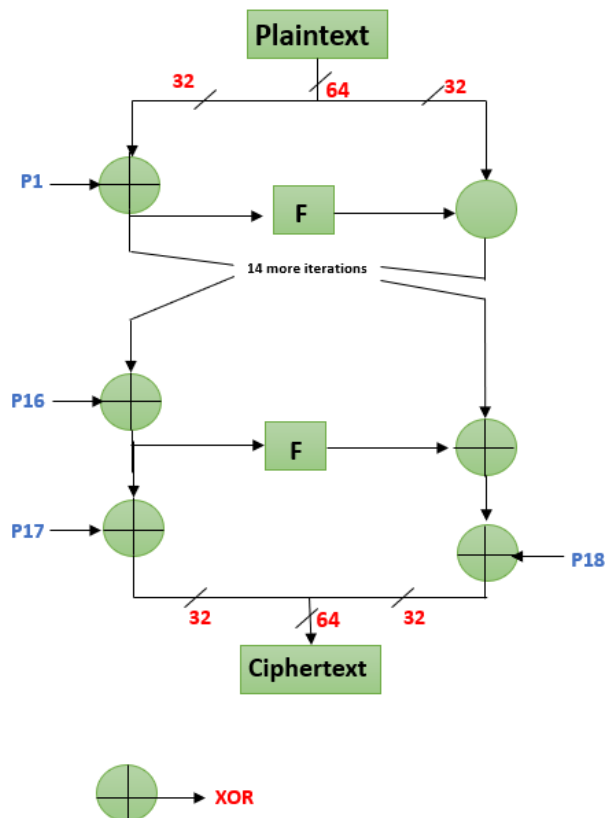
Blowfish is a symmetric encryption algorithm, meaning that it uses the same secret key to both encrypt and decrypt messages. Blowfish is also a block cipher, meaning that it divides a message up into fixed length blocks during encryption and decryption. The block length for Blowfish is 64 bits; messages that aren't a multiple of eight bytes in size must be padded.

Advantages and Disadvantages of Blowfish Algorithm:

- Blowfish is a fast block cipher except when changing keys. Each new key requires a pre-processing equivalent to 4KB of text.
- It is faster and much better than DES Encryption.
- Blowfish uses a 64-bit block size which makes it vulnerable to birthday attacks.
- A reduced round variant of blowfish is known to be susceptible to known plain text attacks(2nd order differential attacks – 4 rounds).

Applications of Blowfish Algorithm:

- Bulk Encryption.
- Packet Encryption(ATM Packets)
- Password Hashing



The entire encryption process can be elaborated as:

Blowfish consists of sixteen rounds. For each round, first XOR the left half of the block with the subkey for that round. Then apply the f-function to the left half of the block, and XOR the right half of the block with the result. Finally, after all but the last round, swap the halves of the block. There is only one subkey for each round; the f-function consumes no subkeys, but uses S-boxes which are key dependent. After the last round, XOR the *right* half with subkey 17, and the *left* half with subkey 18.

The f-function:

Blowfish uses four S-boxes. Each one has 256 entries, and each of the entries are 32 bits long. To calculate the f-function: use the first byte of the 32 bits of input to find an entry in the first S-box, the second byte to find an entry in the second S-box, and so on. The value of the f-function is $((S1(B1) + S2(B2)) \text{ XOR } S3(B3)) + S4(B4)$ where addition is performed modulo 2^{32} .

Subkey generation:

Blowfish uses a large number of subkeys. These keys must be pre computed before any data encryption or decryption.

The P-array consists of 18 32-bit subkeys:

P1, P2, ..., P18.

2. There are four 32-bit S-boxes with 256 entries each:

S1,0, S1,1, ..., S1,255;

S2,0, S2,1, ..., S2,255;

S3,0, S3,1, ..., S3,255;

S4,0, S4,1, ..., S4,255;

The exact method used to calculate these subkeys will be described later

The Blowfish Encryption Algorithm:

is a Feistel network consisting of 16 rounds (Figure.1). The input is a 64-bit data element, X. Divide x into two 32-bit halves: XL, XR

: For i = 1 to 16

$xL = XL \text{ XOR } P_i$

$xR = F(xL) \text{ XOR } xR$

Swap XL and xR

Swap XL and xR (Undo the last swap.)

$xR = xR \text{ XOR } P_{17}$

$xL = xL \text{ XOR } P_{18}$

Recombine xL and xR

The Blowfish Encryption pseudocode:

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The input is a 64-bit data element, x

Divide x into two 32-bit halves: xL, xR

Then, for i = 1 to 16:

$xL = xL \text{ XOR } P_i$

$xR = F(xL) \text{ XOR } xR$

Swap xL and xR

After the sixteenth round, swap xL and xR again to undo the last swap.

Then, $xR = xR \text{ XOR } P_{17}$ and $xL = xL \text{ XOR } P_{18}$

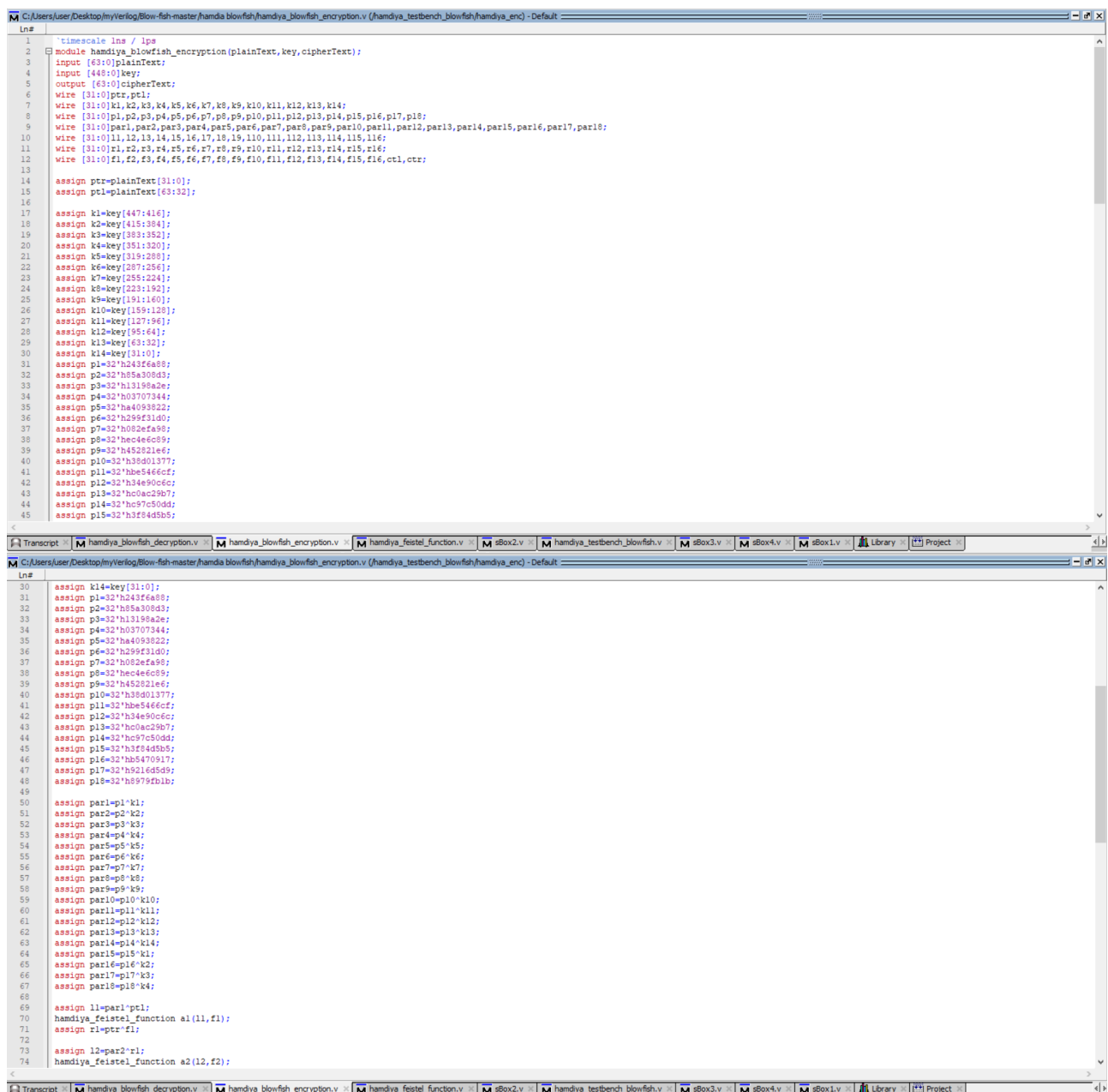
Finally, recombine xL and xR to get the ciphertext

Decryption is exactly the same as encryption, except that P1, P2,..., P18 are used in the reverse order. Implementations of Blowfish that require the fastest speeds should unroll the loop and ensure that all subkeys are stored in cache.

Implementation of Blowfish Encryption:

The code were written in Verilog and simulated with the Verilog simulator. The Verilog simulator ISIM is used here for the simulation. The key generation involving various steps and that can be used to generate the actual key for encryption. The decryption is done exactly same as the encryption except that the keys are used in the reverse order

blowfish_encryption:



```
1 timescale 1ns / 1ps
2 module handiya_blowfish_encryption(plainText, key, cipherText);
3 input [63:0] plainText;
4 input [448:0] key;
5 output [63:0] cipherText;
6 wire [31:0] ptc, p1;
7 wire [31:0] k1, k2, k3, k4, k5, k6, k7, k8, k9, k10, k11, k12, k13, k14;
8 wire [31:0] p1, p2, p3, p4, p5, p6, p7, p8, p9, p10, p11, p12, p13, p14, p15, p16, p17, p18;
9 wire [31:0] par1, par2, par3, par4, par5, par6, par7, par8, par9, par10, par11, par12, par13, par14, par15, par16, par17, par18;
10 wire [31:0] i1, i2, i3, i4, i5, i6, i7, i8, i9, i10, i11, i12, i13, i14, i15, i16;
11 wire [31:0] r1, r2, r3, r4, r5, r6, r7, r8, r9, r10, r11, r12, r13, r14, r15, r16;
12 wire [31:0] f1, f2, f3, f4, f5, f6, f7, f8, f9, f10, f11, f12, f13, f14, f15, f16, c1, c2;
13
14 assign ptc=plainText[31:0];
15 assign p1=plainText[63:32];
16
17 assign k1=key[447:416];
18 assign k2=key[415:384];
19 assign k3=key[383:352];
20 assign k4=key[351:320];
21 assign k5=key[319:288];
22 assign k6=key[287:256];
23 assign k7=key[255:224];
24 assign k8=key[223:192];
25 assign k9=key[191:160];
26 assign k10=key[159:128];
27 assign k11=key[127:96];
28 assign k12=key[95:64];
29 assign k13=key[63:32];
30 assign k14=key[31:0];
31 assign p1=32'h243f6a88;
32 assign p2=32'h85a308d3;
33 assign p3=32'h13198a2e;
34 assign p4=32'h03707344;
35 assign p5=32'h4093822;
36 assign p6=32'h299f31d0;
37 assign p7=32'h082efa98;
38 assign p8=32'hce4e6c09;
39 assign p9=32'h452821e6;
40 assign p10=32'h38d01377;
41 assign p11=32'hbe5466cf;
42 assign p12=32'h34e90c6c;
43 assign p13=32'h0ac2967f;
44 assign p14=32'hc97c50dd;
45 assign p15=32'h3f84d5b5;
46
47
48
49
50 assign par1=p1*k1;
51 assign par2=p2*k2;
52 assign par3=p3*k3;
53 assign par4=p4*k4;
54 assign par5=p5*k5;
55 assign par6=p6*k6;
56 assign par7=p7*k7;
57 assign par8=p8*k8;
58 assign par9=p9*k9;
59 assign par10=p10*k10;
60 assign par11=p11*k11;
61 assign par12=p12*k12;
62 assign par13=p13*k13;
63 assign par14=p14*k14;
64 assign par15=p15*k1;
65 assign par16=p16*k2;
66 assign par17=p17*k3;
67 assign par18=p18*k4;
68
69 assign i1=par1*ptc;
70 handiya_feistel_function a1(i1, f1);
71 assign i1=ptc-f1;
72
73 assign i2=par2*r1;
74 handiya_feistel_function a2(i2, f2);
```

```
C:\Users\User\Desktop\myVerilog\Blow-fish-master\hamdiya_blowfish\hamdiya_blowfish_encryption.v (hamdiya_testbench_blowfish\hamdiya_enc) - Default
Ln#
68
69 assign l1=par1*pt1;
70 hamdiya_feistel_function a1(l1,f1);
71 assign r1=ptr*f1;
72
73 assign l2=par2*r1;
74 hamdiya_feistel_function a2(l2,f2);
75 assign r2=l1*f2;
76
77 assign l3=par3*r2;
78 hamdiya_feistel_function a3(l3,f3);
79 assign r3=l2*f3;
80
81 assign l4=par4*r3;
82 hamdiya_feistel_function a4(l4,f4);
83 assign r4=l3*f4;
84
85 assign l5=par5*r4;
86 hamdiya_feistel_function a5(l5,f5);
87 assign r5=l4*f5;
88
89 assign l6=par6*r5;
90 hamdiya_feistel_function a6(l6,f6);
91 assign r6=l5*f6;
92
93 assign l7=par7*r6;
94 hamdiya_feistel_function a7(l7,f7);
95 assign r7=l6*f7;
96
97 assign l8=par8*r7;
98 hamdiya_feistel_function a8(l8,f8);
99 assign r8=l7*f8;
100
101 assign l9=par9*r8;
102 hamdiya_feistel_function a9(l9,f9);
103 assign r9=l8*f9;
104
105 assign l10=par10*r9;
106 hamdiya_feistel_function a10(l10,f10);
107 assign r10=l9*f10;
108
109 assign l11=par11*r10;
110 hamdiya_feistel_function a11(l11,f11);
111 assign r11=l10*f11;
112
113
114
115
116
117 assign l12=par12*r11;
118 hamdiya_feistel_function a12(l12,f12);
119 assign r12=l11*f12;
120
121 assign l13=par13*r12;
122 hamdiya_feistel_function a13(l13,f13);
123 assign r13=l12*f13;
124
125 assign l14=par14*r13;
126 hamdiya_feistel_function a14(l14,f14);
127 assign r14=l13*f14;
128
129 assign l15=par15*r14;
130 hamdiya_feistel_function a15(l15,f15);
131 assign r15=l14*f15;
132
133 assign l16=par16*r15;
134 hamdiya_feistel_function a16(l16,f16);
135 assign r16=l15*f16;
136
137 assign ctl=par18*116;
138 assign ctr=par17*r16;
139
140 assign cipherText=(ctl,ctr);
141
142 endmodule

Transcript | M hamdiya_blowfish_decryption.v | M hamdiya_blowfish_encryption.v | M hamdiya_feistel_function.v | M sBox2.v | M hamdiya_testbench_blowfish.v | M sBox3.v | M sBox4.v | M sBox1.v | Library | Project
```

blowfish_Decryption:

```
C:\Users\User\Desktop\myVerilog\Blow-fish-master\handa blowfish\hamdiya_blowfish_decryption.v (hamdiya_testbench_blowfish\hamdiya_dec) - Default
Ln#
1 timescale 1ns / 1ps
2 module hamdiya_blowfish_decryption(plainText,key,cipherText);
3 input [63:0]plainText;
4 input [448:0]key;
5 output [63:0]cipherText;
6 wire [31:0]ptr,ptcl;
7 wire [31:0]k1,k2,k3,k4,k5,k6,k7,k8,k9,k10,k11,k12,k13,k14;
8 wire [31:0]p1,p2,p3,p4,p5,p6,p7,p8,p9,p10,p11,p12,p13,p14,p15,p16,p17,p18;
9 wire [31:0]par1,par2,par3,par4,par5,par6,par7,par8,par9,par10,par11,par12,par13,par14,par15,par16,par17,par18;
10 wire [31:0]l1,l2,l3,l4,l5,l6,l7,l8,l9,l10,l11,l12,l13,l14,l15,l16;
11 wire [31:0]r1,r2,r3,r4,r5,r6,r7,r8,r9,r10,r11,r12,r13,r14,r15,r16;
12 wire [31:0]f1,f2,f3,f4,f5,f6,f7,f8,f9,f10,f11,f12,f13,f14,f15,f16,ctrl,ctr;
13
14 assign ptr=plainText[31:0];
15 assign ptcl=plainText[63:32];
16
17 assign k1=key[447:416];
18 assign k2=key[415:384];
19 assign k3=key[383:352];
20 assign k4=key[351:320];
21 assign k5=key[319:288];
22 assign k6=key[287:256];
23 assign k7=key[255:224];
24 assign k8=key[223:192];
25 assign k9=key[191:160];
26 assign k10=key[159:128];
27 assign k11=key[127:96];
28 assign k12=key[95:64];
29 assign k13=key[63:32];
30 assign k14=key[31:0];
31 assign p1=32'h2a3fca85;
32 assign p2=32'h85a308d3;
33 assign p3=32'h13198a2e;
34 assign p4=32'h03707344;
35 assign p5=32'h46093922;
36 assign p6=32'h25ef31d0;
37 assign p7=32'h082efa98;
38 assign p8=32'h6c4e6c89;
39 assign p9=32'h452821e6;
40 assign p10=32'h33d01377;
41 assign p11=32'hbe5466cf;
42 assign p12=32'h34e90c6c;
43 assign p13=32'h0ac29b7;
44 assign p14=32'hc97c50dd;
45 assign p15=32'h3f84d5b5;
46
47
48
49
50 assign par1=p1*k1;
51 assign par2=p2*k2;
52 assign par3=p3*k3;
53 assign par4=p4*k4;
54 assign par5=p5*k5;
55 assign par6=p6*k6;
56 assign par7=p7*k7;
57 assign par8=p8*k8;
58 assign par9=p9*k9;
59 assign par10=p10*k10;
60 assign par11=p11*k11;
61 assign par12=p12*k12;
62 assign par13=p13*k13;
63 assign par14=p14*k14;
64 assign par15=p15*k1;
65 assign par16=p16*k2;
66 assign par17=p17*k3;
67 assign par18=p18*k4;
68
69 assign l1=par18*ptcl;
70 hamdiya_feistel_function a1(l1,f1);
71 assign r1=ptr*f1;
72
73 assign l2=par17*r1;
74 hamdiya_feistel_function a2(l2,f2);
75 assign r2=l1*f2;
76
77 assign l3=par16*r2;
78 hamdiya_feistel_function a3(l3,f3);
79 assign r3=l2*f3;
80
81 assign l4=par15*r3;
82 hamdiya_feistel_function a4(l4,f4);
83 assign r4=l3*f4;
84
85 assign l5=par14*r4;
86 hamdiya_feistel_function a5(l5,f5);
```

```
C:\Users\User\Desktop\myVerilog\Blow-fish-master\handa blowfish\handya_blowfish_decryption.v (handya_testbench_blowfish\handya_dec) - Default
Ln#
81 assign l4=par15*r3;
82 handiya_feistel_function a4(l4,f4);
83 assign r4=l3*f4;
84
85 assign l5=par14*r4;
86 handiya_feistel_function a5(l5,f5);
87 assign r5=l4*f5;
88
89 assign l6=par13*r5;
90 handiya_feistel_function a6(l6,f6);
91 assign r6=l5*f6;
92
93 assign l7=par12*r6;
94 handiya_feistel_function a7(l7,f7);
95 assign r7=l6*f7;
96
97 assign l8=par11*r7;
98 handiya_feistel_function a8(l8,f8);
99 assign r8=l7*f8;
100
101 assign l9=par10*r8;
102 handiya_feistel_function a9(l9,f9);
103 assign r9=l8*f9;
104
105 assign l10=par9*r9;
106 handiya_feistel_function a10(l10,f10);
107 assign r10=l9*f10;
108
109 assign l11=par8*r10;
110 handiya_feistel_function a11(l11,f11);
111 assign r11=l10*f11;
112
113 assign l12=par7*r11;
114 handiya_feistel_function a12(l12,f12);
115 assign r12=l11*f12;
116
117 assign l13=par6*r12;
118 handiya_feistel_function a13(l13,f13);
119 assign r13=l12*f13;
120
121 assign l14=par5*r13;
122 handiya_feistel_function a14(l14,f14);
123 assign r14=l13*f14;
124
125 assign l15=par4*r14;
126
127
128
129
130
131
132
133 assign ctl=par1*l16;
134 assign ctr=par2*r16;
135
136 assign cipherText={ctl,ctr};
137
138 endmodule
139
140
Transcript | handya_blowfish_decryption.v | handya_blowfish_encryption.v | handiya_feistel_function.v | sBox2.v | handya_testbench_blowfish.v | sBox3.v | sBox4.v | sBox1.v | Library | Project
C:\Users\User\Desktop\myVerilog\Blow-fish-master\handa blowfish\handya_blowfish_decryption.v (handya_testbench_blowfish\handya_dec) - Default
Ln#
96
97 assign l8=par11*r7;
98 handiya_feistel_function a8(l8,f8);
99 assign r8=l7*f8;
100
101 assign l9=par10*r8;
102 handiya_feistel_function a9(l9,f9);
103 assign r9=l8*f9;
104
105 assign l10=par9*r9;
106 handiya_feistel_function a10(l10,f10);
107 assign r10=l9*f10;
108
109 assign l11=par8*r10;
110 handiya_feistel_function a11(l11,f11);
111 assign r11=l10*f11;
112
113 assign l12=par7*r11;
114 handiya_feistel_function a12(l12,f12);
115 assign r12=l11*f12;
116
117 assign l13=par6*r12;
118 handiya_feistel_function a13(l13,f13);
119 assign r13=l12*f13;
120
121 assign l14=par5*r13;
122 handiya_feistel_function a14(l14,f14);
123 assign r14=l13*f14;
124
125 assign l15=par4*r14;
126 handiya_feistel_function a15(l15,f15);
127 assign r15=l14*f15;
128
129 assign l16=par3*r15;
130 handiya_feistel_function a16(l16,f16);
131 assign r16=l15*f16;
132
133 assign ctl=par1*l16;
134 assign ctr=par2*r16;
135
136 assign cipherText={ctl,ctr};
137
138 endmodule
139
140
Transcript | handya_blowfish_decryption.v | handya_blowfish_encryption.v | handiya_feistel_function.v | sBox2.v | handya_testbench_blowfish.v | sBox3.v | sBox4.v | sBox1.v | Library | Project
```


Feistel_Function:

```
C:\Users\User\Desktop\myVerilog\Blow-fish-master\hamda blowfish\hamdiya_testel_function.v (hamdiya_testbench_blowfish\hamdiya_dec\@16) - Default
1 timescale 1ns / 1ps
2 module hamdiya_feistel_function(1,o);
3 input [31:0]i;
4 output [31:0]o;
5 wire [31:0]t1,t2,t3,t4;
6 sBox1 a1(i[31:24],t1);
7 sBox2 a2(i[23:16],t2);
8 sBox3 a3(i[15:8],t3);
9 sBox4 a4(i[7:0],t4);
10
11 assign u1=(t1+t2)>>>32;
12 assign u2=(t3^u1);
13 assign o=(u2+t4)>>>32;
14 endmodule
15
16
```

Simulated result of encryption and decryption:

