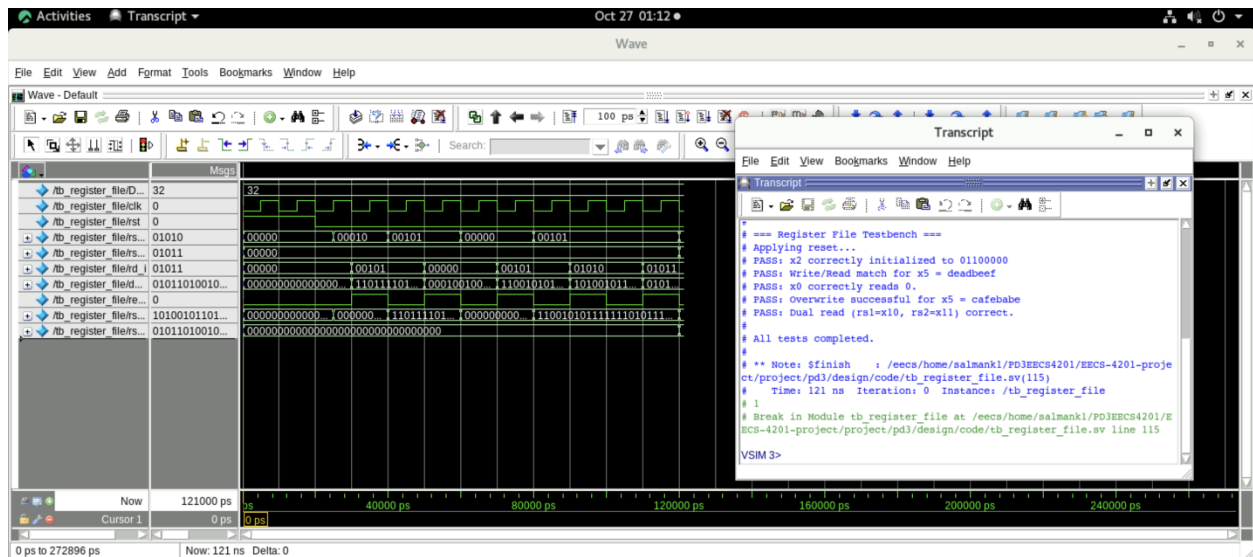# Team non

Members: Salman Kayani, Yousif Kndkji

Testbench Results

Challenges

1. Getting consistent results between the execute and register stages was difficult. Small initialization errors in the stack pointer (x2) and ALU result computation caused unexpected mismatches that took time to trace and debug.

2. A challenge faced was in resolving the interaction between stages to ensure that control and data signals lined up correctly as instructions moved through execution and write-back.