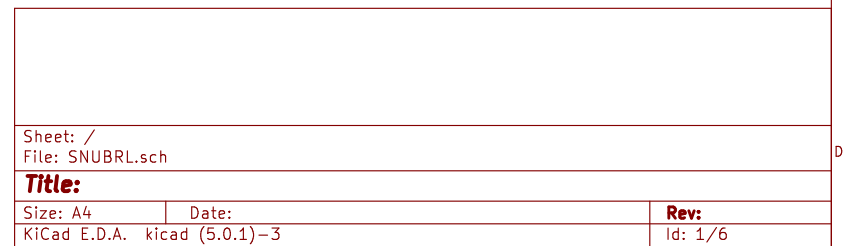
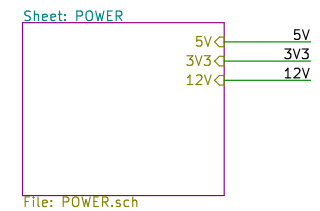


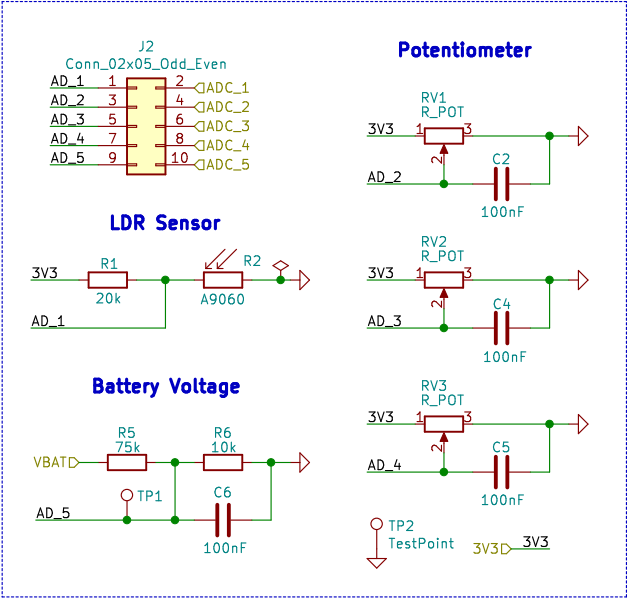
Nucleo F446RE Shield

J1
NUCLEO-64PIN-SHIELD

The diagram illustrates the pin connections for the Nucleo F446RE Shield J1 connector. The pins are organized into two main sections: L1-L20 and L21-L38. Connections include:

- GPIOs:** GPIO_1 (PC10), GPIO_2 (PC12), GPIO_3 (PC11), GPIO_4 (PD2), GPIO_5 (PB7), GPIO_6 (PA0), GPIO_7 (PC2), GPIO_8 (PC3), GPIO_9 (PB6), GPIO_10 (PF7), GPIO_11 (PA13), GPIO_12 (PA14), GPIO_13 (PA15), GPIO_14 (PB13), GPIO_15 (PB12), GPIO_16 (PB11), GPIO_17 (PB10), GPIO_18 (PB9), GPIO_19 (PB8), GPIO_20 (PB7).
- I2C:** I2C_SCL (PB10), I2C_SDA (PB3).
- SPI:** SPI_SCK (PA5), SPI_MISO (PA6), SPI_MOSI (PA7).
- UART:** USART_TX (PA2), USART_RX (PA3).
- Other:** MOT_CH1 (connected to R50 10k), B_UserBtn (PC13), PA0, PA1, PA4, PA8, PA9, PA10, PA11, PA12, PA14, PA15, PA16, PA17, PA18, PA19, PA20, PA21, PA22, PA23, PA24, PA25, PA26, PA27, PA28, PA29, PA30, PA31, PA32, PA33, PA34, PA35, PA36, PA37, PA38, PA39, PA40, PA41, PA42, PA43, PA44, PA45, PA46, PA47, PA48, PA49, PA50, PA51, PA52, PA53, PA54, PA55, PA56, PA57, PA58, PA59, PA60, PA61, PA62, PA63, PA64, PA65, PA66, PA67, PA68, PA69, PA70, PA71, PA72, PA73, PA74, PA75, PA76, PA77, PA78, PA79, PA80, PA81, PA82, PA83, PA84, PA85, PA86, PA87, PA88, PA89, PA90, PA91, PA92, PA93, PA94, PA95, PA96, PA97, PA98, PA99, PA100, PA101, PA102, PA103, PA104, PA105, PA106, PA107, PA108, PA109, PA110, PA111, PA112, PA113, PA114, PA115, PA116, PA117, PA118, PA119, PA120, PA121, PA122, PA123, PA124, PA125, PA126, PA127, PA128, PA129, PA130, PA131, PA132, PA133, PA134, PA135, PA136, PA137, PA138, PA139, PA140, PA141, PA142, PA143, PA144, PA145, PA146, PA147, PA148, PA149, PA150, PA151, PA152, PA153, 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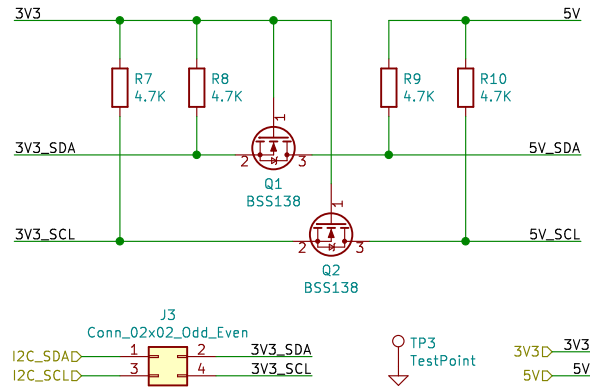
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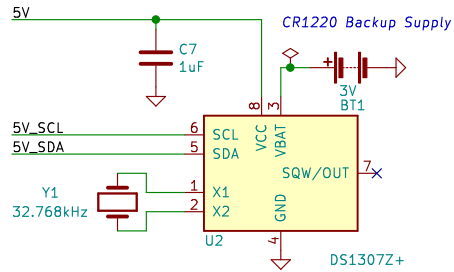
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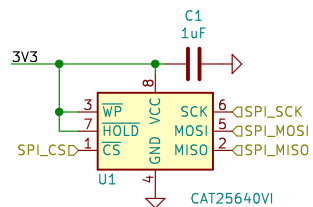
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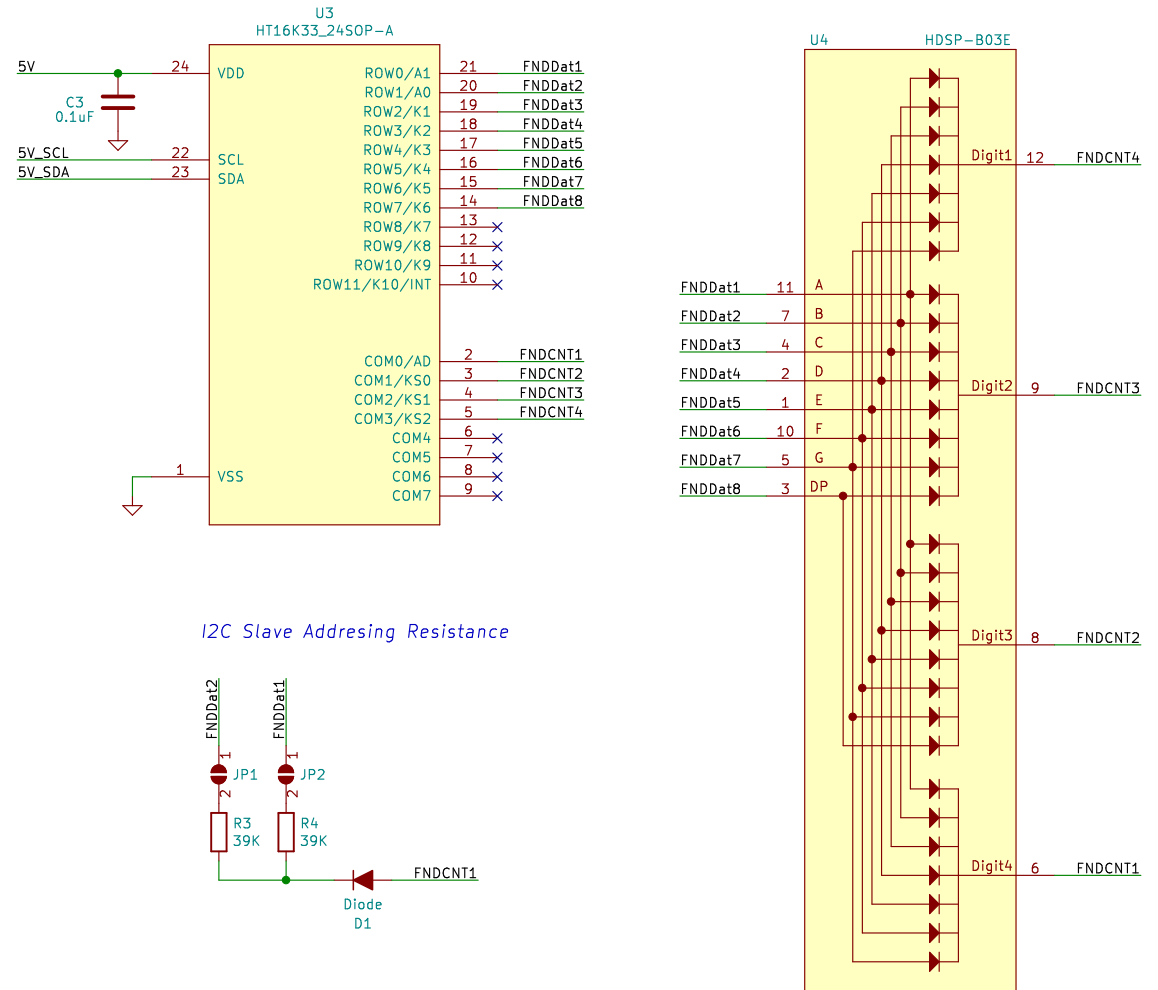
I2C Real Time Clock



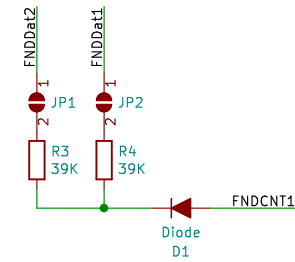
SPI EEPROM



I2C FND(7-Segment)x4



I2C Slave Addressing Resistance



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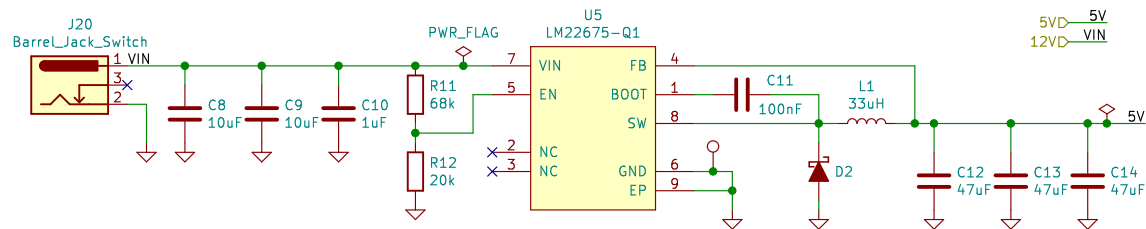
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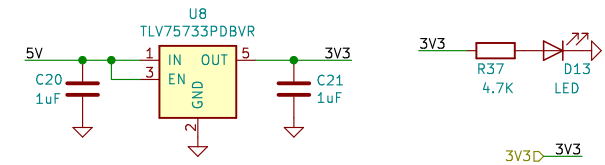
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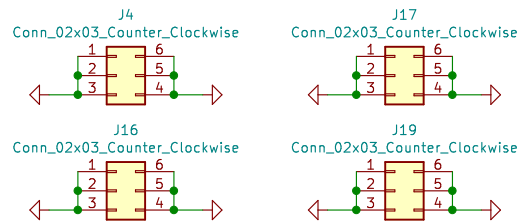
12V to 5V Regulator



5V to 3.3V Linear Regulator



Jumper Socket Cage



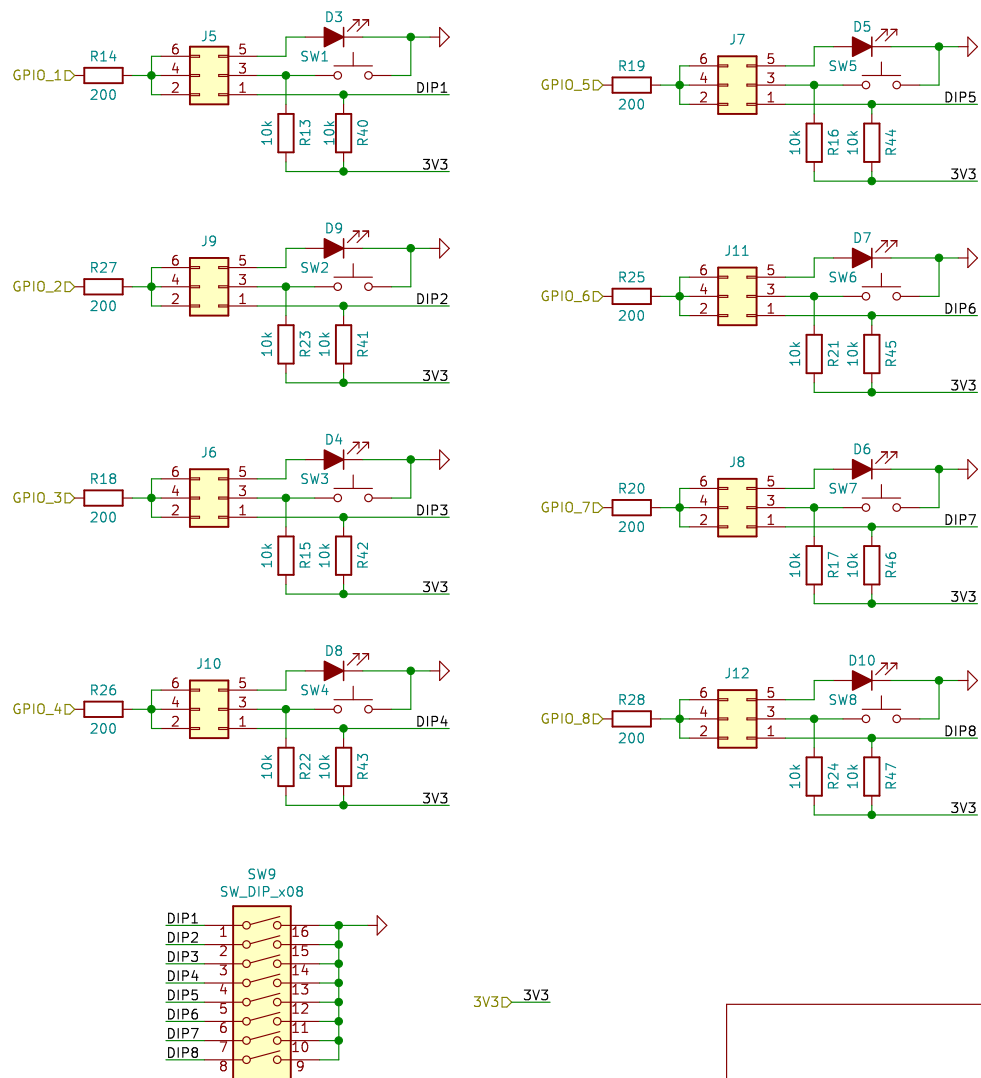
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GPIO Switch Grid

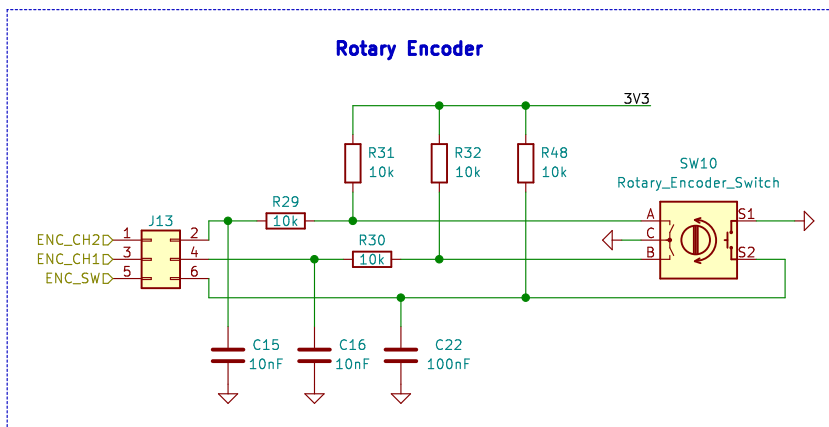
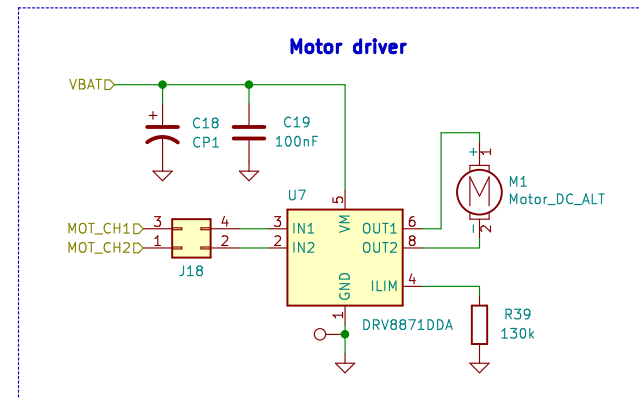
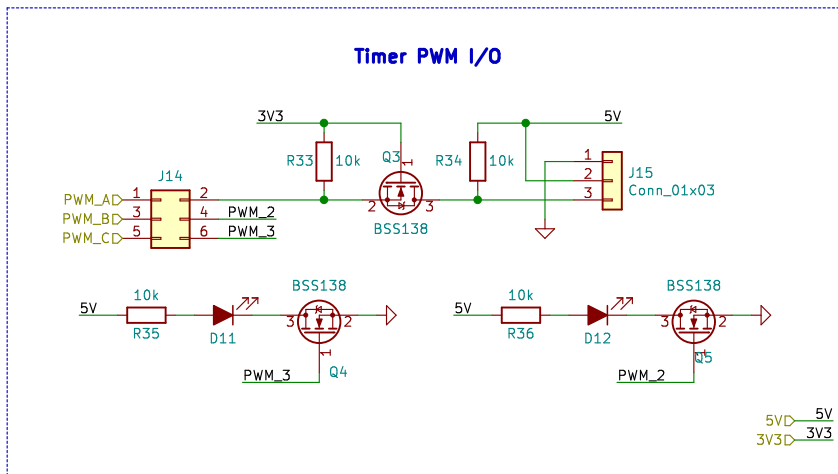


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