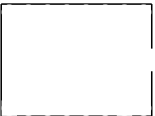


Ref

SD\_D0\_S1\_D1

Pololu AD



PowerIn

Vdd\_`nd\_GndH1\_VH1

Pololu AL



D1\_S1\_D0\_S0

D1\_A2\_D1

D1\_A2\_D1

VALERO