HW 11 CS 4111

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- 1.) Given a virtual memory system with:

 L> 37 virtual address bits

 L> 32 physicall address bits

 L> 32KB pages (15 bit page offset)
 - a. VPN = 37 16 = 22 bits PPN = 32 15 = 17 bits
- #of Virtual page table entries = 2 22 = 4,194,304 entries physical page # needs...
 Valid+ exec+ rd + dirty+ PPN

 1 + 1 + 1 + 1 + 17 = 21 bits
 - # of bits in page tabe = 21* 4,194, 304 = 88,080,384 #abits in PPT = A 2 ab. 4 bits or 107,94 bits
 - b. VPN is now = 38-15=33 bits # of VPT entries is now = $a^{23}=8,388,608$
 - " # of ppT biss = 21 * 8,388,608 = 176,160,768 bits # of bits in PPT = 2 27.4 bits or 108,25 bits
- C.TLB; 32 blocks; lentry/block; like (a) except

 VA = 36; PA = 32, PO = 15; assuming valid, exec, rd, & dirty

 PPN = 36-15 = 21 loga (32 blocks) = 5 bits in TLB idx

 PPN = 32-15 = 17 valtextribed Tog + PTN = bits/block

 TIR To VPN Tdx = 1 lentry block TLB_Tag = VPN-IdxrLB=)
 - 37*32 blocks= 1,184 bits in TLB d. See diagram on next page



