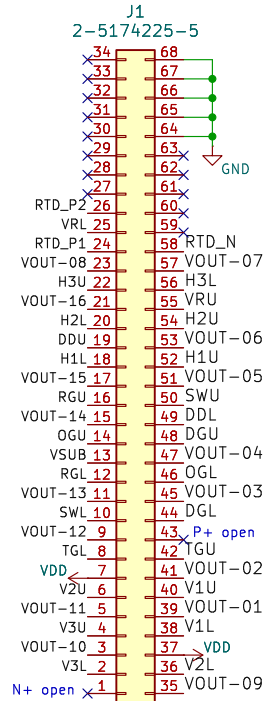
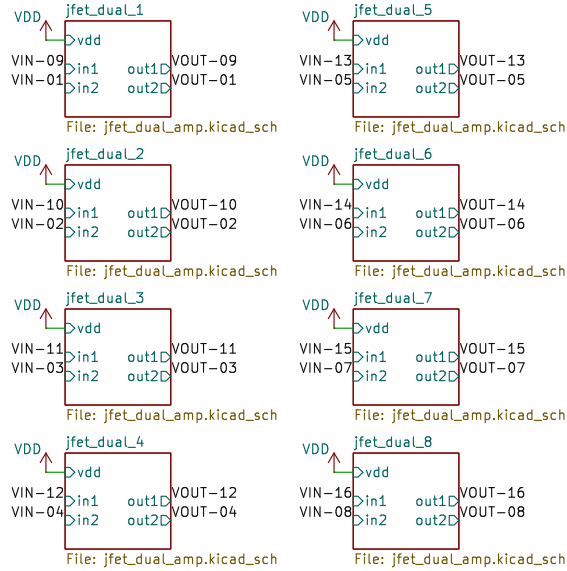


TO LTA

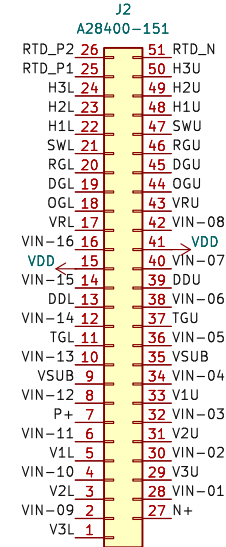


JFETS SOURCE FOLLOWER

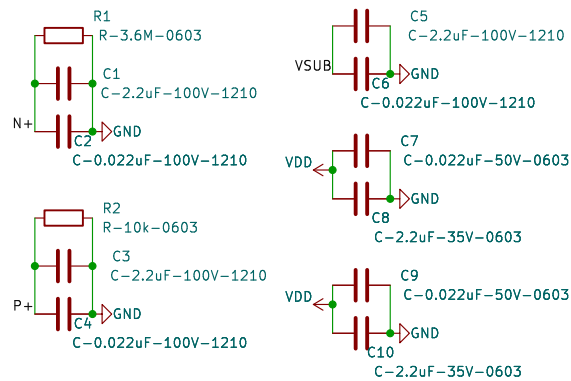


TO CCD

TODO: Verificar pines RTD_P1 y RTD_P2



BIAS



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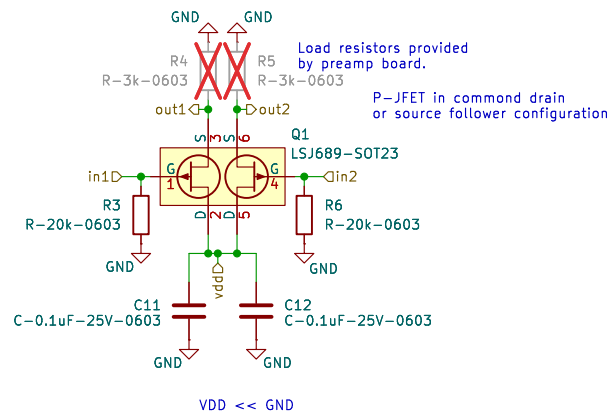
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KiCad E.D.A. 9.0.4

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Id: 1/9



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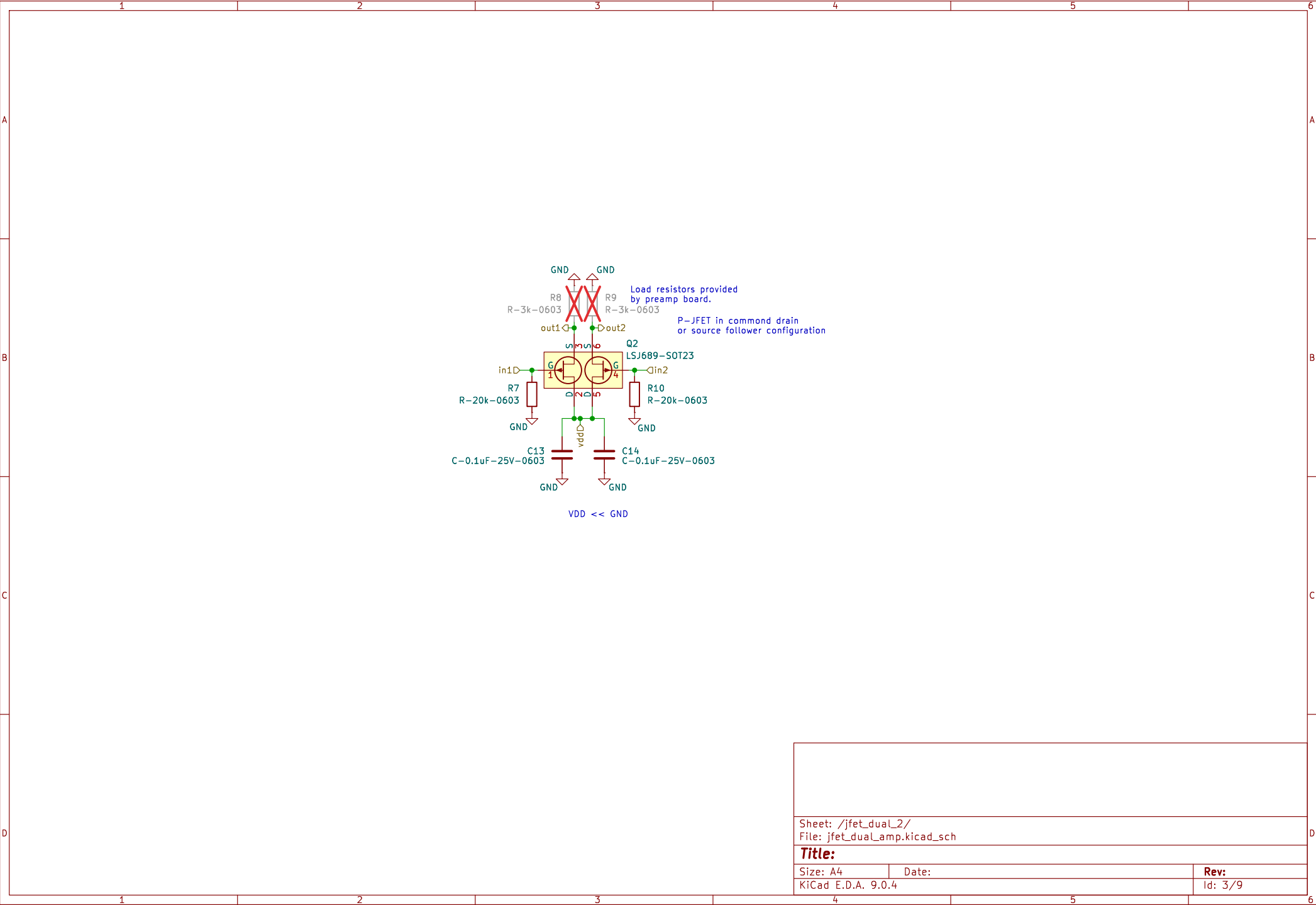
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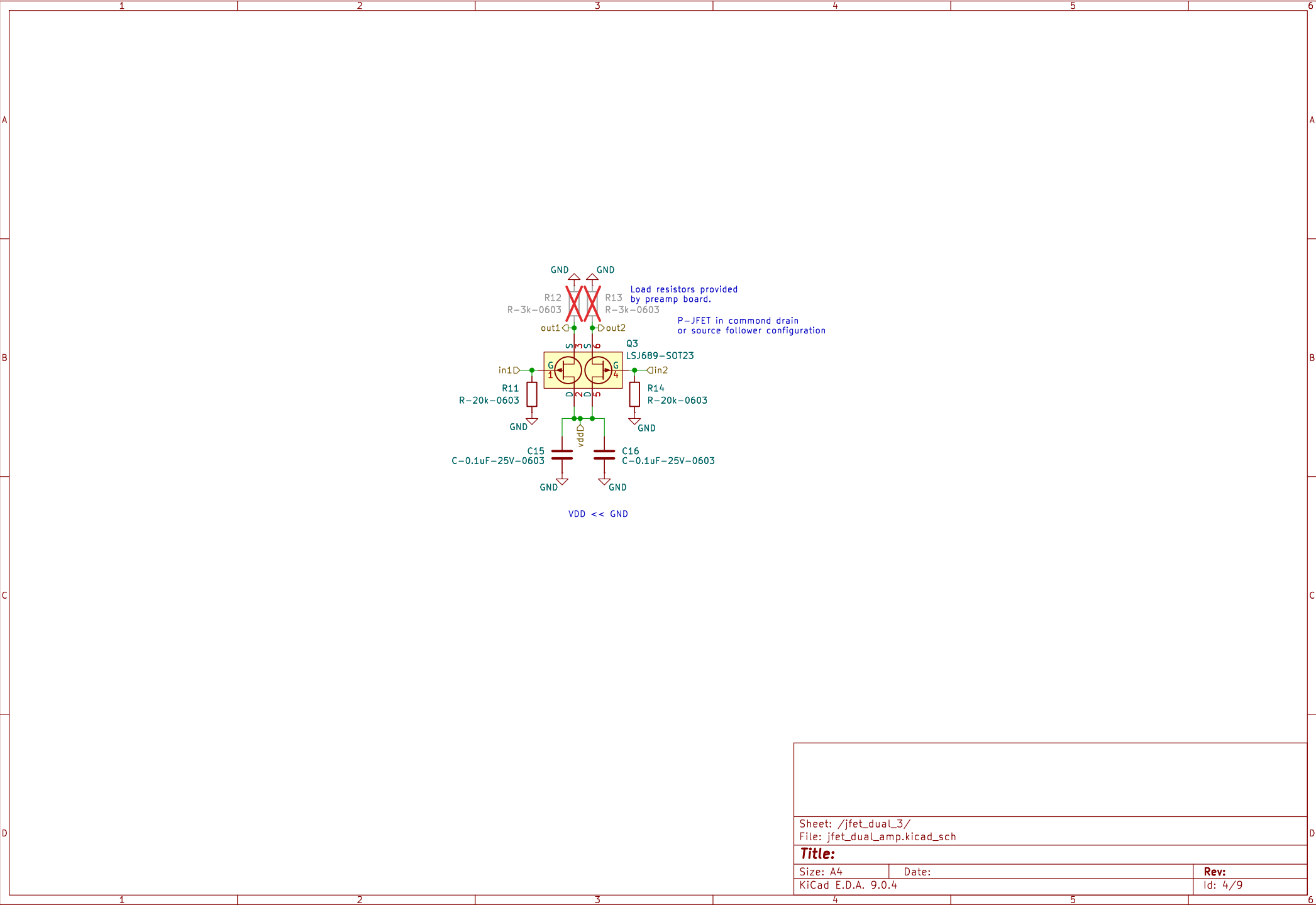
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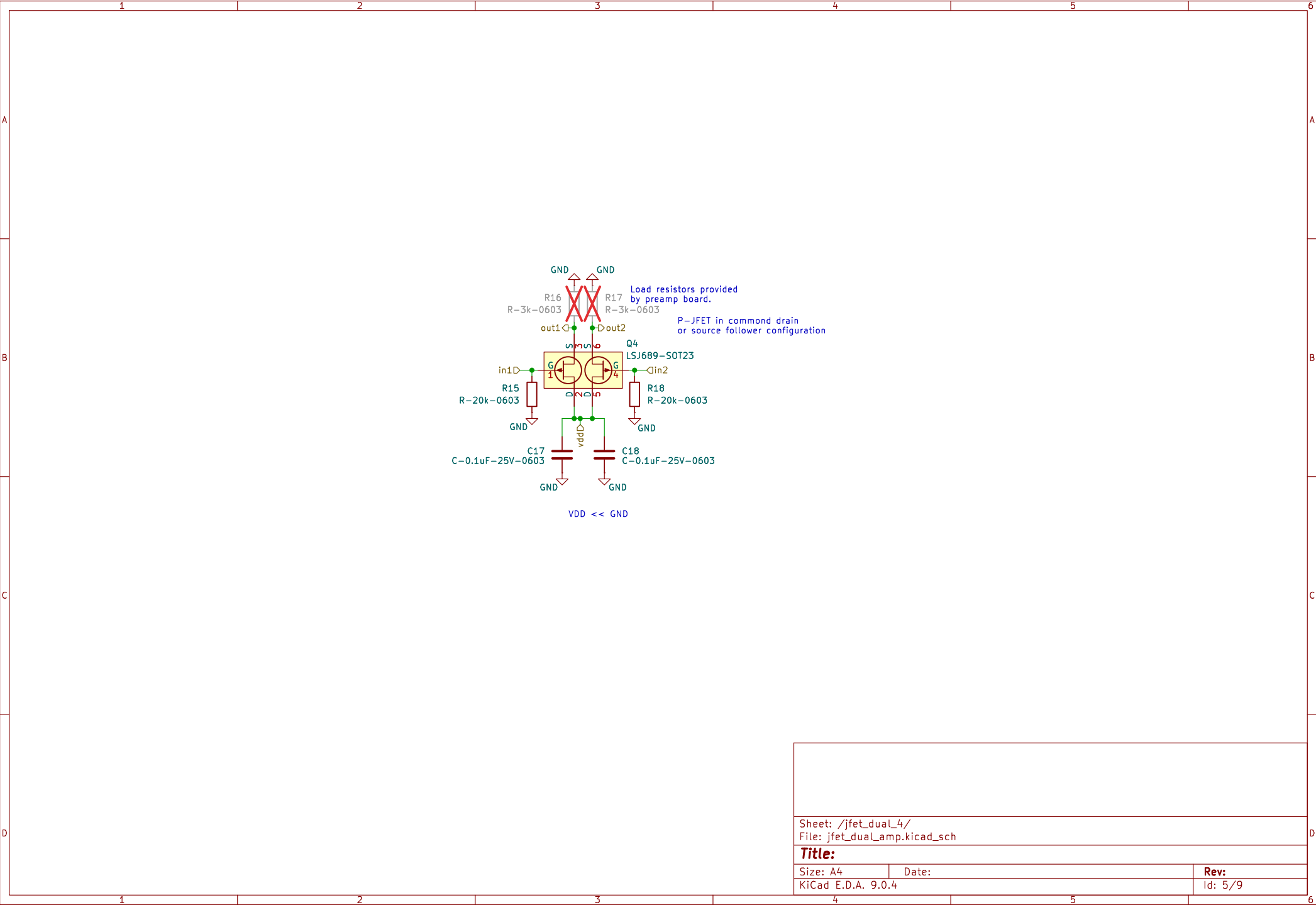
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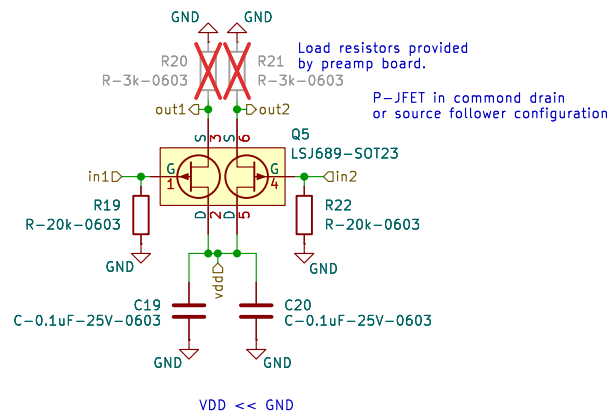
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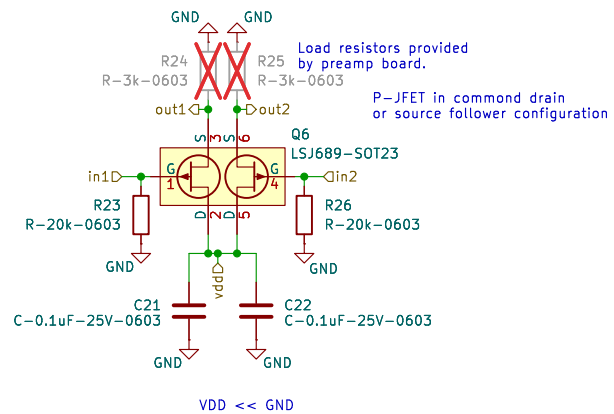
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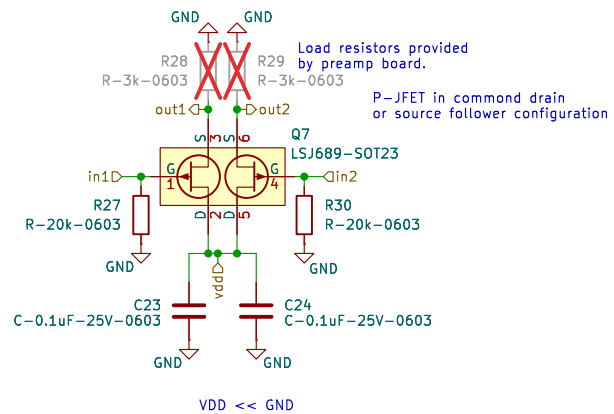
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Id: 7/9



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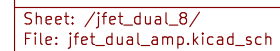
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KiCad E.D.A. 9.0.4	

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Id: 9/9