BRENDAN LONG

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https://github.com/bccbrendan

TECHNICAL STRENGTHS

Skills	Process evolution through CI/CD, mentoring, training, on-boarding documentation
Languages	C++, Python, Rust, C, C#
Technology	boost, asio, gsl, gtest, CMake, I3C, UART, SPI, JTAG, XML, flatbuffers, JSON
Tools	Git, Conan, Coverity, Simics, valgrind, CI/CD (Jenkins, GitHub Actions)

EXPERIENCE

Intel 2023 - Present
Staff Software Engineer *Austin, TX*

- Mentor and train engineers on multiple global teams through group training and pair programming.
- Curate team work backlog, matching tasks to developers' individual strengths
- Set up and manage automatic static code analysis for security, drove reduction of open issues by 90%
- DRA for resolving debug tool and silicon integration problems in 3 next-gen server products, and identifying prevention strategies
- 2023 Project awards for successful silicon power-on, for enabling debug tools in record time, and for executing silicon validation test plan 8 weeks ahead of schedule.

Intel May 2019 - 2023
Senior Software Developer *Austin, TX*

- Led team transition to Git through automated migration process, administration, and team trainings
- Gathered requirements and led development of debug tools, adapting to new architectures and debug protocols - multiple DRAs and two Project awards
- As product owner for pre-silicon debug tools, gathered requirements, planned features, and balanced work priorities of a team to deliver tools across the entire product roadmap.
- Led several multi-team sessions on Git and silicon debug technologies. Received 2021 SRA for on-boarding new overseas team.
- Ported debug tool software and custom embedded hardware to Raspberry Pi by for significant savings
- Developed and verified debug tools and supported record time power-on of discrete graphics product
- Delivered debug tools for next-gen server cpu with exceptional stability - Q1'2019 DRA.

Intel June 2012 - April 2019
Software Developer *Austin, TX*

- Designed and developed software libraries bridging silicon debug software to pre-silicon emulation models in every market segment. Received 3 DRAs and one Special Recognition Award.
- Created in-house software replacement for 3rd party probes, saving $5.75M - DRA and SRA.
- Developed key features of debug tools using Intel's Direct Connect Interrface technology
- Drove company-wide adoption of 3rd party debug tools by developing seamless compatibility software for existing use cases. Created and provided training for new tools. Received Q2'13 Transformation Award, Q3'13 DRA, Q4'14 DRA, and Q4'14 "Above and Beyond" SRA

EDUCATION

University of Texas, Austin
M.S. in Software Engineering; B.S. in Electrical and Computer Engineering