BRENDAN LONG

1525 Cat Mountain Trail ◇ Keller, Texas 76248
(512) · 299 · 2285 ◇ bccbrendan@gmail.com

EDUCATION

University of Texas, Austin *May 2012*
M.S. in Software Engineering
Overall GPA: 3.95

University of Texas, Austin *May 2010*
B.S. in Electrical and Computer Engineering
Member of Eta Kappa Nu
Overall GPA: 3.89

EXPERIENCE

Intel May 2019 - present
Senior Software Developer *Austin, TX*

- As product owner for pre-silicon debug tools, gathered requirements, planned features, and balanced work priorities in a group of 4 developers to deliver tools across the entire product roadmap.
- Mentored 3-4 engineers through weekly 1:1 tech talks, led several multi-team train sessions on git and intel debug technology. Received 2021 SRA for training new remote team members.
- Reduced debug tool costs by adapting tool suite software and hardware to run on Raspberry Pi - received 2020 Project Award.
- Developed and verified debug tools and supported record time successful poweron of discrete graphics product. Received 2020 DRA.
- Delivered debug tools for next-gen server cpu on-time and with exceptional stability for power-on. Received Q1'2019 DRA.

Intel June 2012 - April 2019
Software Developer *Austin, TX*

- Designed and developed software libraries bridging silicon debug software to pre-silicon emulation models. Used to verify new products in every market segment. Received 3 Division Recognition Awards and one Special Recognition Award.
- Created in-house software replacement for 3rd party hardware tools in 2018, saving an estimated total $5.75M; received one DRA and one SRA.
- Developed several key features of the software connecting debug tools to Intel's Direct Connect Interrface. Received a Q1'2016 DRA.
- Drove company-wide adoption of 3rd party debug tools by developing compatibility software for existing use cases. Developed and provided training for new tools. Received Q2'13 Transformation Award, Q3'13 DRA, Q4'14 DRA, and Q4'14 "Above and Beyond" SRA

Intel January 2010 - June 2012
Validation Engineer Intern *Austin, TX*

- Developed embedded HTTP server to provide remote debug access to silicon validation platforms. Received Q2/13 Excellence award for silicon power-on support.
- Developed Linux kernel module to provide PCI access to FPGA platform for hybrid simulation model.

Schlumberger Summer 2009, Summer 2010
Software Engineering Intern *Houstin, TX*

- Enhanced cable tension monitoring/prediction system for oil well devices. Implemented features requested by oilfield engineers and reduced risk of equipment loss.
- Created prototype document classification and search system to enable efficient search of unstructured data.

TECHNICAL STRENGTHS

Computer Languages	C/C++, Python, C#, Rust, Java
Protocols, Libraries, & APIs	JTAG, XML, JSON, gmock, gtest
Tools	git, conan, vim, CI/CD through TeamCity, GitHub, GitLab