TALKING ELECTRONICS COMPUTER
TEC-1G

Signature Edition

AUX

Probe

9v DC (Tip+)

Power
OFF
ON

SW1

L7805
REG1
Heatsink

1000U
Must be laid Horizontally

330R R1
R18 1K
IN4002 D0

C1
Ah
G.JMP

C2 100n
C3 100n

2K2 R15
R31
2K2

FTDI Module
GND
CTS
+5V
TX
RX
DTR
J5

4800-8-N-2

GPI/O

22nf C8

CONTRAST VR2

10k RN3

10k

JOYSTICK

4.7k RN2

Matrix Keyboard

#

2K Comparator
74HCT688
U4

CONFIG
DIP1
K P E

DC4 100n

10K R29
R30 10K

74HCT10
U11

74HCT138
U12

SysI/O

74HC02 U5
74HC00

1N4148 x2 D2
1N4148

DC5 100n

R11 10K

74HCT138
DC7 100n

1N4148 x4
D3
D4
D5
D6
R12 10K
O/I/O 01n

74HCT138

O/I/O Memy

ROM SELECT
A14 A11
A15
L H

PINS
ROM SIZE
28 24

6116
74HCT00
U14Protect

OTHER
PINS
EXP SIZE
28 24 27C256

DS1233
10n(10uf) C5

27C512
PIN 1 1N5817
U7
16K ROM

SD1

61256 62256
U8
32K RAM
DC3 100n

61256
EXTRA ROM/RAM
U9
16K EXPANSION

Z80Bus+ Vertical Female Socket (for TEC Deck)

Z80 CPU
U2
J1

KB / HALT
CLK
R33 0R

4Mhz
X1

100n DC1

4049
C4 100p
U1 Inverter

RN1
R3 10k

RA Female IDC Socket
TEC Expander
Z80Bus Female IDC Socket

74HCT245
U19 Matrix

100n DC2

74HCT373
U18 S.imp
100n DC8

74HCT273
U13 SysLa

SPEAKER
8Ω

10K R4
RN6

MM74C923
U15 The'923
DC6 100n
1u C7
C6 100n

10k x 4
R6
R7
R8
R9
R10
330R R13

HALT

SPKR
L2

10k x4 R2 2k2

SLOW / FAST
VR1 20K
SPEED
SW2

VRS1
20K
VOLUME

74HCT273
U17 Segments
DC9 100n

74HCT273
U16 Digits

Q9 Q10 Q11 Q12 Q13 Q14 Q15 Q16
BC557

10K R26
10K R27
10K R28
1K R16
R5 4N958

1K R25
1K R24
Q7 Q6 Q5 Q4 Q2 Q1
BC547

1K R21
1K R23
1K R22
1K R20
1K R19

Q3 BC547
330R R5
Q8
100R R17

RESET

FND560 x 6

DISCO
RKGB
LEDs
RKGB

D7 1N4148
R14 330R

STATUS
CAPS BANK 8 4 2 1 EXP PROT SHAD SPRR HALT
2 1
BAR1
RN4 330R

RN5 330R
RN7 330R
Fullsikt LED's
FullsIkt
BRGH7

AD C D E F
GO 8 9 A B
- 4 5 6 7
+ 0 1 2 3
Fn

PCB layout created by Mark Jelic
Modelled on the TEC-1 rev.D with DAT add-on
Originally designed by John Hardy & Ken Stone
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