

## 1 Overview

These are the release notes for the NXP Multicore Software Development Kit (MCSDK) version 2.9.0. This software package contains components for efficient work with multicore devices as well as for the multiprocessor communication.

## 2 What is new

- eRPC: Support win32 thread, GitHub PR #108.
- eRPC: Add mbed support for malloc() and free(), GitHub PR #92.
- eRPC: Update makefile.
- eRPC: Fixed warnings and error with using MessageLoggers, GitHub PR #127.
- eRPC: Extend error msg for python server service handle function, GitHub PR #132.
- eRPC: Update CMSIS UART transport layer to avoid busy loops in rtos environments, introduce semaphores.
- eRPC: Introduced pre and post callbacks for eRPC call, GitHub PR #131.
- eRPC: Introduced new USB CDC transport.
- eRPC: Introduced new Linux spidev-based transport.
- eRPC: SPI transport update to allow usage without handshaking GPIO.
- eRPC: Native \_WIN32 erpc serial transport and threading.
- eRPC: Arbitrator deadlock fix, TCP transport updated, TCP setup functions introduced, GitHub PR #121.
- eRPC: Update of matrix\_multiply.py example: Add --serial and --baud argument, GitHub PR #137.
- eRPC: Added formatting extension for VSC, GitHub PR #134.
- eRPC: Update of .clang-format, GitHub PR #140.
- eRPC: Update of erpc\_framed\_transport.cpp: return error if received message has zero length, GitHub PR #141.
- eRPC, erpcgen: Fixed error messages produced by -Wall -Wextra -Wshadow -pedantic-errors compiler flags, GitHub PR #136, #139.
- eRPC, erpcgen: Core re-formatted using Clang version 10.
- erpcgen: Enable deallocation in server shim code when callback/function pointer used as out parameter in IDL.
- erpcgen: Removed '\$' character from generated symbol name in '\_\$union' suffix, GitHub PR #103.
- erpcgen: Resolved mismatch between C++ and Python for callback index type, GitHub PR #111.
- erpcgen: Python generator improvements, GitHub PR #100, #118.
- erpcgen: Fixed error messages produced by -Wall -Wextra -Wshadow -pedantic-errors compiler flags, GitHub PR #136.
- erpcgen: Introduce ustring type for unsigned char and force cast to char\*, GitHub PR #125.

### Contents

1	Overview.....	1
2	What is new.....	1
3	Development tools.....	2
4	Release contents.....	2
5	Multicore SDK release overview	
	.....	3
6	Demo applications.....	3
7	Revision history.....	3



- RMPmsg-Lite: Introduced RL\_ALLOW\_CONSUMED\_BUFFERS\_NOTIFICATION config option to allow opposite side notification sending each time received buffers are consumed and put into the queue of available buffers.
- RMPmsg-Lite: Added environment layers for Threadx.
- Supported evaluation boards (multicore examples):
  - LPCXpresso54114
  - LPCXpresso55S69
  - FRDM-K32L3A6
  - MIMXRT1170-EVK
- Supported evaluation boards (multiprocessor examples):
  - FRDM-K22F
  - FRDM-K28FA
  - FRDM-K64F
  - FRDM-K66F
  - FRDM-KL27Z
  - FRDM-K32L2B

### 3 Development tools

The Multicore SDK (MCSDK) version 2.9.0 was compiled and tested with these development tools:

- IAR Embedded Workbench for Arm<sup>®</sup> version 8.50.6
- MDK-ARM Microcontroller Development Kit (Keil)<sup>®</sup> version 5.33
- Makefiles support with GCC revision 9-2020q2-update from Arm Embedded
- MCUXpresso IDE v11.3.0

### 4 Release contents

This table describes the release contents. Not all MCUXpresso SDK packages contain the whole set of these components.

**Table 1. Release contents**

Deliverable	Location
Multicore SDK location within the MCUXpresso SDK folder structure	<MCUXpressoSDK_install_dir>/middleware/multicore/...
Documentation	<MCUXpressoSDK_install_dir>/docs/multicore/...
Embedded Remote Procedure Call component	<MCSDK_dir>/erpc/...
Multicore Manager component	<MCSDK_dir>/mcmgr/...
RMPmsg-Lite	<MCSDK_dir>/rmpmsg_lite/...
Multicore demo applications	<MCUXpressoSDK_install_dir>/boards/ <board_name>/ multicore_examples/...
Multiprocessor demo applications	<MCUXpressoSDK_install_dir>/boards/ <board_name>/ multiprocessor_examples/...

## 5 Multicore SDK release overview

Together, the Multicore SDK (MCSDK) and the MCUXpresso SDK (SDK) form a framework for the development of software for NXP multicore devices. The MCSDK release consists of the following elementary software components for multicore:

- Embedded Remote Procedure Call (eRPC)
- Multicore Manager (MCMGR) - included just in SDK for multicore devices
- Remote Processor Messaging - Lite (RPMMsg-Lite) - included just in SDK for multicore devices

The MCSDK is also accompanied with documentation and several multicore and multiprocessor demo applications.

## 6 Demo applications

The multicore demo applications demonstrate the usage of the MCSDK software components on supported multicore development boards. The following multicore demo applications are located together with other MCUXpresso SDK examples in the `<MCUXpressoSDK_install_dir>/boards/<board_name>/multicore_examples...` subdirectories.

- erpc\_matrix\_multiply\_mu
- erpc\_matrix\_multiply\_mu\_rtos
- erpc\_matrix\_multiply\_rpmsg
- erpc\_matrix\_multiply\_rpmsg\_rtos
- erpc\_two\_way\_rpc\_rpmsg\_rtos
- freertos\_message\_buffers
- hello\_world
- low\_power
- multicore\_manager
- rpmsg\_lite\_pingpong
- rpmsg\_lite\_pingpong\_rtos
- rpmsg\_lite\_pingpong\_tzm

The eRPC multicore component can be leveraged for inter-processor communication and remote procedure calls between SoCs / development boards. The following multiprocessor demo applications are located together with other MCUXpresso SDK examples in the `<MCUXpressoSDK_install_dir>/boards/<board_name>/multiprocessor_examples...` subdirectories.

- erpc\_client\_matrix\_multiply\_spi
- erpc\_server\_matrix\_multiply\_spi
- erpc\_client\_matrix\_multiply\_uart
- erpc\_server\_matrix\_multiply\_uart
- erpc\_server\_dac\_adc
- erpc\_remote\_control

## 7 Revision history

This table summarizes revisions to this document.

**Table 2. Revision history**

Revision number	Date	Substantive changes
0	09/2015	Initial release
1	03/2016	Updated for the KSDK 2.0.0 and the MCSDK 1.1.0
2	08/2016	Updated for the MCSDK 2.0.0 and the LPCXpresso54114 support
3	09/2016	Updated for the MCSDK 2.1.0
4	03/2017	Updated for the MCSDK 2.2.0
5	06/2017	Updated for the MCSDK 2.2.1 and the LPCXpresso54102 support
7	11/2017	Updated for the MCSDK 2.3.0 and MCUXpresso SDK 2.3.0 release
8	05/2018	Updated for the MCSDK 2.4.0 and MCUXpresso SDK 2.4.0 release
9	12/2018	Updated for the MCSDK 2.5.0 and MCUXpresso SDK 2.5.0 release
10	06/2019	Updated for the MCSDK 2.6.0 and MCUXpresso SDK 2.6.0 release
11	12/2019	Updated for the MCSDK 2.7.0 and MCUXpresso SDK 2.7.0 release
12	07/2020	Updated for the MCSDK 2.8.0 and MCUXpresso SDK 2.8.0 release
13	11/2020	Updated for the MCSDK 2.9.0 and MCUXpresso SDK 2.9.0 release

## How To Reach Us

### Home Page:

[nxp.com](http://nxp.com)

### Web Support:

[nxp.com/support](http://nxp.com/support)

Information in this document is provided solely to enable system and software implementers to use NXP products. There are no express or implied copyright licenses granted hereunder to design or fabricate any integrated circuits based on the information in this document. NXP reserves the right to make changes without further notice to any products herein.

NXP makes no warranty, representation, or guarantee regarding the suitability of its products for any particular purpose, nor does NXP assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation consequential or incidental damages. "Typical" parameters that may be provided in NXP data sheets and/or specifications can and do vary in different applications, and actual performance may vary over time. All operating parameters, including "typicals," must be validated for each customer application by customer's technical experts. NXP does not convey any license under its patent rights nor the rights of others. NXP sells products pursuant to standard terms and conditions of sale, which can be found at the following address: [nxp.com/SalesTermsandConditions](http://nxp.com/SalesTermsandConditions).

Security — Customer understands that all NXP products may be subject to unidentified or documented vulnerabilities. Customer is responsible for the design and operation of its applications and products throughout their lifecycles to reduce the effect of these vulnerabilities on customer's applications and products. Customer's responsibility also extends to other open and/or proprietary technologies supported by NXP products for use in customer's applications. NXP accepts no liability for any vulnerability. Customer should regularly check security updates from NXP and follow up appropriately. Customer shall select products with security features that best meet rules, regulations, and standards of the intended application and make the ultimate design decisions regarding its products and is solely responsible for compliance with all legal, regulatory, and security related requirements concerning its products, regardless of any information or support that may be provided by NXP. NXP has a Product Security Incident Response Team (PSIRT) (reachable at [PSIRT@nxp.com](mailto:PSIRT@nxp.com)) that manages the investigation, reporting, and solution release to security vulnerabilities of NXP products.

NXP, the NXP logo, NXP SECURE CONNECTIONS FOR A SMARTER WORLD, COOLFLUX, EMBRACE, GREENCHIP, HITAG, ICODE, JCOP, LIFE, VIBES, MIFARE, MIFARE CLASSIC, MIFARE DESFire, MIFARE PLUS, MIFARE FLEX, MANTIS, MIFARE ULTRALIGHT, MIFARE4MOBILE, MIGLO, NTAG, ROADLINK, SMARTLX, SMARTMX, STARPLUG, TOPFET, TRENCHMOS, UCODE, Freescale, the Freescale logo, Altivec, CodeWarrior, ColdFire, ColdFire+, the Energy Efficient Solutions logo, Kinetis, Layerscape, MagniV, mobileGT, PEG, PowerQUICC, Processor Expert, QorIQ, QorIQ Qonverge, SafeAssure, the SafeAssure logo, StarCore, Symphony, VortiQa, Vybrid, Airfast, BeeKit, BeeStack, CoreNet, Flexis, MXC, Platform in a Package, QUICC Engine, Tower, TurboLink, EdgeScale, EdgeLock, eIQ, and Immersive3D are trademarks of NXP B.V. All other product or service names are the property of their respective owners. AMBA, Arm, Arm7, Arm7TDMI, Arm9, Arm11, Artisan, big.LITTLE, Cordio, CoreLink, CoreSight, Cortex, DesignStart, DynamIQ, Jazelle, Keil, Mali, Mbed, Mbed Enabled, NEON, POP, RealView, SecurCore, Socrates, Thumb, TrustZone, ULINK, ULINK2, ULINK-ME, ULINK-PLUS, ULINKpro, µVision, Versatile are trademarks or registered trademarks of Arm Limited (or its subsidiaries) in the US and/or elsewhere. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. Oracle and Java are registered trademarks of Oracle and/or its affiliates. The Power Architecture and Power.org word marks and the Power and Power.org logos and related marks are trademarks and service marks licensed by Power.org.

© NXP B.V. 2015-2020.

All rights reserved.

For more information, please visit: <http://www.nxp.com>

For sales office addresses, please send an email to: [salesaddresses@nxp.com](mailto:salesaddresses@nxp.com)

Date of release: 24 November 2020

Document identifier: MCSDKRN

