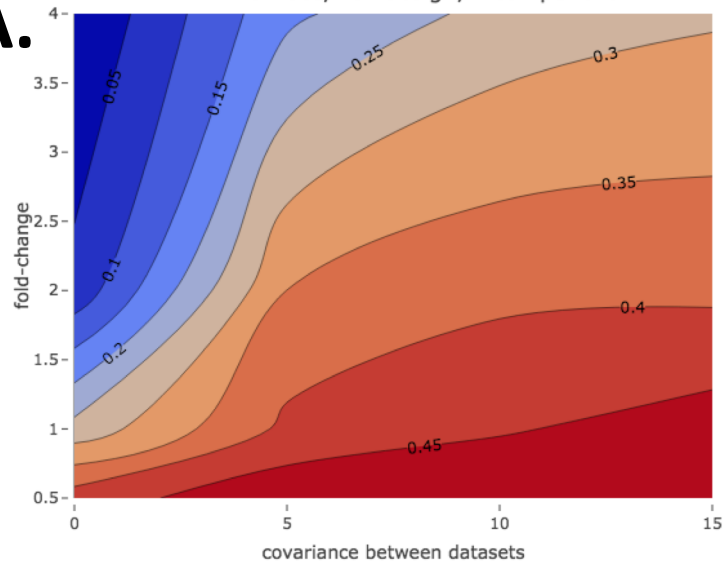
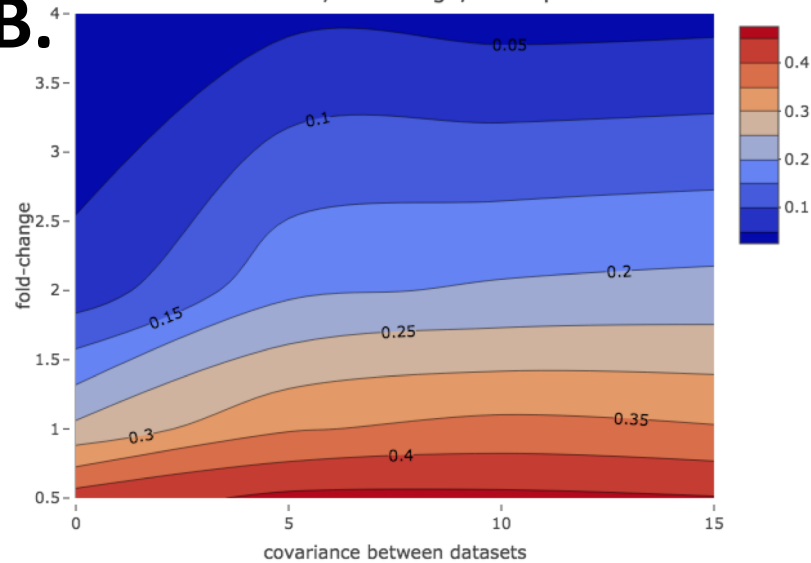


**A.**

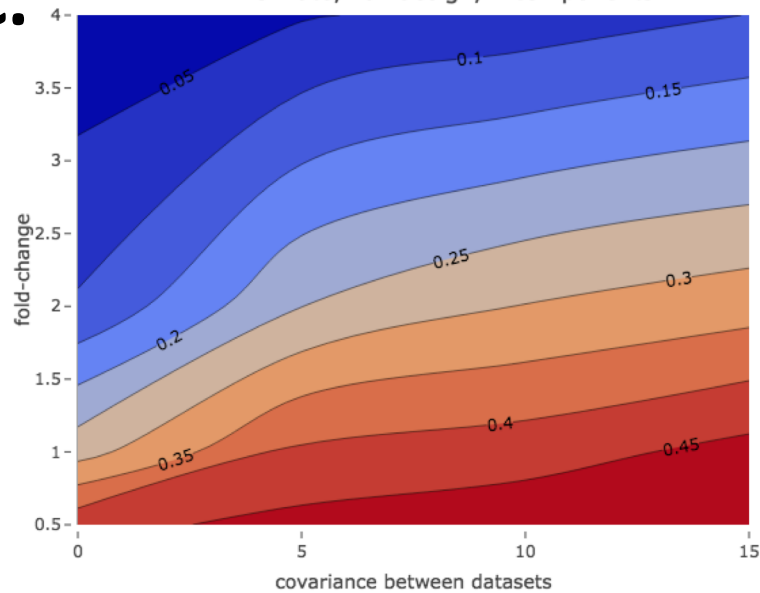
Error rate, Full design, 1 component

**B.**

Error rate, Null design, 1 component

**C.**

Error rate, Full design, 2 components

**D.**

Error rate, Null design, 2 components

