Divya Bojja

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PROFILE

ECE undergraduate skilled in programming (C,C++, Java, Python), VLSI and RTL design, MATLAB,DSP, embedded and IoT systems, and machine learning. Strong ability to build efficient hardware–software solutions with solid analytical and problem-solving skills.

EDUCATION

Vellore Institute of Technology (VIT)   *Chennai, India*

Bachelor of Technology in Electronics and Communication Engineering   2023– 2027(Expected)

- CGPA: 9.02/10
- Demonstrated strong academic performance through hands-on technical projects.
- Relevant coursework in [Relevant Subjects: DSA, OOP, ML, VLSI, Verilog, Digital Electronics, DSP, Embedded Systems]

SKILLS

Technical: C, C++, Java, Python, SQL, Data Structures & Algorithms, Object-Oriented Programming, Machine Learning, MATLAB, Verilog HDL, RTL Design, Digital Electronics, DSP, Embedded Systems, IoT Development, PCB Design, HTML, CSS
Soft Skills: Problem-Solving, Analytical Thinking, Attention to Detail, Team Collaboration, Adaptability, Communication Skills

Tools: Git, GitHub, VS Code, Sublime Text, MySQL, ModelSim, Xilinx Vivado, Cadence Virtuoso, MATLAB/Simulink, Arduino IDE, ESP8266, NI Circuit Design Suite (PCB Design), Ubuntu (SoC flow), Google Colab, Linux, PyCharm

PROJECTS

Comparative Analysis of 16-bit Ripple Carry Adder and Weinberger Adder (VLSI)   02/25 – 04/25

- Designed and simulated 16-bit RCA and Weinberger adders using **Cadence Virtuoso (180 nm)**.
- Analyzed delay and power, showing faster performance of the Weinberger architecture.
- Verified transistor-level functionality through schematic design and waveform analysis.

Accident Detection and Emergency Alert System (ML +Python + MATLAB)   09/25 – 11/25

- Trained a **ResNet50** accident detection model in **Google Colab (Python)** achieving ~94.9% accuracy.
- Used **MATLAB** to simulate adaptive modulation for reliable emergency alert transmission.
- Implemented automated WhatsApp alerts using **PyWhatKit** with nearest-hospital routing.

EXPERIENCE

Surya's MIB Enterprises   Chennai, India

PCB Design Intern   Aug 2025 – Sep 2025

- Designed and simulated PCB circuits using **NI Circuit Design Suite**, including schematic creation and layout routing.
- Performed DRC checks, debugging, and optimization for signal integrity and manufacturability.
- Supported hardware design tasks by refining schematics and documenting design workflows.

CERTIFICATIONS

- PCB Design Internship Completion – Surya's MIB Enterprises (2025) *(link)*
- C Language Training and Course Completion – IIT Bombay (Spoken Tutorials, 2024) *(link)* , Universal Computer Education Center *(link)*
- C++ Language Training – IIT Bombay (Spoken Tutorials, 2024) *(link)*
- Java (Basic) – HackerRank (2024) *(link)*
- HTML, CSS Front-End Development Bootcamp – VIT Chennai (2024) *(link)*