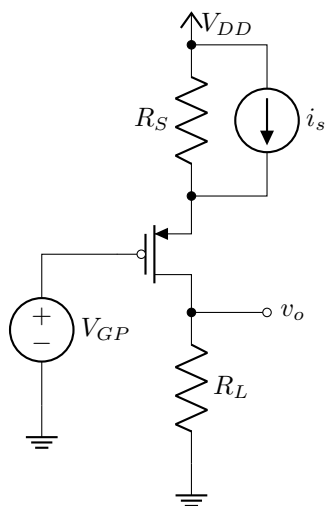


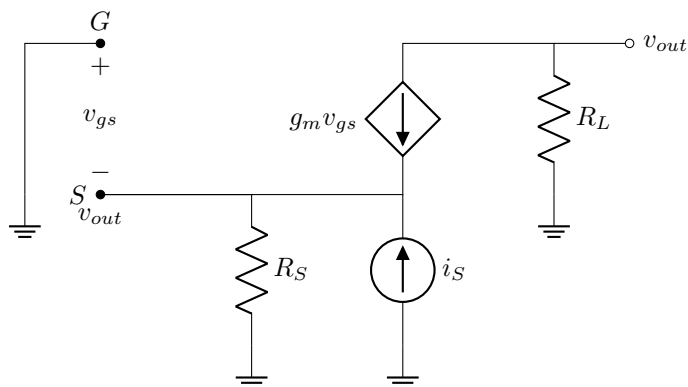
EE 105 HW 10

1



(a) This is a *common-gate* amplifier.

(b) The small-signal model is



The node equations governing the circuit are

$$g_m(-v_S) + i_s = \frac{v_S}{R_S} \quad (1)$$

$$\frac{v_{out}}{R_L} + g_m(-v_S) = 0 \quad (2)$$

$$\Rightarrow v_S = \frac{i_S}{\frac{1}{R_S} + g_m} \quad (3)$$

$$\Rightarrow \frac{v_{out}}{i_S} = \frac{g_m R_L}{\frac{1}{R_S} + g_m} \quad (4)$$

(c)

$$V_{GP} = 0 \text{ V} \quad (5)$$

$$R_S = 2 \text{ k}\Omega \quad (6)$$

$$V_{DD} = 12 \text{ V} \quad (7)$$

$$k_p = 1 \text{ mA V}^{-1} \quad (8)$$

$$V_{TP} = -3 \text{ V} \quad (9)$$

To maximize the small-signal gain, we would want R_L to be as large as possible while remaining in the saturation region. In order to remain in the saturation region, we want

$$V_{SD} \geq V_{DD} - I_{DS}R_S - |V_{TP}| \quad (10)$$

$$\cancel{V_{DD} - I_{DS}R_S} - I_{DS}R_L \geq \cancel{V_{DD} - I_{DS}R_S} - |V_{TP}| \quad (11)$$

$$\implies R_L \leq \frac{|V_{TP}|}{I_{DS}} \quad (12)$$

The current given the MOSFET parameters is

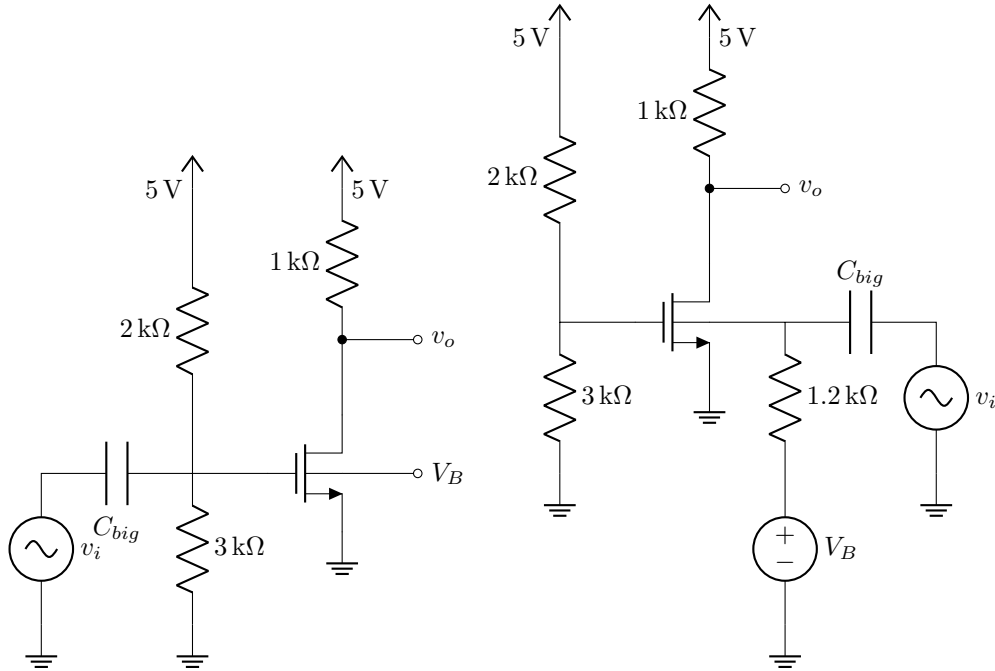
$$I_{DS} = \frac{k_p}{2} (v_S - |V_{TP}|)^2 \quad (13)$$

$$= \frac{k_p}{2} (v_{DD} - I_{DS}R_S - |V_{TP}|)^2 \quad (14)$$

$$\implies I_{DS} = 3.23 \text{ mA}, 6.27 \text{ mA} \quad (15)$$

Picking the smaller value of I_{DS} , we get $R_L = 928.8 \Omega$.

2 Impact of Body Effect on Amplifiers



$$V_{TN} = 1 \text{ V} \quad (16)$$

$$k_n = 2 \text{ mA V}^{-1} \quad (17)$$

$$\lambda = 0 \text{ V}^{-1} \quad (18)$$

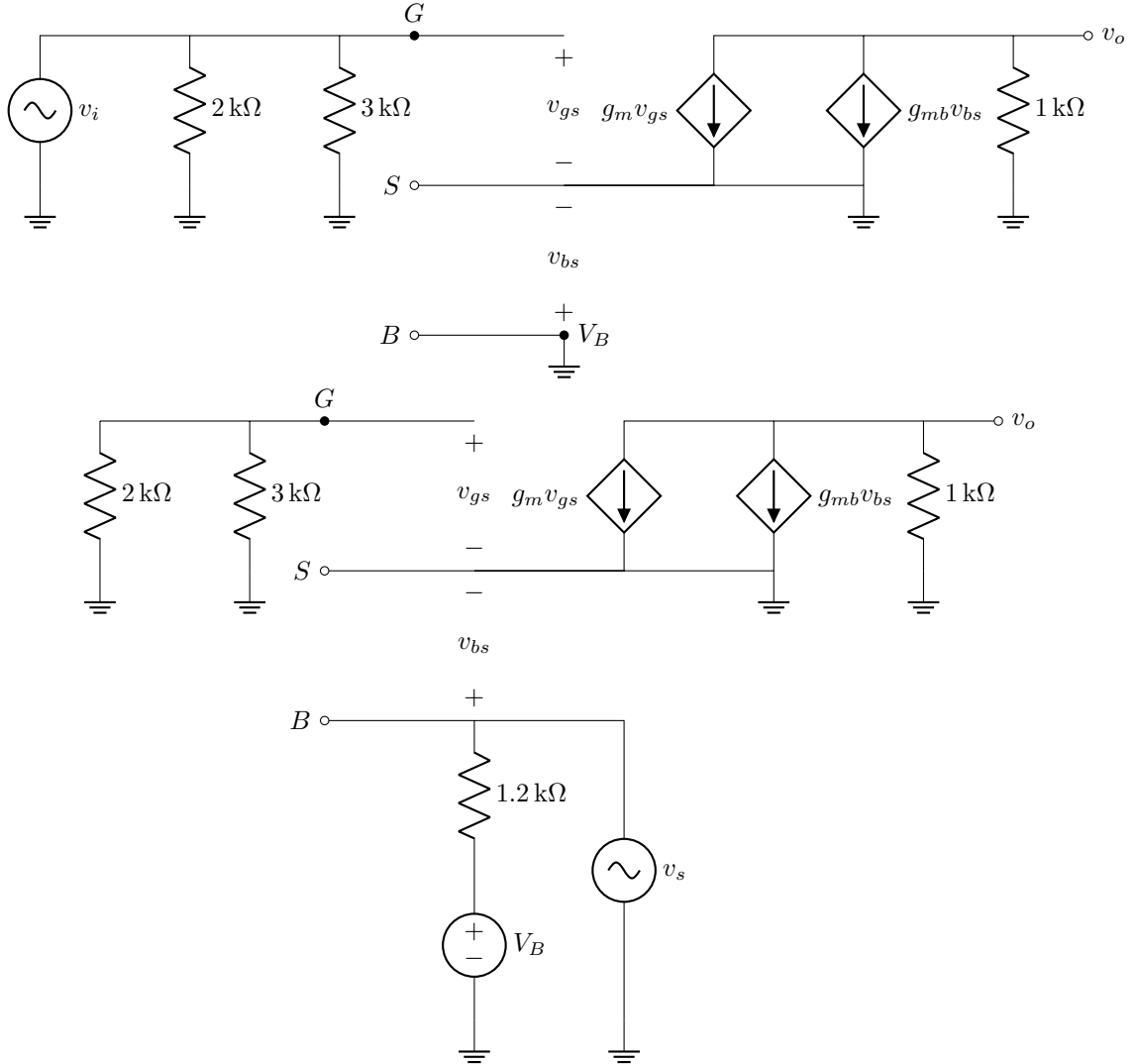
$$\gamma = 0.5 \text{ V}^{1/2} \quad (19)$$

$$\phi_p = -200 \text{ mV} \quad (20)$$

$$V_T = V_{T0} + \gamma \left(\sqrt{V_{SB} - 2\phi_p} - \sqrt{-2\phi_p} \right) \quad (21)$$

(a) Amplifier A is a *common-source* amplifier, and amplifier B is a *common-gate* amplifier.

(b) The small-signal model for both amplifiers when we reduce C_{big} to a short-circuit is



Analyzing amplifier A,

$$g_m v_i + \frac{v_o}{1 \text{ k}\Omega} = 0 \implies \frac{v_o}{v_i} = -g_m (1 \text{ k}\Omega) \quad (22)$$

Calculating g_m ,

$$g_m = k_n \left(v_{gs} - \left(V_{T0} + \gamma \left(\sqrt{V_{SB} - 2\phi_p} - \sqrt{-2\phi_p} \right) \right) \right) = k_n (V_g - 1.27 \text{ V}) = 3.45 \text{ mS} \quad (23)$$

This means that our gain is -3.45 .

For amplifier B,

$$g_{mb}v_s + \frac{v_o}{1 \text{ k}\Omega} = 0 \implies \frac{v_o}{v_i} = -g_{mb}(1 \text{ k}\Omega) \quad (24)$$

Calculating g_{mb} ,

$$g_{mb} = \frac{\gamma g_m}{2\sqrt{-v_{bs} - 2\phi_p}} \quad (25)$$

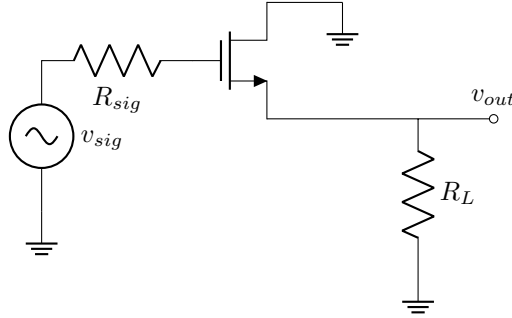
$$g_m = 3.45 \text{ mS} \quad (26)$$

$$\implies g_{mb} = 7.29 \times 10^{-4} \text{ S} \quad (27)$$

This means that our gain is -0.729 .

- (c) In amplifier A, an increased V_B would decrease V_{SB} , which would decrease v_T , which would increase g_m , which would *increase* our absolute gain. In amplifier B, an increased V_B would decrease V_{SB} , which would increase g_{mb} , which would *increase* our absolute gain.

3



$$\mu_n C_{ox} = 0.5 \text{ mA V}^{-2} \quad (28)$$

$$V_{GS} - V_T = 0.2 \text{ V} \quad (29)$$

$$(30)$$

The output resistance of a source follower is

$$R_{out} \approx \frac{1}{g_m} \implies g_m = \mu_n C_{ox} \frac{W}{L} (V_{GS} - V_T) = 3.33 \text{ mS} \quad (31)$$

$$\implies \frac{W}{L} = \frac{3.33 \text{ mS}}{\mu_n C_{ox} (V_{GS} - V_T)} = 33.33 \quad (32)$$

The DC bias current is

$$I_D = \frac{\mu_n C_{ox}}{2} \frac{W}{L} (V_{GS} - V_T)^2 = 33.33 \text{ }\mu\text{A} \quad (33)$$

The range of gains based on R_L is

$$\frac{v_{out}}{v_{sig}} = \frac{g_m}{\frac{1}{R_L} + g_m} \bigg|_{1 \text{ k}\Omega}^{10 \text{ k}\Omega} = [0.77, 0.97] \quad (34)$$

4

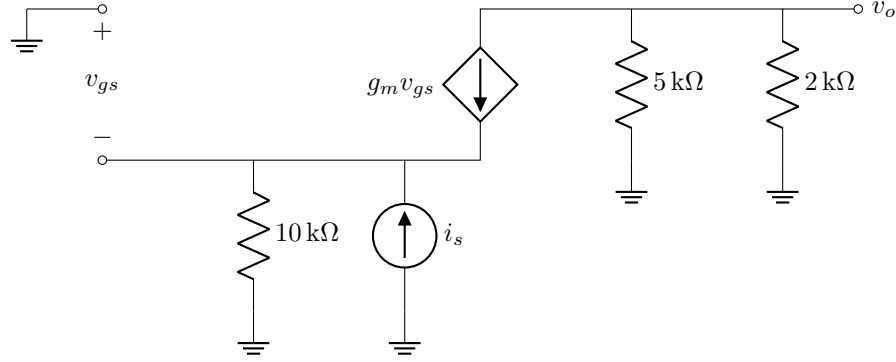
$$g_m = 10 \text{ mA V}^{-1} \quad (35)$$

(a) For the source follower, the gain and output resistance are

$$\frac{v_o}{v_i} = \frac{g_m}{\frac{1}{R_L} + g_m} = 0.99 \quad (36)$$

$$R_o = \frac{1}{g_m} = 100 \Omega \quad (37)$$

(b) Analyzing the small-signal model of a common-gate amplifier,



We can find the gain as

$$\frac{v_o}{5 \text{ k}\Omega} + \frac{v_o}{2 \text{ k}\Omega} + g_m(-V_s) = 0 \quad (38)$$

$$\frac{V_s}{10 \text{ k}\Omega} = i_s + g_m(-V_s) \implies V_s = \frac{i_s}{\frac{1}{10 \text{ k}\Omega} + g_m} \quad (39)$$

$$\implies \frac{v_o}{i_s} = \left(\frac{1}{10 \text{ k}\Omega} + g_m \right) \frac{g_m(10 \text{ k}\Omega)}{\frac{1}{5 \text{ k}\Omega} + \frac{1}{2 \text{ k}\Omega}} = 1.44 \text{ k}\Omega \quad (40)$$

For the common-gate amplifier, the input resistance is $R_i = \frac{1}{g_m} = 100 \Omega$.