

	25	26	27	28	29	30	31	32			
A	HT0_RX_CADp03	HT0_RX_CADn03	HT0_RX_CADp04	HT0_RX_CADn04	HT0_RX_CADp06	HT0_RX_CADn06	HT0_RX_CTLp0	HT0_RX_CTLn0			
B	HT0_RX_CADn02	VDDE_1V2	HT0_RX_CLKn0	VDDE_1V2	HT0_RX_CADn05	VDDE_1V2	HT0_RX_CADn07	VDDE_1V2			
C	HT0_RX_CADp02	VDDE_1V2	HT0_RX_CLKp0	VDDE_1V2	HT0_RX_CADp05	VDDE_1V2	HT0_RX_CADp07	VDDE_1V2			
D	GNDE	HT0_RX_CADp11	GNDE	HT0_RX_CADp12	GNDE	HT0_RX_CADp14	GNDE	HT0_RX_CTLp1			
E	GNDE	HT0_RX_CADn11	GNDE	HT0_RX_CADn12	GNDE	HT0_RX_CADn14	GNDE	HT0_RX_CTLn1			
F	HT0_RX_CADp10	HT0_RX_CADn10	HT0_RX_CLKn1	HT0_RX_CLKp1	HT0_RX_CADp13	HT0_RX_CADn13	HT0_RX_CADp15	HT0_RX_CADn15			
G	GND	GND	VDD	VDD	GND	GND	VDD	VDD			
H											
J											
K											
L											
M											
N									GND	VDD	GND
P									VDD	GND	VDD
R									GND	VDD_NO	GND
T									VDD_NO	GND	VDD_NO
U									GND	VDD_NO	GND
V									VDD_NO	GND	VDD_NO
W									GND	VDD_NO	GND
Y									VDD_NO	GND	VDD_NO
AA									GND	VDD_NO	GND
AB									VDD	GND	VDD
AC	GND	VDD	GND								
AD	VDD	GND	VDD								
AE	GND	VDD	GND								
AF	VDD	GND	VDD								
AG	GND	VDD	GND								
AH											
AJ											
AK											
AL											
AM											
AN	GNDE	GNDE	VDDE_VREF	VDDE_VREF	GND	VDD	MCO_DDR_DQ52	MCO_DDR_DQ46			
AP	PCI_REQn6	PCI_IDSEL	MCO_DDR_DQ63	MCO_DDR_DQ58	MCO_DDR_DQ54	MCO_DDR_DQM6	MCO_DDR_DQ48	MCO_DDR_DQ42			
AR	PCI_AD05	PCI_AD03	MCO_DDR_DQ59	MCO_DDR_DQ56	MCO_DDR_DQ50	VDDE_DDR	MCO_DDR_DQ53	MCO_DDR_DQ47			
AT	PCI_AD04	VDDE_1V8	GNDE	VDDE_DDR	MCO_DDR_DQ55	MCO_DDR_DQSn6	MCO_DDR_DQ49	VDDE_DDR			
AU	PCI_AD06	PCI_AD00	MCO_DDR_DQSn7	MCO_DDR_DQ61	GNDE	MCO_DDR_DQSp6	GNDE	MCO_DDR_DQSn5			
AV	DDR_PLL_GND	PCI_AD02	MCO_DDR_DQSp7	MCO_DDR_DQ57	MCO_DDR_DQ51	MCO_DDR_CLKp5	MCO_DDR_CLKn4	MCO_DDR_DQSp5			
AW	DDR_PLL_AVDD	PCI_AD01	MCO_DDR_DQ62	MCO_DDR_DQM7	MCO_DDR_DQ60	MCO_DDR_CLKn5	MCO_DDR_CLKp4	MCO_DDR_DQ43			
	25	26	27	28	29	30	31	32			