ORP Monitor Quick Reference

Command	Parameters	Description
help		shows help
dm	<pre><start addr=""> [<end addr="">]</end></start></pre>	display 32-bit memory location(s)
pm	<addr> [<stop_addr>]</stop_addr></addr>	patch 32-bit memory location(s)
	<pre><value></value></pre>	
сору	[<dst_addr> [<src_addr< td=""><td>copy memory</td></src_addr<></dst_addr>	copy memory
	[<length>]]]</length>	
mfspr	<pre><spr_addr></spr_addr></pre>	show SPR
mtspr	<pre><spr_addr> <value></value></spr_addr></pre>	set SPR
crc	[<src_addr> [<length></length></src_addr>	calculates a 32-bit CRC on specified memory
	[<init_crc>]]]</init_crc>	region
tftp	[<file> [<srv_ip></srv_ip></file>	TFTP download
•	[<src_addr>]]]</src_addr>	
ic_enable		enable instruction cache
ic_disable		disable instruction cache
dc_enable		enable data cache
dc_disable		disable data cache
ram_test	<pre><start_addr> <stop_addr></stop_addr></start_addr></pre>	run a simple RAM test
	[<test_no>]</test_no>	
dhry	[<num_runs>]</num_runs>	run dhrystone
globals		show globals and their current values
src_addr	<pre><value></value></pre>	sets global parameter source address
dst_addr	<pre><value></value></pre>	sets global parameter destination address
start_addr	<value></value>	sets global parameter start address
length	<value></value>	sets global parameter length
erase_method	<pre><value> <value> <value></value></value></value></pre>	sets flash erase method global parameter
erase_method	<pre><value <="" <value="" pre=""></value></pre>	(0=do not erase, 1=fully, 2=as needed)
set_dest_addr	<addrhi> <addrmid></addrmid></addrhi>	set destination address global parameter
set_dest_addi	<addrin> <addrina <<="" td=""><td>set destination address global parameter</td></addrina></addrin>	set destination address global parameter
in	<pre><value> <value> <value></value></value></value></pre>	sets ip address global parameter
ip erv in	<pre><value> <value> <value></value></value></value></pre>	sets server ip address global parameter
srv_ip eth_init		init ethernet
show_txbd	[<start bd="">] [<max>]</max></start>	show Tx buffer desc
show_rxbd	[<start bd="">] [<max>]</max></start>	show Rx buffer desc
send_packet	<pre><length> [<start data="">]</start></length></pre>	create and send packet(s)
send_packet	[<num_of_packets>]</num_of_packets>	create and send packet(s)
init_txbd_pool	<max></max>	initialize Tx buffer descriptors
init_rxbd_pool	<max></max>	initialize Rx buffer descriptors
show_phy_reg	[<start_addr>] [<end addr="">]</end></start_addr>	show PHY registers
		9
set_phy_reg	<addr> <value></value></addr>	set PHY register
show_mac_regs		show all MAC registers enable ethernet interrupt
eth_int_enable	[<=h]	
show_rx_buffs	[<show_all>]</show_all>	show receive buffers (optional argument will also show empty buffers)
ahan tu buffa		- · · · · · · · · · · · · · · · · · · ·
show_tx_buffs		show transmit buffers
crt_enable		enables CRT
crt_disable		disables CRT
crt_test		enables CRT and displays some test patterns
camera_enable		enables camera
camera_disable		disables camera