

G. VEENA VYSHNAVI
B. TECH(EEE)



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CAREER OBJECTIVE

To be a sincere and dedicated employee and would enhance myself with the new technologies and will do research on that in a way which would be helpful for the upliftment of the company and also my career.

EDUCATION

Degree/ Examination	Year of Passing	School/Institute	Board/University	Percentage /Grade
B.Tech (EEE)	2021	Madanapalle Institute of Technology and Science, Madanapalle	JNTU Anantapur	7.81
Intermediate	2017	Sri Chaitanya Mahila Junior Kalasala, Vijayawada.	Board of Intermediate Education, AP	77
SSC	2015	Sri Chaitanya H.S, Hindupur.	Board of Secondary Education, AP	8.5

PROJECT

Title: Design and Implementation of Phase Sequence Protection System for Three Phase Electric Load

Role: Team Leader

Team Size: 4 members

Abstract - Our project allows to check the three-phase supply sequence. Knowing the phase sequence of three phase motors is quite an important thing. Considering if the motor uses a pumping action, even a single-phase reversal that results in the wrong sequence may lead the motor to run in the wrong the direction. Well, it may also lead to fail permanently due to dry run of the motor.

CERTIFICATION

- Completed 8-week online course" Introduction to Research" from IIT Madras,2020.
- Completed 12-week online course "Power System Analysis" from IIT Delhi, 2019.
- Completed 12-week online course "Microprocessors and Microcontrollers" from IIT Kharagpur, 2018.

AREAS OF INTEREST

- Control Systems.
- Digital Design.

SOFTWARE SKILL SET

- Languages : C
- Packages : M S Office.
- Operating systems : Windows 10, Window 7, Ubuntu.

ACADEMIC ACHIEVEMENTS & CO-CURRICULAR ACTIVITIES

- Presentation on BEL Company at MITS, 2020.
- As a part of Group Discussion on DAY ZERO, 2020.
- Coordinator for the seminar on World Science Day For Peace And Development organized at MITS, 2019.
- Paper Presentation on solar power generation in a National Level Technical Symposium at MITS, 2018.
- Student volunteer for PHOTON 2K18 at MITS, 2018.
- Attended a workshop on super capacitor at BITS Pilani Hyderabad, 2018.

EXTRA CURRICULAR ACTIVITIES



- Volunteer for NSS.

PERSONAL DETAILS

- Father's Name : Mr G. Govinda Reddy
- Date of Birth : 24 October 1999
- Linguistic Proficiency : Telugu, English, Kannada, Hindi.
- Address : #7 Ambica Green Villas Nimkampalli Road, Hindupur, Anantapur, Andhra Pradesh-515201.

DECLARATION

I hereby declare that the information furnished above is true to best of my knowledge.

Date:03-08-2021

Signature:

G.Veena Vyshnavi

