

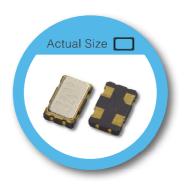
OV Type 5.0 x 3.2 mm SMD Crystal Oscillator

FEATURE

- Typical 5.0 x 3.2 x 1.2 mm ceramic SMD package.
- Tight symmetry (45 to 55%) available.
- Realize the standby function with Tri-State

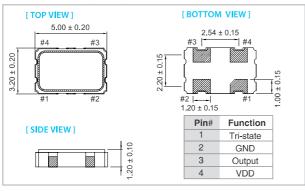
TYPICAL APPLICATION

- GPS, Mobile Phone
- WLAN, Wireless, Fiber/10Gbit Ethernet
- Notebook, PDA, DSC

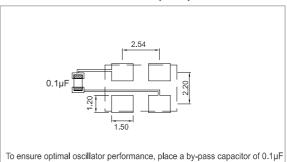


RoHS Compliant

DIMENSION (mm)



SOLDER PAD LAYOUT (mm)



To ensure optimal oscillator performance, place a by-pass capacitor of 0.1µf as close to the part as possible between Vdd and GND pads.

ELECTRICAL SPECIFICATION

Parameter	3.3V		2.5V		1.8V		unit
- didiiotoi	Min.	Max.	Min.	Max.	Min.	Max.	umi
Supply Voltage Variation(VDD)	VDD-10%	V DD+10%	V DD-10%	VDD+10%	VDD-10%	VDD+10%	V
Frequency Range	0.0137	160	0.0137	160	0.0137	135	MHz
Supply Frequency		2.048, 2	5, 26, 27,	50, 66.667	, 100, 125		MHz
Supply Current							
13.7 kHz ≦Fo ≦ 93 kHz	_	1	_	1	_	1	
0.3125 MHz ≦ Fo < 50 MHz (A1)	_	10	_	8	_	7	
40 MHz≦ Fo < 75 MHz	_	20	_	18	_	15	mA
75 MHz ≦ Fo < 135 MHz	_	35	_	30	_	25	
135 MHz ≦ Fo	_	45	_	40	_	_	
Output Level (CMOS) Output High (Logic "1")	2.97	_	2.25	_	1.62	_	V
Output Low (Logic "0")	_	0.33	_	0.25	_	0.18	V
Transition Time:Rise/Fall Time							
13.7 kHz ≦ Fo ≦ 93 kHz	_	50	_	50	_	50	
0.3125 MHz≦ Fo < 100 MHz	_	5	_	5	_	5	nSec
100 MHz≦ Fo	_	3	_	3	_	3	
Start Time	_	5	_	5	_	5	mSec
Output Drive Capability (CL)	_	15	_	15	_	15	pF
Tri-State (Input to Pin 1)							
Enable (High voltage or floating)	2.31	_	1.75	_	1.26	_	V
Disable (Low voltage or GND)	_	0.99	_	0.75	_	0.54	V
Period Jitter(Pk-Pk)	_	40	_	40	_	40	pSec
RMS Phase Jitter (Integrated 12 kHz~20 MHz)	_	1		1	_	1	pSec
Standby Current	_	10	_	10	_	10	μΑ
Aging (@ 25°C 1st year)	_	±3	_	±3	_	±3	ppm
Storage Temp, Range	-55	125	-55	125	-55	125	°C

Standard frequencies are frequencies which the crystal has been designed and does not imply a stock position.

FREQ. STABILITY vs. TEMP. RANGE

Temp. (°C)	±20	±25	±50	
-10 ~ +60	0	0	0	
-20 ~ +70	\triangle	0	0	
-40 ~ +85	\triangle	0	0	
-40 ~ +125	×	×]

^{*} \bigcirc : Available \triangle :Conditional X: Not available

⁺ Transition times are measured between 10% and 90% of VDD, with an output load of 15pF.

 $^{^*}$ Inclusive of calibration @ 25 °C, operating temperature range, input voltage variation, load variation, aging (1st year), shock, and vibration