

## Instructions after succeeding in booting Linux on COPPER CPU

Jun. 4, 2014      S. Yamada

### 1, How to install COPPER driver on COPPER CPU

#### 1-1, Get driver's source code

~~svn co <https://belle2.cc.kek.jp/svn/trunk/software/daq/copper/driver/cprdist-0.1.0>~~  
~~svn co <https://belle2.cc.kek.jp/svn/trunk/software/daq/copper/driver/cprdist-0.1.5>~~  
~~svn co <https://belle2.cc.kek.jp/svn/trunk/software/daq/copper/driver/cprdist-0.1.6>~~  
~~or~~

Download the latest version from

<https://belle2.cc.kek.jp/~twiki/bin/view/Detector/DAQ/COPPER>

(Updated on Nov.8, 2013)

#### 1-2, How to compile and install COPPER drivers

To avoid an error message below during installation of modules, please check notices,

**"insmod: error inserting 'cprdist-\*\*\*/drv/copper.ko': -1 Invalid module format"**

NOTICE : the version of compiler(/usr/bin/gcc) should be  
same as the one used for kernel compilation or PXE server's gcc version.  
You can check the version by   % /usr/bin/gcc --dumpversion

NOTICE : If you try to compile drivers on a PXE server and COPPER\_CPU's linux kernel  
is different from  
the one used on PXE server, please modify KERNEL\_VERSION in drv/Makefile and  
cprfin\_fngeneric/Makefile.

The “invalid module format” occurs when the vermagic of copper.ko is different from that  
of the running kernel on cpr\*\*\*. You can check the vermagic of copper.ko as follows;

[user @cpr\*\*\*: daq/copper/driver/cprdist-\*.\*/drv]\$ modinfo copper.ko

*filename:           copper.ko*

*license:            GPL*

*srcversion:         C4ED63ABE5BABCCCFF7C688*

*depends:*

*vermagic:           2.6.18 SMP mod\_unload 686 REGPARM 4KSTACKS gcc-4.1*

```
% make clean
% make
% cd cprfin_fngeneric/
% make
% cd ..
% /bin/su
# ./initd_copper start
# ./initd_cprfin_fngeneric start
```

check if drivers are successfully installed.

```
% /sbin/lsmmod
```

Module	Size	Used by
cprfin_fngeneric	9808	0
copper	30208	0

### 1-3, How to install COPPER drivers modules automatically

1) correct path of module and script

e.g.)

```
insmod ./drv/copper.ko -> Need to modify
```

```
action $"Making nodes: " ./src/mkdevs.sh -> Need to modify
```

2) copy initd\_copper and initd\_cprfin\_fngeneri files

```
[ on COPPER CPU] # cp initd_copper /etc/init.d/copper
```

```
[ on COPPER CPU] # cp initd_cprfin_fngeneric /etc/init.d/cprfin_fngeneric
```

If you boot many COPPER CPUs w/ one boot server, note that those files may be common to all COPPER boards,

unless you specify them in /tftpboot/copper/snapshot/files.

3) use chkconfig to register

```
[ on COPPER CPU] # chkconfig --add copper
```

```
[ on COPPER CPU] # chkconfig --add cprfin_fngeneric
```

4)

Maybe it will work.

It is better to place drivers under `/lib/modules`.

## 2, Configure CPLD before using an unused HSLB

### 2-1, download a CPLD configuration directory

% svn co <https://belle2.cc.kek.jp/svn/trunk/software/daq/copper/HSLB> HSLB

The CPLD configuration file is HSLB/130621\_HSLB\_cpld\_readback.jed .

### 2-2, Configure CPLD

1) Attach a HSLB on a COPPER, set the COPPER in a KEK-VME crate, and turn on the power of the crate.

2) Download the CPLD configuration file to the CPLD on the HSLB

- A) Prepare a USB-JTAG adapter cable
- B) Connect between your PC and the HSLB by the cable
- C) Use iMPACT software to download the CPLD configuration file to the CPLD

### 3, How to download FPGA firmware and check data from HSLB

#### 3-1, Download a utility folder

svn co [https://belle2.cc.kek.jp/svn/trunk/software/daq/copper/test\\_program](https://belle2.cc.kek.jp/svn/trunk/software/daq/copper/test_program)

#### 3-2, Download a HSLB firmware

(The following is just a copy of Nakao-san's e-mail :

[b2link\_ml:0082] Re: new HSLB firmware on bdaq SVN )

Get a firmware from the svn repository on bdaq.

*[on bdaq]*

`svn co file:///bdaq/svn/firmware/hslb/trunk hslb`

*[on elsewhere in KEK]*

**`svn co svn+ssh://xxx@bdaq.local.kek.jp/bdaq/svn/firmware/hslb/trunk hslb`**

*(please replace xxx with your bdaq account name)*

*[from outside of KEK]*

*add two lines in ~/.subversion/config*

*[tunnels]*

`ssh_bpost = ssh -A xxx@bpost.kek.jp ssh`

*(again, replace xxx with your bpost account name)*

*Then*

`svn co svn+ssh_bpost://xxx@bdaq/bdaq/svn/firmware/hslb/trunk hslb`

*(again, replace xxx with your bdaq account name.*

*bpost knows bdaq is bdaq.local.kek.jp.)*

*If you are using Windows, similar instruction can be found by googling with "subversion windows ssh tunnels".*

*Setting an SVN repository on bdaq instead of using belle2 SVN may sound like duplicating efforts, but I have a few reasons.*

- It will be a bit more convenient for online environment in experiment hall while it is a bit less convenient for people outside world.  
Construction will continue for next a few years, but experiment should continue much longer than that.*
- Based on agreement with IHEP, I don't think it's a good idea to unnecessarily widen the accessibility to the firmware code. Belle2 repository can be accessed by any Belle II members, while bdaq repository can be accessed by those who has a bdaq account.*

### 3-3, Download firmware to FPGA on HSLB

% gunzip ./hslb/ise12/HSLB.bit.gz (the file from the b2daq repository)

Then use an executable file in the utility folder to download the bit file.

% ./test\_program/hslb/boothslb -abcd ./HSLB.bit

"-abcd" specifies slots for the HSLB cards

If you fail to download firmware, please try following

- Check if CPLD on HSLB is already programmed.
- Check if copper and cprfin\_fngeneric have already been installed  
% /sbin/lsmmod
- Install drivers again  
# driver/cprdist-0.1.0/initd\_copper stop  
# driver/cprdist-0.1.0/initd\_copper start  
# driver/cprdist-0.1.0/initd\_cprfin\_fngeneric stop (do not use "restart")  
# driver/cprdist-0.1.0/initd\_cprfin\_fngeneric start
- Download firmware to CPLD on HSLB again. ( basically not necessary ).

### 3-4, Read data in COPPER FIFO

% ./copper/record-nakao -abcd

"-abcd" specify slots for HSLB cards

### 3-5, Use HSLB as a dummy data producer for test

1, Download a test firmware which make a HSLB produces dummy data with size of about 256byte and 1Hz rate

% ...daq/copper/test\_program/hslb/boothslb -abcd ./bit/HSLB\_1Hz256B.bit

"-abcd" specify slots for HSLB cards

2, Read data

% ...daq/copper/test\_program/copper/record-nakao -abcd

#### 4, How to get and download a TTRX firmware

##### 4-1, download a bit file from the svn repository on bdaq

~~svn co <https://belle2.cc.kek.jp/svn/trunk/software/daq/copper/driver/ttrx/ttrxprogs-20060413-for-SL5>~~

(From Nakao-san's e-mail :

[daq2ml:0162] FTSW firmware / tools update (ft2u) on bdaq SVN )

*[on bdaq]*

*svn co file:///bdaq/svn/firmware/ft2u/trunk ft2u*

*[on elsewhere in KEK]*

*svn co svn+ssh://xxx@bdaq.local.kek.jp/bdaq/svn/firmware/ft2u/trunk ft2u*

*(please replace xxx with your bdaq account name)*

*[from outside of KEK]*

*add two lines in ~/.subversion/config*

*[tunnels]*

*ssh\_bpost = ssh -A xxx@bpost.kek.jp ssh*

*(again, replace xxx with your bpost account name)*

*Then*

*svn co svn+ssh\_bpost://xxx@bdaq/bdaq/svn/firmware/ft2u/trunk ft2u*

*(again, replace xxx with your bdaq account name.*

*bpost knows bdaq is bdaq.local.kek.jp.)*

*If you are using Windows, similar instruction can be found by googling with "subversion windows ssh tunnels".*

There is a bit file for TTRX:

ft2u/bit-files/tt4r\*\*\*.bit

##### 4-2, Download the TTRX firmware

% cd test\_program

# ~nakao/bin/bootrx ~nakao/daq/ftsw/tt4r\*\*\*.bit

## 5, How to get and download a FTSW firmware

FTSW:

```
ttd3% ~nakao/bin/bootft -48 ~nakao/daq/ftsw/ft2u026.bit
```