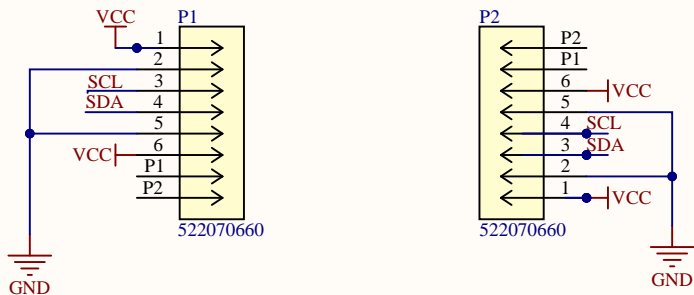
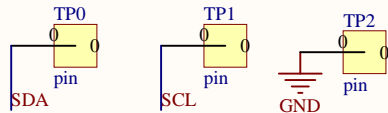


FFC Connector

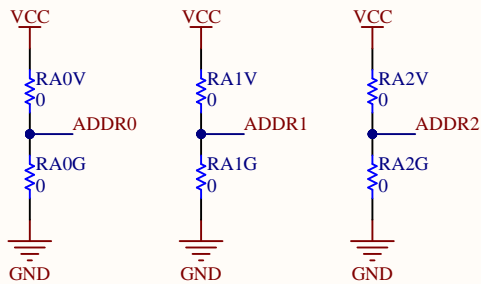


Master I2C Test Pin

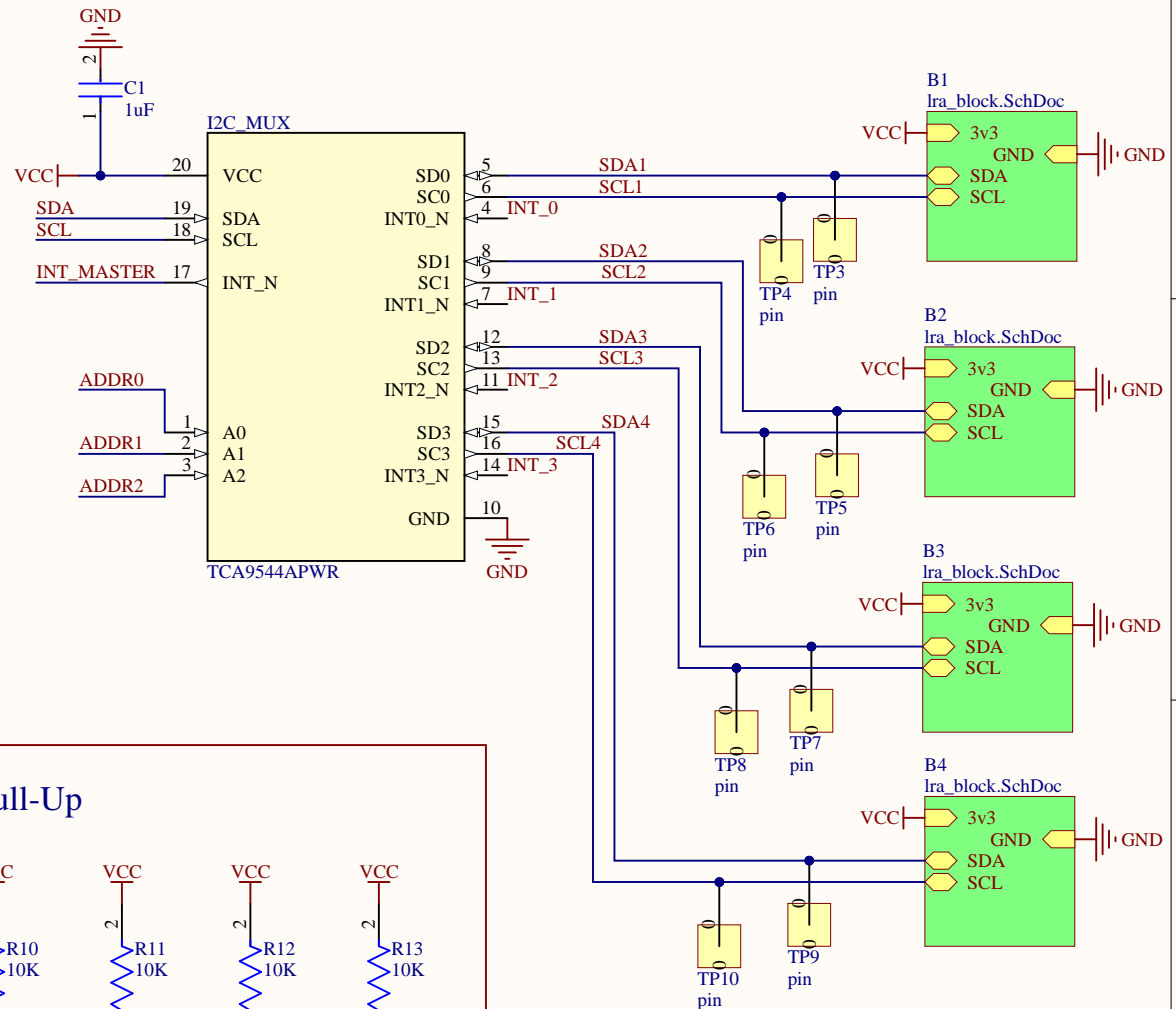
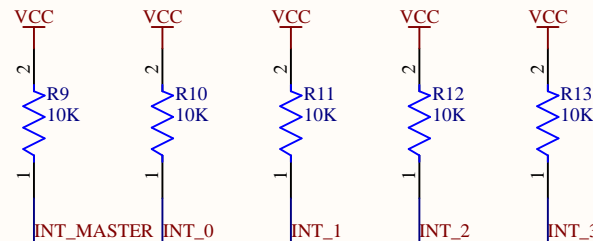


MUX Address Select

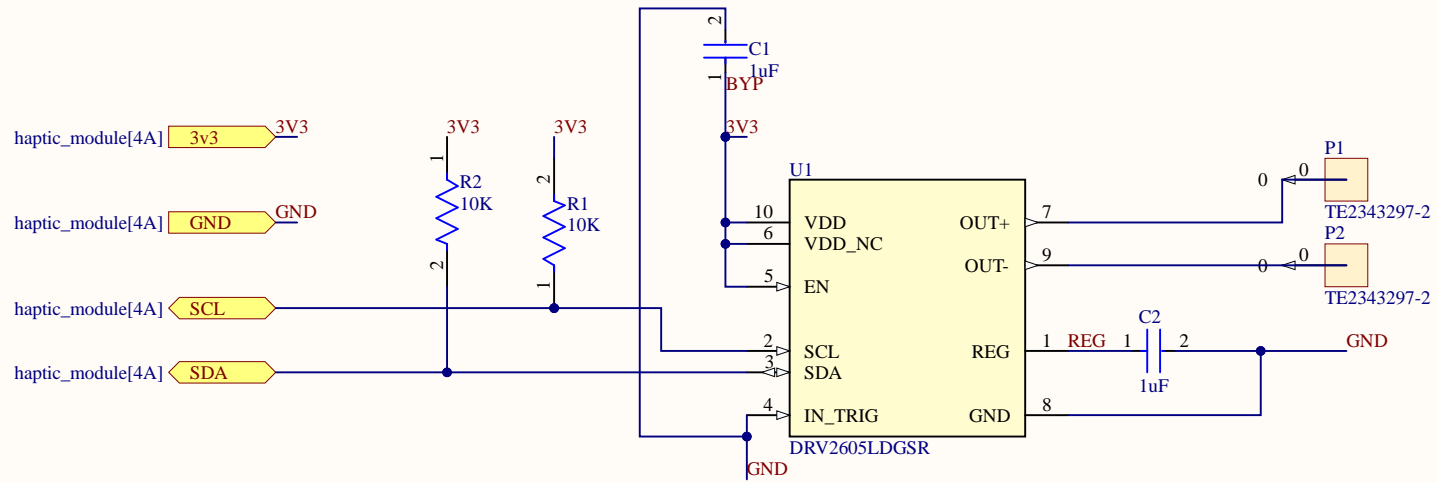
populate one resistor per addr bit



Interrupt Pull-Up

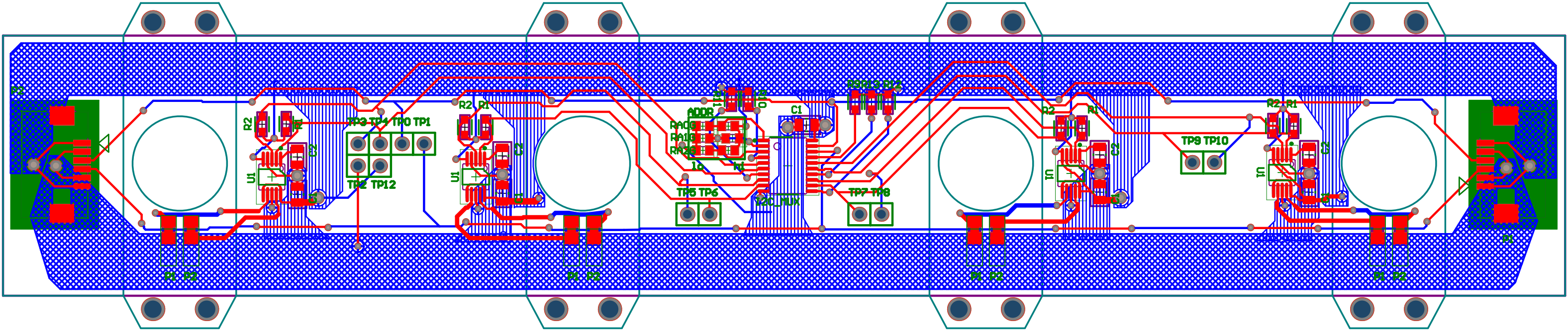


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Haptic Module			
Size	Number		Revision
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Date:	12/29/2024	Sheet of	
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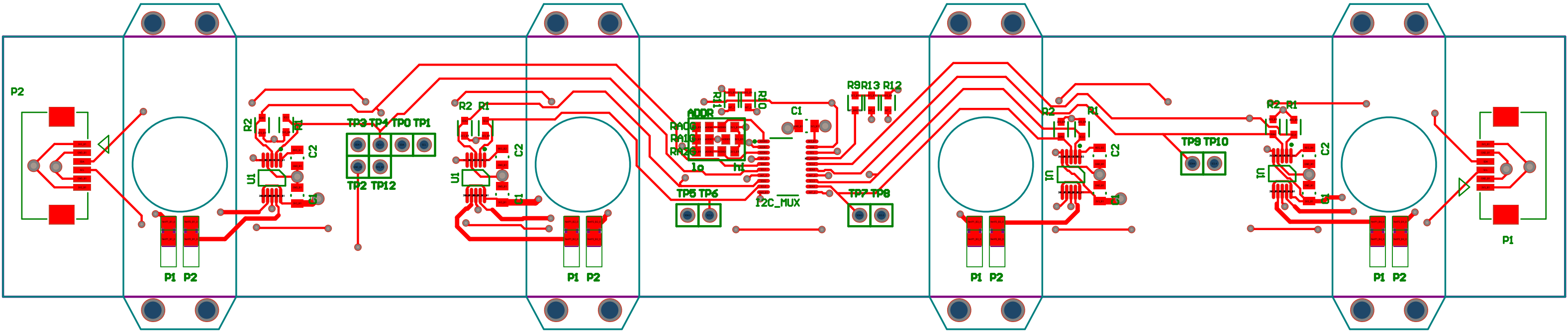


Title			
LRA_motor_block			
Size	Number	Revision	
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Date:	12/29/2024	Sheet of	
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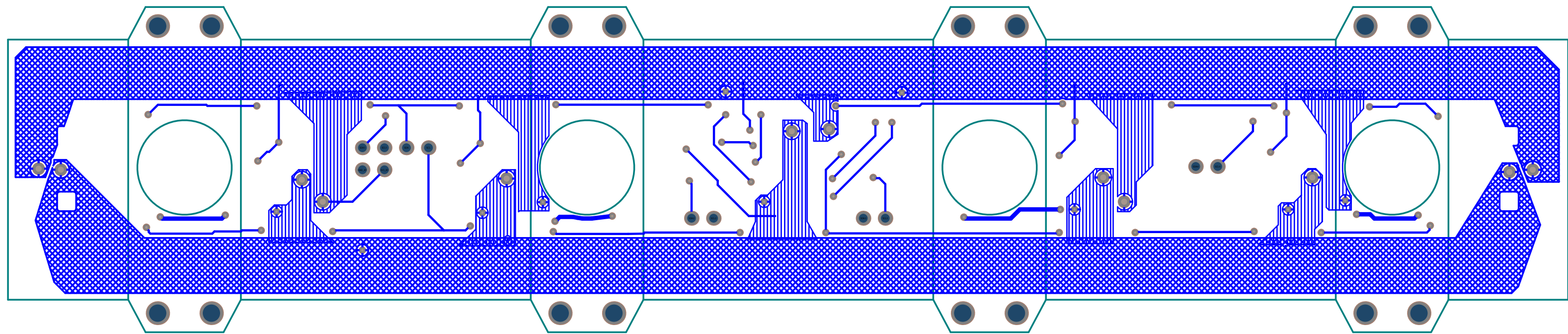
HAPTIC BELT // HAPTIC MODULE // PO-SHENG CHENG



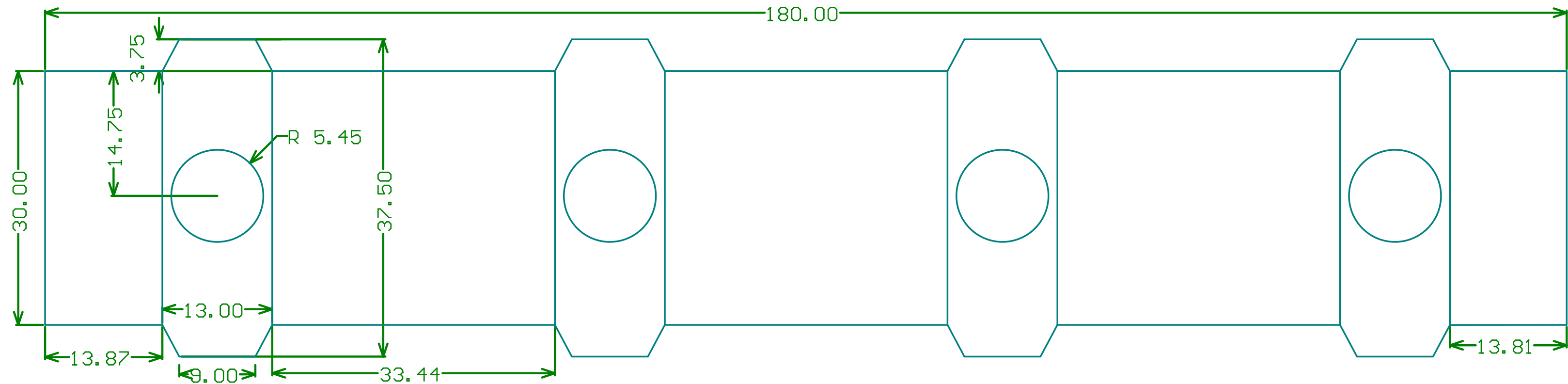
HAPTIC BELT // HAPTIC MODULE // PO-SHENG CHENG // LAYER 1



HAPTIC BELT // HAPTIC MODULE // PO-SHENG CHENG // LAYER 2



HAPTIC BELT // HAPTIC MODULE // PO-SHENG CHENG



HAPTIC BELT // HAPTIC MODULE // PO-SHENG CHENG

