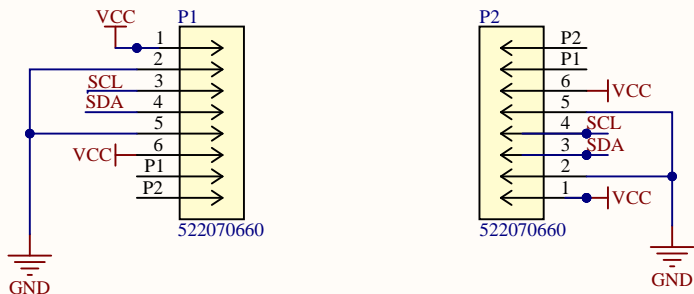
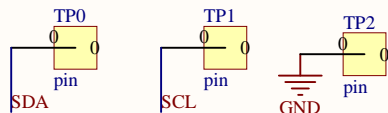


FFC Connector

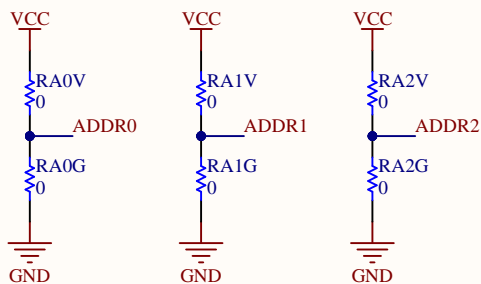


Master I2C Test Pin

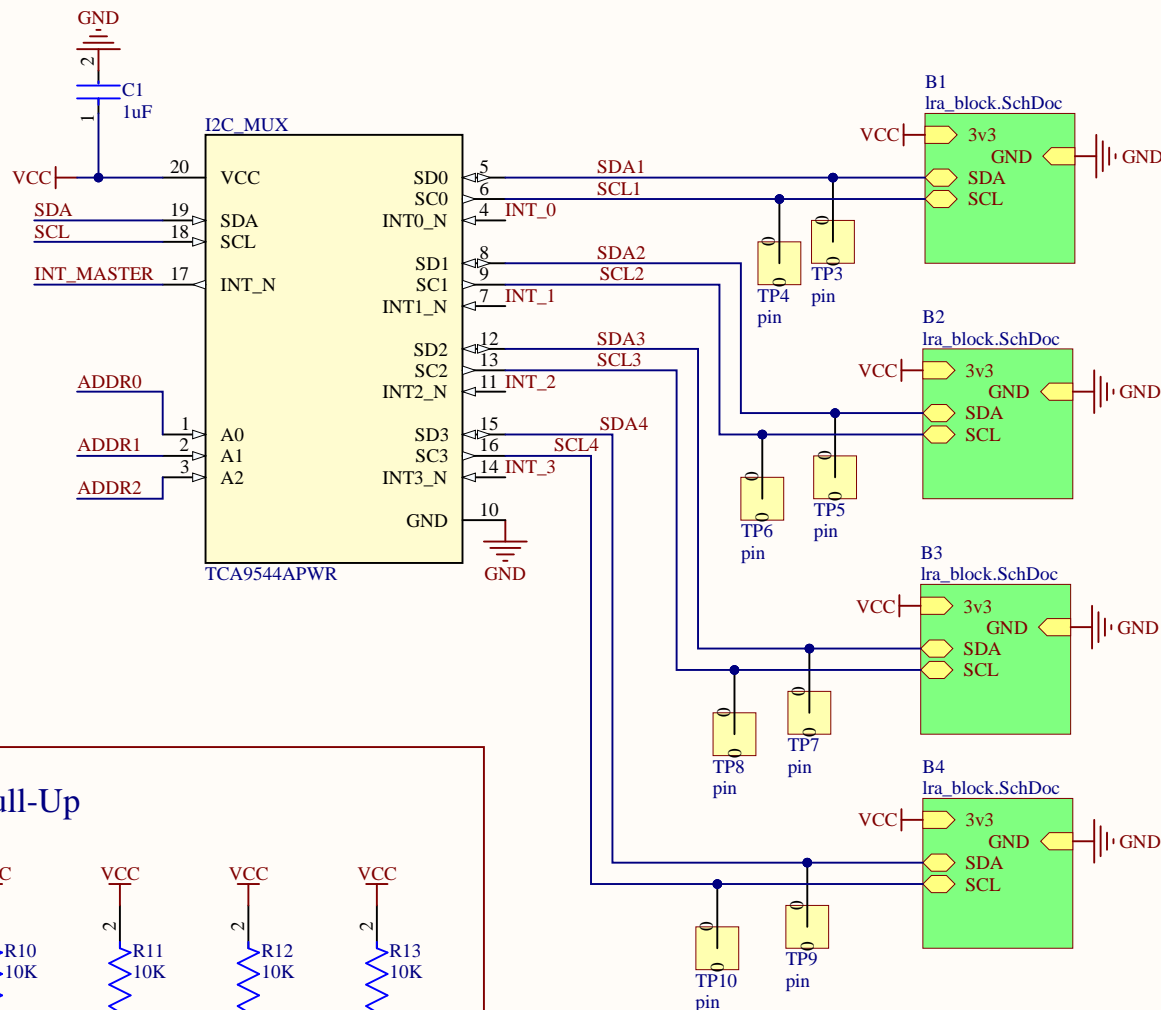
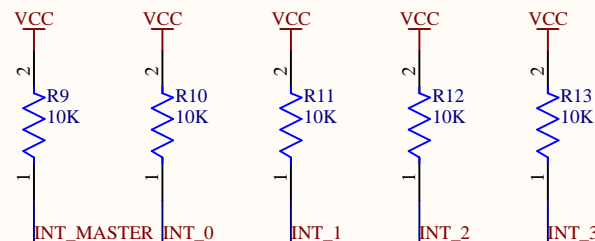


MUX Address Select

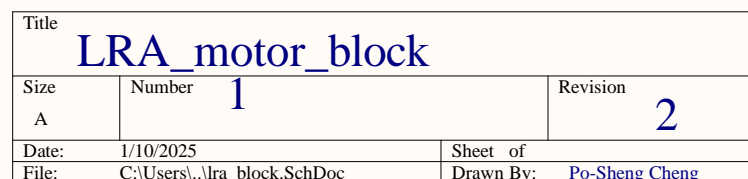
populate one resistor per addr bit



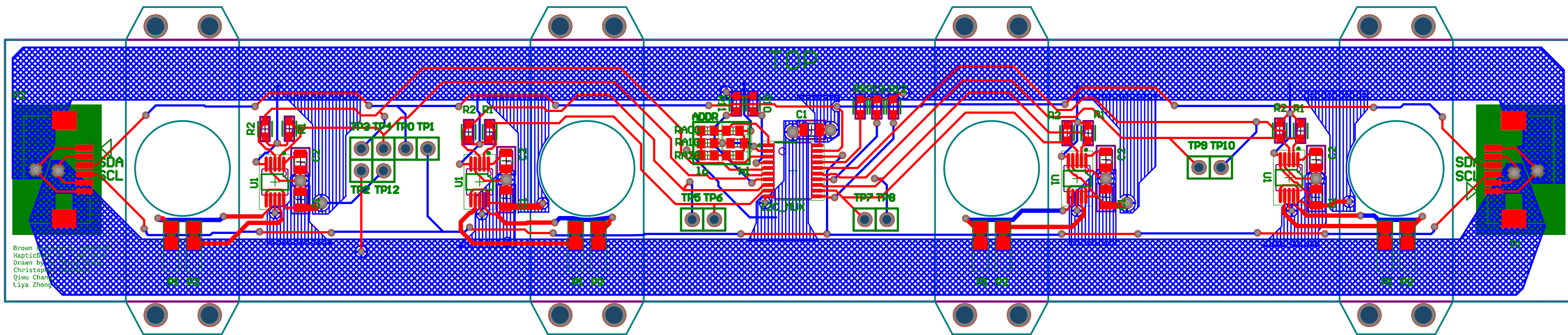
Interrupt Pull-Up



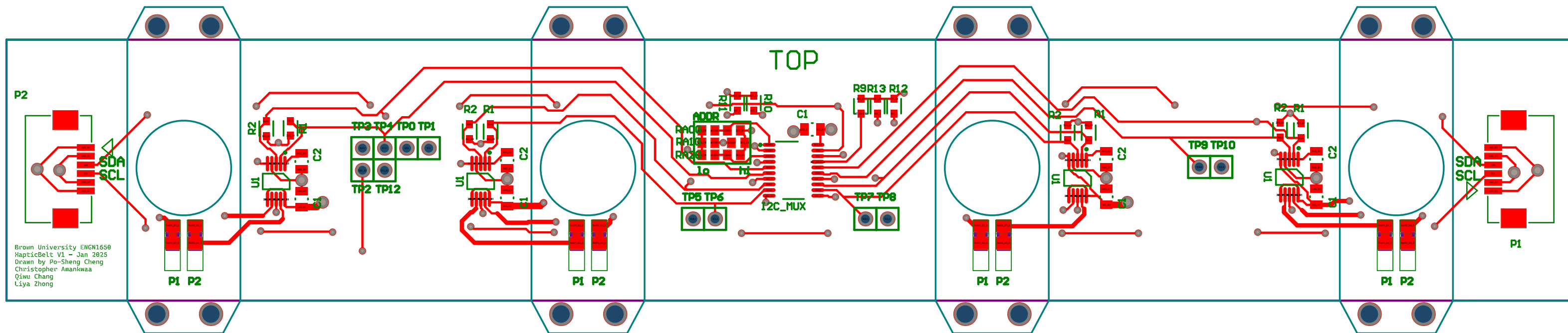
Title		
Haptic Module		
Size	Number	Revision
A	1	1
Date:	1/10/2025	Sheet of
File:	C:\Users\...\haptic_module.SchDoc	Drawn By: Po-Sheng Cheng



HAPTIC BELT // HAPTIC MODULE // PO-SHENG CHENG

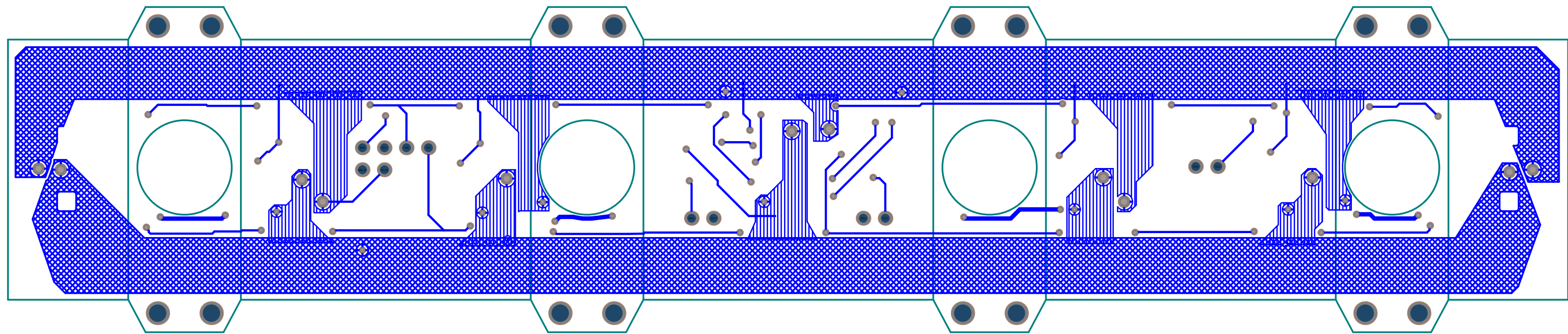


HAPTIC BELT // HAPTIC MODULE // PO-SHENG CHENG // LAYER 1

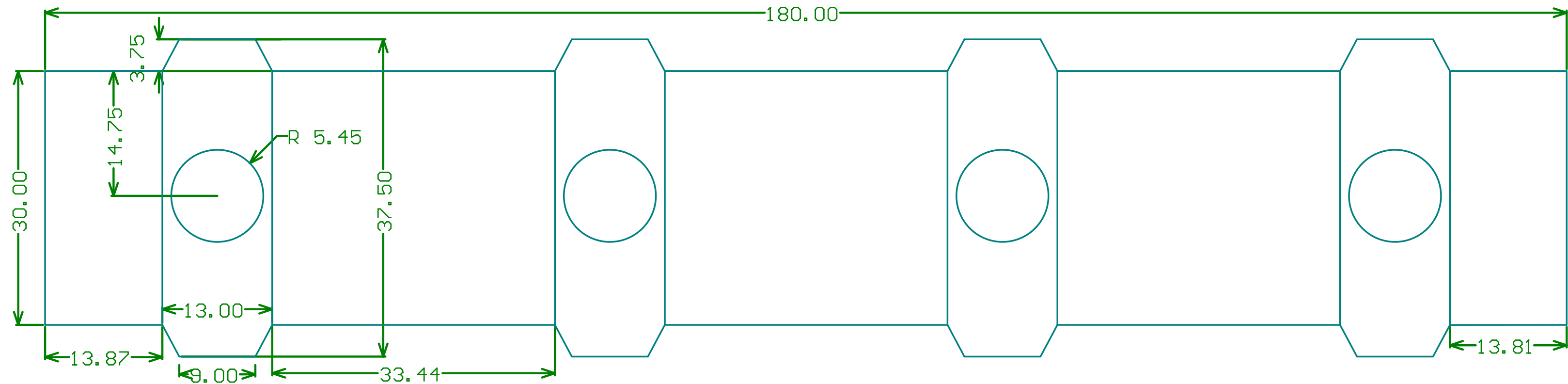


Brown University ENGN1650
HapticBelt V1 - Jan 2025
Drawn by Po-Sheng Cheng
Christopher Amankwaa
Qiwu Chang
Liya Zhong

HAPTIC BELT // HAPTIC MODULE // PO-SHENG CHENG // LAYER 2



HAPTIC BELT // HAPTIC MODULE // PO-SHENG CHENG



HAPTIC BELT // HAPTIC MODULE // PO-SHENG CHENG

