











```
Summary This script checks checks IAR and OAR
           Conflicting pads and vias have been reported
 Created by: Henk de Jonge, Transfer BV, The Netherlands
 Date: 15-11-2011
 This script has to be used with Pattern Class 6 and Drill Class C
Procedure FindAnnularRingClass6C;
Var
Begin
    // Obtain the PCB document interface
    If Board = Nil Then Exit;
    //Showmessage('Layer: ' + FloatToStr(Layer));
   RunProcess('PCB:DeSelect');
    //Create the list with reports
    ReportList.Add('Inner and Outer Annular Ring violations report:');
```

1

```
// PHD = finished hole size + 0.100mm for via's (no more difference for holes <= 0.45 mm)
// IAR = 1/2 * (Inner pad diameter - PHD)
// OAR = 1/2 * (Outer pad diameter - PHD)
// ToleranceS is Tolerance for holes Smaller then 0.45mm -- not valid
// ToleranceL is Tolerance for holes Larger then 0.45mm -- not valid
//Obtain the PCB Current Origin, needed for X and Y coordinates
While Via <> Nil Do
Begin
     // eTopLayer is 1 ; eBottomLayer is 32
    For Layer := eTopLayer to eBottomLayer Do
     Begin
         If (Layer = 1) or (Layer = 32) Then
         Begin
           If (Via_Afm <> 0) Then
             Begin
               If (AnnularRing < OAR) Then
               Begin
                 ReportList.Add('OAR Violation for Via on ' +
                 'Outer Annular Ring ' +
                 ' is smaller dan OAR Rule: ' +
               End;
           End
         End
```

```
Begin
            If Layer < NumOfLayers Then
            Begin
              If (Via_Afm <> 0) Then
              Begin
                If (AnnularRing < IAR) Then</pre>
                Begin
                  ReportList.Add('IAR Violation for Via on ' +
                  'Inner Annular Ring ' +
                  ' is smaller dan IAR Rule: ' +
               End;
             End;
            End;
          End;
     End;
End;
ReportList.Add('PADS:');
// PADS
While Pad <> Nil Do
Begin
     For Layer := MinLayer to MaxLayer Do
     Begin
         if (Layer = 74) Then
         End;
     End;
```

```
if Hole Width > 0 Then
Begin
  // eTopLayer is 1 ; eBottomLayer is 32
  For Layer := eTopLayer to eBottomLayer Do
    If (Layer = 1) or (Layer = 32) Then
    Begin
       If (Pad_AfmX <> 0) And (Pad_AfmY <> 0) Then
         If (PadXAnnularRing < OAR) Then</pre>
         Begin
          ReportList.Add('OAR Violation for Pad-X on ' +
           'Outer Annular Ring ' +
          ' is smaller dan OAR Rule: ' +
         End;
         If (PadYAnnularRing < OAR) Then</pre>
         Begin
          ReportList.Add('OAR Violation for Pad-Y on ' +
           'Outer Annular Ring ' +
          ' is smaller dan OAR Rule: ' +
        End;
      End:
    End
    Else
    Begin
      If Layer < NumOfLayers Then</pre>
        If (Pad_AfmX <> 0) And (Pad_AfmY <> 0) Then
          If (PadXAnnularRing < IAR) Then</pre>
           Begin
            ReportList.Add('IAR Violation for Pad-X on ' +
```

```
'Inner Annular Ring ' +
                  ' is smaller dan IAR Rule: ' +
                End;
                If (PadYAnnularRing < IAR) Then</pre>
                Begin
                  ReportList.Add('IAR Violation for Pad-Y on ' +
                  'Inner Annular Ring ' +
                  ' is smaller dan IAR Rule: ' +
                End;
               End;
             End;
           End;
         End;
       End;
   End;
   ReportList.SaveToFile('C:\TEMP\AnnularRing.txt');
   Showmessage('Output completed. See C:\TEMP\AnnularRing.txt for results.');
End;
{......}
```

Comment	Description	Designator	Footprint	LibRef	Quantity
batery		BAT?	jst 2pin	batery	1
0,022uF		C1, C5	0603	capacitor	2
470pF		C2	0603	capacitor	1
10uF		C3, C4, C8	0603	capacitor	3
47nF		C6, C7	CAPC1005X04L	capacitor	2
screw connector	screw connector	CON1	qi coil	screw connector	1
USB		CON2	microusb	USB	1
Tagconnect		CON3	Tagconnect	Tagconnect	1
MBR0540-TP		D1, D2, D3, D4, D5	Diode	diode	5
FC4SDCBMF6.0-T1		D6	Diode	diode	1
ferite bead		L1	0603	Spoel	1
LED		LED1	LED	LED	1
ESP32S_Module	Esp-32S	M1	ESP32S_W	ESP32S_Module	1
2N7002ET1G		Q1, Q2	N-channel FET	fet	2
10K		R1	CAPC1005X04L	resistor	1
1k		R2, R3, R4, R5, R6, R9	0603	resistor	6
resistor		R7, R8	0603	resistor	2
ap2112 SOT25		u3	AP2112 sot25	ap2112 SOT25	1
MCP1755T-500E/OT		U1	AP2112 sot25	MCP1755T-500E/OT	1
MCP73831		U2	MCP73831/2	MCP73831	1