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## PERSONAL PROFILE

I am passionate about Embedded Firmware/Software and FPGAs with great experience from an RF Firmware Engineering placement. I am interested in Linux kernel and driver development and I am active in the open-source community with contributions to Gravity-lang (compiler). I am always looking for an interesting project to dive into.

Key strengths:

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| * Self-motivated * Problem-solving * FPGA Placement experience | * C & C++ * Embedded Firmware/Software * Linux kernel + driver development |

## Employment

Firmware Engineer, Placement Spirent Communications June 2016 – August 2017

* Embedded programming on Xilinx MicroBlaze FPGAs and PIC16/24 microcontrollers.
* Linux USB and PCIe kernel driver development.
* Implemented on-chip power levelling and calibration for GNSS RF signal generators.
* Controlling on-board fans, LEDs, EEPROM, and other peripherals with I2C and SMBus.
* Configuring, building, and maintaining Embedded Linux distributions using Yocto.

## EDUCATIoN

MSc (Eng) Embedded Systems Engineering University of Leeds Fall 2018 – Summer 2019

* Courses include: Digital Signal Processing for Communications, FPGA Design for System-on-Chip, Embedded Microprocessor System Design, Medical Electronics and E-Health, Secure Hardware Design

BSc (Hons) Computer Science University of Plymouth Fall 2014 – Summer 2018

* **First Class Honours** with Certificate of Professional Industrial Experience.
* **Final Project:** FPGA-based 16-bit RISC soft-microprocessor (with IO & interrupts) and Compiler.
* **Awards**: Top Final Year Student, Best Final Project, Revell Research Systems Prize.
* Courses include: Digital Electronics, Embedded Systems and Compilers, Machine Vision, Computation Theory.

## Open-Source Projects & Contributions

* **16-bit RISC soft-microprocessor**  [bendl/](https://github.com/bendl/vcc)prco304 An FPGA-based RISC soft-microprocessor written in Verilog, complete with Compiler and programming languange.
* **ARM Cortex M0 Processor Board**  [bendl/armm0](https://github.com/bendl/armm0) A 2-layer board for the Minispartan6+ FPGA development kit. Features an STM32F0 TSSOP processor, dual power supplies, I2C, ICSP, and LEDs.
* **Gravity-lang**  [marcobambini/gravity](https://github.com/marcobambini/gravity) Contributor to an open-source compiler and virtual-machine. Contributions include fixing Windows runtime.

## Additonal Experience and Awards

* Dean’s List 2015-2018 member. List of students who achieved academic excellence in their studies.

## Technologies

* C, C++, Python, Linux (user + kernel), Bash
* Xilinx FPGAs, ISE, Vivado, Impact, Visual Studio, CMake, CUDA
* GitHub, GitLab, SVN

## References

Available on request.