

FPGA-based RISC Microprocessor and Compiler (Rev. 1.00)

PRCO304 - Final Stage Computing Project

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Revision History

Table 1: Document revisions.

| Date | Version | Changes |
|------------|---------|--------------------------|
| 11/03/2018 | 1 | Initial section outline. |

Abstract

Table of Contents

| | | |
|-----------|---|----------|
| 1 | Introduction | 3 |
| 2 | Background | 3 |
| 2.1 | Current Implementations | 3 |
| 3 | Objectives | 3 |
| 4 | Legal and Ethical Considerations | 3 |
| 5 | Project Management | 3 |
| 6 | Requirements | 3 |
| 7 | High Level Design | 3 |
| 8 | Testing and Verification | 3 |
| 9 | Conclusion | 3 |
| 10 | Appendices | 3 |
| 10.1 | Appendix A. PRCO Core Reference Guide | 3 |
| 10.2 | Appendix B. PRCO Compiler Reference Guide | 3 |
| 10.3 | Appendix C. Project Initiation Document | 3 |

1 Introduction**2 Background****2.1 Current Implementations****3 Objectives****4 Legal and Ethical Considerations****5 Project Management****6 Requirements****7 High Level Design****8 Testing and Verification****9 Conclusion****10 Appendices****10.1 Appendix A. PRCO Core Reference Guide****10.2 Appendix B. PRCO Compiler Reference Guide****10.3 Appendix C. Project Initiation Document**