

BSC-1816 - Processor Documentation

PRCO304 - Processor Documentation

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Revision History

Date	Version	Changes
06/02/2018	1.01	Foo bar.
04/02/2018	1.00	Initial revision. Processor introduction. Initial ISA. Initial Register definitions.

Table 1: Document revisions.

Table of Contents

1	BSC-1816 Processor	3
1.1	Features	3
2	BSC-1816 Architecture	4
2.1	Registers	4
2.2	Interrupts and Exceptions	4
3	BSC-1816 Instruction Set Architecture	5
3.1	General Instructions	5
3.2	Special Instructions	5
4	Compiler	6
4.1	6

1 BSC-1816 Processor

The BSC-1816 processor is a soft-microprocessor design targeted for general purpose computing and co-processing. test test testing.

1.1 Features

- Small, embeddable, Verilog core.
- 16-bit RISC instruction set.
- 16-bit register, ALU, and IO, bus widths.
- 12+12 general purpose IO inputs and outputs.
- 9 special IO pins.
 - 4 PWM pins.
 - 2 RS232 pins.
 - 3 SPI pins.
 - foo.

2 BSC-1816 Architecture

2.1 Registers

2.2 Interrupts and Exceptions

3 BSC-1816 Instruction Set Architecture

3.1 General Instructions

3.2 Special Instructions

4 Compiler

4.1