

Multi-core RISC Processor Design and Implementation (Rev. 1.00)

ELEC5881M - Interim Report

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Revision History

Date	Version	Changes
20/05/2018	3.14	Add background research to appendix.
19/05/2018	3.13	Update abstract to align with guidelines.
19/05/2018	3.12	Fix ISA pseudo-codes.
11/03/2018	1.00	Initial section outline.

Table 1: Document revisions.

Abstract

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Date: March 30, 2019

Table of Contents

1	Abstract (not in toc)	8
2	Declaration of Academic Integrity (not in toc)	9
3	Acknowledgements (not in toc)	10
4	Introduction	11
4.1	Why Multi-core?	11
4.2	Why RISC?	11
4.3	Why FPGA?	11
5	Background	12
5.1	Single core vs. Multi-core vs. Many-core	12
5.2	Network-on-chip	12
5.2.1	OpenPiton	12
5.3	Summary	12
6	RISC Core Design	13
6.1	Design Goals	13
6.2	Instruction Set Architecture	13
6.2.1	Data Sizes	13
6.2.2	Registers	13
6.2.3	Endianness	13
6.3	High Level Design	13
6.4	Memory-mapped peripherals	13
6.4.1	Wishbone Master Bus	13
6.5	Optimisations	13
6.5.1	Pipelining	13
6.5.2	Stall Avoidance	13
6.6	Core Verification	13
6.7	Conclusion	13
7	Multi-core Communication Design	14
7.1	Interconnect Goals	14
7.2	Interconnect Layout	14
7.2.1	Bus Design	14
7.2.2	Core Pin Assignments	14
7.3	Core-to-core Communication	14

7.4	Shared-Resource Control	14
7.4.1	Resource Scheduling	14
7.5	Verification	14
7.6	Conclusion	14
8	Core Analysis	15
8.1	FPGA Implementation Analysis	15
8.1.1	Space/Resource Usage	15
8.1.2	Static Timing Analysis	15
8.2	Speed Analysis	15
8.2.1	Parallel Reduction Algorithms	15
8.2.2	Parallel DFT Algorithm	15
9	Conclusion	16
9.1	RISC Core Review	16
9.2	Multi-core Review	16
9.3	Verification Review	16
9.4	Goal Review	16

[1] [2] [3]

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Chapter 1

Abstract (not in toc)

Chapter 2

Declaration of Academic Integrity (not in toc)

Chapter 3

Acknowledgements (not in toc)

Chapter 4

Introduction

4.1 Why Multi-core?

4.2 Why RISC?

4.3 Why FPGA?

Chapter 5

Background

5.1 Single core vs. Multi-core vs. Many-core

5.2 Network-on-chip

5.2.1 OpenPiton

5.3 Summary

Chapter 6

RISC Core Design

6.1 Design Goals

6.2 Instruction Set Architecture

6.2.1 Data Sizes

6.2.2 Registers

6.2.3 Endianness

6.3 High Level Design

6.4 Memory-mapped peripherals

6.4.1 Wishbone Master Bus

6.5 Optimisations

6.5.1 Pipelining

6.5.2 Stall Avoidance

6.6 Core Verification

6.7 Conclusion

Chapter 7

Multi-core Communication Design

7.1 Interconnect Goals

7.2 Interconnect Layout

7.2.1 Bus Design

7.2.2 Core Pin Assignments

7.3 Core-to-core Communication

7.4 Shared-Resource Control

7.4.1 Resource Scheduling

7.5 Verification

7.6 Conclusion

Chapter 8

Core Analysis

8.1 FPGA Implementation Analysis

8.1.1 Space/Resource Usage

8.1.2 Static Timing Analysis

8.2 Speed Analysis

8.2.1 Parallel Reduction Algorithms

8.2.2 Parallel DFT Algorithm

Chapter 9

Conclusion

9.1 RISC Core Review

9.2 Multi-core Review

9.3 Verification Review

9.4 Goal Review