

Multi-core RISC Processor Design and Implementation (Rev. 1.00)

ELEC5881M - Interim Report

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in Embedded Systems Engineering

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Revision History

| Date | Version | Changes |
|------------|---------|---|
| 20/05/2018 | 3.14 | Add background research to appendix. |
| 19/05/2018 | 3.13 | Update abstract to align with guidelines. |
| 19/05/2018 | 3.12 | Fix ISA pseudo-codes. |
| 11/03/2018 | 1.00 | Initial section outline. |

Table 1: Document revisions.

Abstract

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Date: March 30, 2019

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