Cache miss: packet\_num: 33 src: 0 address: 4026531840, cycle: 1

when the request is pushed into the outgoing queue

when the request is sent

ICNT2 (request arrive): packet type: 0 src: 193 dst: 192 packet\_num: 33 sid: 32 chiplet: 0 packet is pushed to incoming queue, cycle: 2

1- L2 (push) : packet type: 0 src: 193 dst: 192 packet\_num: 33 move mem request from icnt to ROP queue, cycle: 34

1- L2 (pop) : packet type: 0 src: 193 dst: 192 packet\_num: 33 popped from ROP queue and pushed to ICNT-2-L2 queue, cycle: 154

1- L2 (pop) : packet type: 0 src: 193 dst: 192 packet\_num: 33 packet is popped in the destination from incoming queue. It's cache hit or miss!, cycle: 155

1- L2 (push) : packet type: 0 src: 193 dst: 192 packet\_num: 33 cache MISS,

cycle: 155

DRAM Operation

1- L2 (push) : packet type: 2 src: 193 dst: 192 packet\_num: 33 packet is pushed to L2-2-ICNT outgoing queue, cycle: 273

ICNT(1)(remote): packet type: 2 src: 192 dst: 193 packet\_num: 33, packet is about to be sent, cycle: 274

ICNT2 (reply arrive): packet type: 2 src: 192 dst: 193 packet\_num: 33 sid: 32 chiplet: 1 reply is pushed to processing queue, cycle: 274

packet num: 33 is popped from cluster 32 queue and is about to be processed in core, cycle: 306

Cache miss: packet\_num: 85 src: 0 address: 4026531840, cycle: 1

when the request is pushed into the outgoing queue

when the request is sent

ICNT2 (request forward) : packet type: 0 src: 194 dst: 192 packet\_num : 85 sid : 84 chiplet: 1 the packet is pushed to the forwarding queue, cycle: 2

CORE(forward\_waiting\_pop): packet type: 0 src: 194 dst: 192 packet\_num: 85 sid: 84 chiplet: 1 packet is popped from outgoing queue and is about to be sent, cycle: 34

ICNT2 (request arrive): packet type: 0 src: 193 dst: 192 packet\_num: 85 sid: 84 chiplet: 0 packet is pushed to incoming queue, cycle: 35

1- L2 (push) : packet type: 0 src: 193 dst: 192 packet\_num: 85 move mem request from icnt to ROP queue, cycle: 118

1- L2 (pop) : packet type: 0 src: 193 dst: 192 packet\_num: 85 popped from ROP queue and pushed to ICNT-2-L2 queue, cycle: 416

1- L2 (pop) : packet type: 0 src: 193 dst: 192 packet\_num: 85 packet is popped in the destination from incoming queue. It's cache hit or miss!, cycle: 445

1- L2 (pop) : packet type: 0 src: 193 dst: 192 packet\_num: 85 packet is popped in the destination from incoming queue. It's cache hit or miss!, cycle: 446

1- L2 (pop) : packet type: 0 src: 193 dst: 192 packet\_num: 85 packet is popped in the destination from incoming queue. It's cache hit or miss!, cycle: 447

1- L2 (pop) : packet type: 0 src: 193 dst: 192 packet\_num: 85 packet is popped in the destination from incoming queue. It's cache hit or miss!, cycle: 448

1- L2 (push) : packet type: 2 src: 193 dst: 192 packet\_num: 85 cache HIT. move from cache bank to L2-2-ICNT queue, cycle: 448

ICNT(1)(remote): packet type: 2 src: 192 dst: 194 packet\_num: 85, packet is about to be sent, cycle: 449

ICNT2 (reply forward) : packet type: 2 src: 192 dst: 194 packet\_num: 85 sid: 84 chiplet: 3 the packet is pushed to the forwarding queue, cycle: 449

CORE(forward\_waiting\_pop): packet type: 2 src: 192 dst: 194 packet\_num: 85 sid: 84 chiplet: 3 packet is popped from outgoing queue and is about to be sent, cycle: 481

ICNT2 (reply arrive): packet type: 2 src: 195 dst: 194 packet\_num: 85 sid: 84 chiplet: 2 reply is pushed to processing queue, cycle: 482

packet num: 85 is popped from cluster 84 queue and is about to be processed in core, cycle: 514

Cache miss: packet\_num: 87 src: 0 address: 4026531840 cycle: 2

when the request is pushed into the outgoing queue

when the request is sent

ICNT2 (request forward) : packet type: 0 src: 194 dst: 192 packet\_num : 87 sid : 86 chiplet: 1 the packet is pushed to the forwarding queue, cycle: 3

CORE(forward\_waiting\_pop): packet type: 0 src: 194 dst: 192 packet\_num: 87 sid: 86 chiplet: 1 packet is popped from outgoing queue and is about to be sent, cycle: 35

ICNT2 (request arrive): packet type: 0 src: 193 dst: 192 packet\_num: 87 sid: 86 chiplet: 0 packet is pushed to incoming queue, cycle: 36

1- L2 (push) : packet type: 0 src: 193 dst: 192 packet\_num: 87 move mem request from icnt to ROP queue, cycle: 120

1- L2 (pop) : packet type: 0 src: 193 dst: 192 packet\_num: 87 popped from ROP queue and pushed to ICNT-2-L2 queue, cycle: 424

1- L2 (pop) : packet type: 0 src: 193 dst: 192 packet\_num: 87 packet is popped in the destination from incoming queue. It's cache hit or miss!, cycle: 453

1- L2 (pop) : packet type: 0 src: 193 dst: 192 packet\_num: 87 packet is popped in the destination from incoming queue. It's cache hit or miss!, cycle: 454

1- L2 (pop) : packet type: 0 src: 193 dst: 192 packet\_num: 87 packet is popped in the destination from incoming queue. It's cache hit or miss!, cycle: 455

1- L2 (pop) : packet type: 0 src: 193 dst: 192 packet\_num: 87 packet is popped in the destination from incoming queue. It's cache hit or miss!, cycle: 456

1- L2 (push) : packet type: 2 src: 193 dst: 192 packet\_num: 87 cache HIT. move from cache bank to L2-2-ICNT queue, cycle: 456

ICNT(1)(remote): packet type: 2 src: 192 dst: 194 packet\_num: 87, packet is about to be sent, cycle: 457

ICNT2 (reply forward) : packet type: 2 src: 192 dst: 194 packet\_num: 87 sid: 86 chiplet: 3 the packet is pushed to the forwarding queue, cycle: 457

CORE(forward\_waiting\_pop): packet type: 2 src: 192 dst: 194 packet\_num: 87 sid: 86 chiplet: 3 packet is popped from outgoing queue and is about to be sent, cycle: 489

ICNT2 (reply arrive): packet type: 2 src: 195 dst: 194 packet\_num: 87 sid: 86 chiplet: 2 reply is pushed to processing queue, cycle: 490

packet num: 87 is popped from cluster 86 queue and is about to be processed in core, cycle: 522