Fragment Abstraction for Concurrent Shape Analysis

No Institute Given

1 Introduction

Concurrent algorithms with an unbounded number of threads that concurrently access a dynamically allocated shared state are of central importance in a large number of software systems. They provide efficient concurrent realizations of common interface abstractions, and are widely used in libraries, such as the Intel Threading Building Blocks or the `java.util.concurrent` package. They are notoriously difficult to get correct and verify, since they often employ fine-grained synchronization and avoid locking when possible. A number of bugs in published algorithms have been reported [9, 27]. Consequently, significant research efforts have been directed towards developing techniques to verify correctness of such algorithms, in particular to verify that concurrent algorithms that implement standard data structure interfaces are *linearizable*, meaning that each method invocation can be considered to occur atomically at some point between its call and return. Many such techniques require significant *manual* effort for constructing a proof of correctness (e.g., [25, 39]), in some cases with the support of an interactive theorem prover (e.g., [37, 6, 7, 32, 31]). Development of automated verification techniques is a difficult challenge.

A major difficulty is that a successful verification technique must be able to reason about fine-grained concurrent algorithms that are infinite-state in many dimensions: they consist of an unbounded number of concurrent threads, which operate on an unbounded domain of data values, and use unbounded dynamically allocated memory. Perhaps the hardest of these is the problem of handling dynamically allocated memory. Consequently, all existing techniques for automatically proving correctness of such concurrent algorithms restrict attention to the case where the heap represents shared data byp singly-linked lists [1, 17, 2, 33, 40]. Many of these techniques impose additional restrictions on the considered problem, such as bounding the number of accessing threads [3, 44, 42]. [Add other restrictions] However, many concurrent data structure implementations employ more sophisticated structures, such as skip lists [13, 22, 35], trees, and arrays of of singly-linked lists [8]. There are no techniques that have been applied to automatically verify concurrent algorithms that operate on such data structures.

> The last list and its description must be improved. We actually should be more precise about what previous work has achieved, in terms of which combinations of challenges have been overcome

Contributions In this paper, we present a technique for automatic verification of concurrent data structure implementations that operate on dynamically allocated heap structures which are more complex than just singly-linked lists. Our approach is the first

framework that can automatically verify concurrent data structure implementations that employ skip lists, singly linked lists, as well as arrays of singly linked lists, at the same time as handling an unbounded number of concurrent threads, an unbounded domain of data values (including timestamps), and an unbounded shared heap.

Our technique is based on a novel shape abstraction, called *fragment abstraction*, which in a simple way is able to represent different kinds of unbounded heap structures, such as arrays of singly linked lists and skip lists. Its main idea is to represent a set of heap states by a set of *fragments*. Each fragment is simply a pair of node types (called *tags*) that are connected by a pointer. A tag can be seen as a finitary abstraction of a heap node, which summarizes both local information about values of its data fields as well as global information about its position in the heap, including how it can reach to and be reached from (by following chains of pointers) other heap cells that are pointed to by global variables. A set of fragments represents the set of heap structures in which each pair of pointer-connected nodes are represented by some fragment in the set. Inuitively, a set of fragments describes the set of heaps that can be formed by "pieced together" fragments in the set. This "piecing together" must be both locally consistent (appending only fragments that agree on their common node), and globally consistent (respecting the global reachability information). By construction, our fragment abstraction is finitary, since there is a bounded set of tags.

Fragment abstraction can, in a natural way, be combined with other abstractions for handling unbounded data domains and for handling an unbounded number of threads. Our fragment abstraction technique copes with an unbounded data domain by letting the definition of tags incorporate a suitable data abstraction to the data fields in heap nodes. We cope with the challenge of an unbounded number of threads by incorporating the successful thread-modular approach [4]; this is done simply by letting each set of fragments represent only the heap cells that are accessible to an arbitrary single thread.

We have implemented our approach and applied it to automatically verify correctness, in the sense of linearizability, of a large number of concurrent data structure algorithms. More specifically, we have automatically verified linearizability of most linearizable concurrent implementations of sets, stacks, and queues, which emply singly-linked lists, skip lists, or arrays of timestamped singly-linked lists, which are known to us in the literature on concurrent data structures.

For this verification, we we specify linearizability using the simple and powerful technique of *observers* [1], which can be seen as monitors that report violations of the linearizability criterion. Observers synchronize with the monitored concurrent programs at designated actions. This can be done in two ways. (1) For concurrent implementations of stacks and queues, linearizability can be precisely specified by observers that synchronize on call and return actions of methods, as shown by [5, 20]; this is done without any user annotation. (2) For sets, the verification requires the user to annotate how linearization points are placed in each method; in most cases this is a small burden for the verifier. The observer then synchronizeson these linearization points. Our implementation then automatically checks, using our novel technique based on fragment abstraction, that a supplied C-like description of a concurrent data structure is a correct linearizable implementation of a stack, queue, or set.

The fact that our fragment abstraction has been able to automatically verify all supplied concurrent algorithms, also those that employ skiplists or arrays of SLLs, indicates that the fragment abstraction is a simple mechanism for capturing both the local and global information about heap cells that is necessary for verifying correctness, in particular for concurrent algorithms where an unbounded number of threads interact via a shared heap.

> Here goes the outline

Related Work [14] presents a thread-modular shape analysis for multi-threaded programs in which locks protect portions of the heap, and threads have to acquire a lock before accessing the corresponding portion of the heap. Fine-grained concurrent algorithms do not follow this pattern.

Thread-modular approaches for verifying fine-grained concurrent algorithms have been presented in several works. [1] introduce observers to specify the semantics of data structures. It verifies program using the thread-modular approach, where heaps are specified by reachability constraints between cells pointed to by program variables. The work is applied to concurrent stack and queue implementaitons based on singly-linked lists. [23] present a more efficient way to handle the expensive interference steps for common programming idioms in lock-free data structures. [2] extend this approach to be able to verify linearizability for a large class of concurrent data structures based on singly-linked lists. It used a specifically designed finitary abstraction of unbounded singly-linked list segments.

[We should say something about to CAVE]

[We should have a paragraph on verification of skiplists, and on verification of the TS queue/stack]

> Below is related work from the SAS paper

Much previous work has been devoted to the *manual* verification of linearizability for concurrent programs. Examples include [25, 37]. In [30], O'Hearn *et al.* define a *hindsight lemma* that provides a non-constructive evidence for linearizability. The lemma is used to prove linearizability of an optimistic variant of the lazy set algorithm. Vafeiadis [39] uses forward and backward simulation relations together with history or prophecy variables to prove linearizability. These approaches are manual, and without tool implementations. *Mechanical* proofs of linearizability, using interactive theorem provers, have been reported in [6, 7, 32, 31]. For instance, Colvin *et al.* [6] verify the lazy set algorithm in PVS, using a combination of forward and backward simulations.

Several techniques for verifying linearizability are based on establishing commutation properties between atomic actions inside each method. Such methods can be partly automated [12, 34] or part of automated program analysis [24].

There are several works on *automatic* verification of linearizability. In [40], Vafeiadis develops an automatic tool for proving linearizability that employs instrumentation to verify logically pure executions. However, this work can handle non-fixed LPs only for read-only methods, i.e, methods that do not modify the heap. This means that the method cannot handle algorithms like the *Elimination* queue [29], *HSY* stack [19], *CCAS* [16], *RDCSS* [16] and *HM* set [22] that we consider in this paper. In addition,

their shape abstraction is not powerful enough to handle algorithms like *Harris* set [15] and *Michael* set [26] that are also handled by our method. Chakraborty *et al.* [20] describe an "aspect-oriented" method for modular verification of concurrent queues that they use to prove linearizability of the Herlihy/Wing queue. Bouajjani et al. [5] extended this work to show that verifying linearizability for certain fixed abstract data types, including queues and stacks, is reducible to control-state reachability. We can incorporate this technique into our framework by a suitable construction of observers. The method can not be applied to sets. The most recent work of Zhu *et al.* [46] describe a tool that is applied for specific set, queue, and stack algorithms. For queue algorithms, their technique can handle queues with helping mechanism except for *HW* queue [21] which is handled by our paper. For set algorithms, the authors can only handle those that perform an optimistic contains (or lookup) operation by applying the *hindsight lemma* from [30]. Hindsight-based proofs provide only *non-constructive* evidence of linearizability. Furthermore, some algorithms (e.g., the unordered list algorithm considered in Sec. 8 of this paper) do not contain the code patterns required by the hindsight method. Algorithms with non-optimistic contains (or lookup) operation like *HM* [22], *Harris* [15] and *Michael* [26] sets cannot be verified by their technique. Vechev *et al.* [44] check linearizability with user-specified non-fixed LPs, using a tool for finite-state verification. Their method assumes a bounded number of threads, and they report state space explosion when having more than two threads. Dragoi *et al.* [11] describe a method for proving linearizability that is applicable to algorithms with non-fixed LPs. However, their method needs to rewrite the implementation so that all operations have linearization points within the rewritten code. Černý *et al* [42] show decidability of a class of programs with a bounded number of threads operating on concurrent data structures. Finally, the works [1, 4, 38] all require fixed linearization points.

We have not found any report in the literature of a verification method that is sufficiently powerful to automatically verify the class of concurrent set implementations based on sorted and non-sorted singly-linked lists having non-optimistic contains (or lookup) operations we consider. For instance the lock-free sets of *HM* [22], *Harris* [15], or *Michael* [26], or unordered set of [45],

2   Overview

This is derived from the previous overview section, which described TS-stack

In this section, we illustrate our technique by using it to prove correctness, in the sense of linearizability, of a concurrent data structure implementation which uses skiplists. We consider an implementation of a set data structure, which operates on a shared heap which represents a skiplist. Here, we consider the **WHICH ONE?** implementation in [22]. To the best of our knowledge, no existing automated verification technique has succeeded in verifying funcional correctness of concurrent skiplist algorithms. We have verified **WHICH?** algorithms automatically using fragment abstraction, as reported in Section 8.

2.1 The Skiplist Algorithm

In this section, we describe a concurrent lock-free skiplist algorithm [22]. Firstly, let us describe the definition of a skiplist structure. A skiplist is a list consisting of a number of sorted linked lists, each of which is located at a layer. Each skiplist node has a key value, one successor at each layer in which it participates, and is assigned a height. The lowest-level list is an ordered list of all stored elements. Higher-level lists serve as shortcuts into lower-level lists. A skiplist has head and tail nodes with the maximum heights at the head and tail positions respectively. The heads key value is assigned $-\infty$, and the tails key value is $+\infty$. Figure 1 shows a skiplist with maximum height of 3.
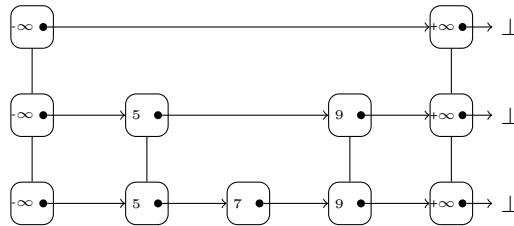

Fig. 1: A concrete shape example of 3-level skiplist

Now, let us describe an important method `find` of the algorithm. The method Figure 2 shows the skiplist find method. [Start with the most important property of the method. What does it do?] The method returns `false` if the item `x` is not found. It traverses the SkipList using two pointer variables `pred` and `curr` which start from the head and at the highest level. It then proceeds in each level down the list, filling in `preds` and `succs` nodes that are repeatedly advanced until `pred` points to a node with the largest value on that level that is strictly less than the `x`. The method repeatedly remove marked nodes from the given level as they are encountered (Lines 15 to 19) using a `compareAndSet` statement. Once an unmarked `curr` is found (Line 20), it is tested to see if its key is less than the target key. If so, `pred` is advanced to `curr`. Otherwise, `curr`s key is greater than or equal to `x`, so the current value of `pred` is the target nodes immediate predecessor. The find method breaks out of the current level search loop, saving the current values of `pred` and `curr`. It proceeds by this way until reaching the bottom level. The `add` method for adding a node with the key `k` works as follows:

The `add` method uses `find` method to check whether a node with key `k` is already in the list or not. If an unmarked node with key `k` is found in the bottom-level list, the `add` method returns `false`. If no node is found, then the next step is to try to add the new node into the list. It is performed by linking it into the bottom-level list between the `preds` and `succs` nodes returned by `find`. The method then links the node in at higher levels. The `remove` method also use `find` to determine whether an unmarked node with a matching key `k` is in the bottom-level list. If no node is found in the bottom-level list,

```
struct Node { int data; int topLayer; Node *next[]; bool marked;}
```

```
boolean find(int x,Node* preds[],Node* succs[])
1  int mLevel = 0;
2  boolean marked[MAXLEVEL] = false;
3  boolean snip;
4  Node* pred = null,curr = null,succ = null;
5  int k;
6  for(int i=0; i<maxThreads; i++)
7    Node* pred = null,curr = null,succ = null;
8    retry:
9    while (true)
10     pred = head;
11     for (int i = MAXLEVEL; i >= mLevel; i--)
12     curr = pred.next[i];
13     while (true)
14      succ = curr.next[i].get(marked);
15      while (marked[0])
16       s= CAS(pred.next[i],curr,succ,false
       false);
17       if (!s) continue retry;
18       curr = pred.next[i];
19       succ = curr.next[i].get(marked);
20      if (curr.key < x)
21       pred = curr;
22       curr = succ;
23      else break;
24    preds[i] = pred;
25    succs[i] = curr;
26  return (curr.key == x);
```

```
boolean add (int x):
1  int topLevel = randomLevel; mLevel = 0;
3  Node* preds[MAXLEVEL+1];
4  Node* succs[MAXLEVEL+1];
5  while (true);
6    boolean found = find(x,preds,succs);
7    if (found);
8     return false;
9    else
10    Node* new = new Node(x, topLevel);
11    for(int i= mLevel;level<=topLevel;i++)
12     Node* succ = succs[i];
13     new.next[i].set(succ, false);
14    Node* pred = preds[mLevel];
15    Node* succ = succs[mLevel];
16    new.next[mLevel].set(succ, false);
      if (!CAS(pred.next[mLevel],succ,new;
      ,false,false));
17     continue;
18    for (int i=mLevel+1;i<=topLevel;i++)
19     while (true);
20      pred = preds[i];
21      succ = succs[i];
22      if (CAS(pred.next[i],succ,new
22      ,false,false));
23       break;
24      find(x,preds,succs);
25  return true;
```

Fig. 2: Description of the find function of the Skip-list algorithm

or the node with a matching key is marked, the method returns false. If an unmarked node is found, then the method logically removes the associated key from the list, and prepares it for physical removal. First, starting from the topLevel, all links up to and not including the bottom-level link are marked . If the link is found to be marked the method moves on to the next-level link. Otherwise, the marking attempt must be repeated. Once all levels but the bottom one have been marked, the method marks the bottom levels next reference. Before returning true, the `find` method is called again. This call physically removes all links to the node it is searching for if that node is already logically removed.

2.2 Specifying the Correctness Criterion of Linearizability

In our verification, we establish that the skiplist algorithm of Figure 2 is correct in the sense that it is a linearizable implementation of a set data structure. Linearizability intuitively states that each operation on the data structure can be considered as being performed atomically at some point (called the *linearization point (LP)*) between its invocation and return [21].

We specify linearizability by extending of the technique of *observers* [1], which can be extended in different ways [5, 20, 2]. In the case of the skiplist algorithm, LPs can be associated to fixed statements in the code. The user then instruments these statements so that they also announce the corresponding operation on the data structure. For instance, [Quy: Please insert a text of how some LP is inserted into the code of Figure 2]. Having instrumented methods at LPs, we then consider an arbitrary concurrent program consisting of an arbitrary collection of threads, each of which executes some method call. We must check that the concurrent execution of such a program generates (through its instrumentation) a sequence of operations which satisfies the semantics of the set data structure. This check is performed by an *observer*, which monitors the sequence of operations that is announced by the instrumentation, and report when it violates the semantics of the set data structure.
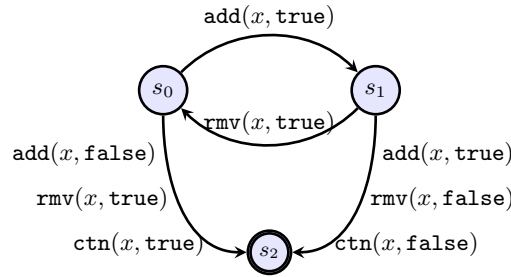
Fig. 3: Set observer.

Observers are finite automata extended with a finite set of *registers* that assume values in $\mathbb{Z}$, which are nondeterministically initialized with arbitrary values, which never change during a run of the observer. The observer accepts a trace if, for *some* initial values of the registers, the trace can be processed in such a way that an accepting state is reached. In other words, the observer is defined in such a way that it accepts precisely those traces that do *not* belong to the behavior of the data structure. Fig. 3 depicts an observer that accepts the sequences of operations that do *not* conform to the semantics of a set data structure.

To verify that no execution of the program may cause the observer to accept, we form as in the automata-theoretic approach [41] (adapted in [1]), the cross-product of the program and the corresponding observer, where the observer synchronizes with the program on the operations that are announced at LPs. This reduces the problem of checking linearizability to the problem of checking that, in the cross-product, the observer cannot reach an accepting state.

We note that linearizability of stacks and queues can be verified without any user-supplied instrumentation. For stacks and queues, linearizability can be precisely specified by observers that process the sequence of call and return actions of methods, instead of the user-supplied LPs [5, 20]. We use this technique for sets and stacks in Section XXX.

2.3 Verification by Fragment Abstraction

In the actual verification, we must compute a symbolic representation of an invariant that is satisfied by all reachable configurations of the cross-product of the program and an observer. The verification must address the challenges of an unbounded domain of data values, an unbounded number of concurrently executing threads, and an unbounded heap. For this, we have developed a novel shape representation, called *fragment abstraction*, which can also be combined with data abstraction and thread abstraction. Let us illustrate how fragment abstraction applies to the skiplist algorithm.
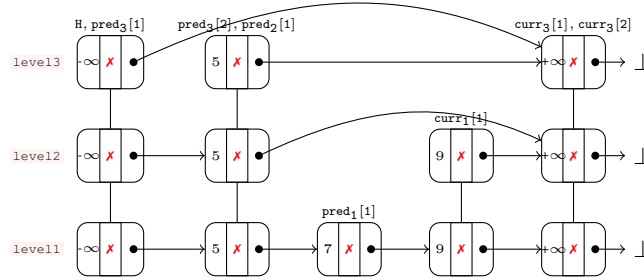
Fig. 4: A concrete shape of 3-level skiplist with two threads

Figure **??** shows an example state of the heap of the skiplist algorithm. The heap consists of a set of singly linked lists (SLLs), each of which is accessed from a pointer in the array pools[maxThreads] in a configuration when it is accessed concurrently by three threads th_1, th_2, and th_3. The heap consists of three SLLs accessed from the three pointers pools[1], pools[2], and pools[3] respectively. Each heap cell is shown with the values of its fields, using the layout shown to the right in Figure **??**. In addition, each cell is labeled by the pointer variables that point to it. We use lvar[i] to denote the local variable lvar of thread th_i.

In the heap state of Figure **??**, thread th_1 is trying to push a new node with data value 4, pointed by its local variable new, having reached line 3. Thread th_3 has just called the push method. Thread th_2 has reached line 12 in the execution of the pop method, and has just assigned youngest to the first node in the list pointed to by pools[3] which is not logically removed (in this case it is the last node of that list). Not shown in Figure **??** is the configuration of the observer. In this case, the observer is the one in Figure **??**, which has two registers x_1 and x_2, which are assigned the values 4 and 2, respectively.
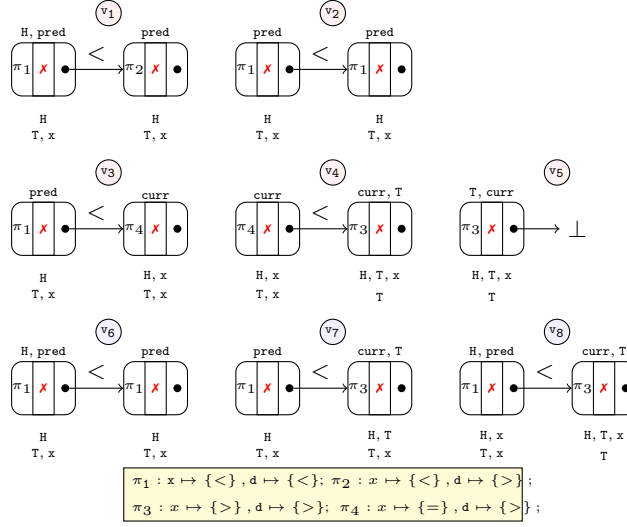
Fig. 5: Fragment abstraction of the skiplist algorithm

Our verification technique is based on a combination of a thread abstraction, a data abstraction, and a shape abstraction.

- Our thread abstraction adapts the thread-modular approach by representing only the view of single, but arbitrary, thread th. Such a view consists of (i) the local state of thread th, (ii) the part of the heap that is accessible to thread th via its local pointer variables or via global pointer variables, and (iii) the state of the observer.
- A natural data abstraction is applied to the non-pointer local variables of the thread, the non-pointer fields of each fragment, as well as the observer registers. For our skiplist algorithm, fields that range over small finite domains are represented with their concrete values, whereas the fields that range over the domain of data values, as well as the observer registers, are represented by constraints over their relative ordering (wrp. to the order $<$).
- The part of the heap that is accessible to a thread th is represented by a set of *fragments*. Each fragment is an abstraction of a pair of nodes in the heap that are connected by a pointer. The abstraction represents
  - the contents of the data fields in the pair of nodes (under the empoyed data abstraction),
  - which pointer variables (either global or local to th) point to the nodes, and
  - global reachability information, which expresses how each node in the pair can reach to and be reached from (by following a chain of pointers) a finite set of *globally significant* heap cells. A cell is globally significant if it is pointed to by a global pointer variable or if it triggers some significant behavior in a thread that reaches it: in the [WHICH?] skiplist algorithm, this happens for nodes whose key field has the same value as the register of the set observer.

In our fragment abstraction, a set of fragments represents the set of heap structures in which each pair of pointer-connected nodes is represented by some fragment in

the set. Inuitively, a set of fragments describes the set of heaps that can be formed by "piecing together" fragments in the set. This "piecing together" must be both locally consistent (appending only fragments that agree on their common node), and globally consistent (respecting the global reachability information). By construction, our fragment abstraction is finitary, since there is a bounded set of tags.

Let a *local symbolic configuration* be an abstraction of the program counter and local data variables of an arbitrary thread th. Our symbolic representation of a set of program configurations consists of a mapping from a set of local symbolic configurations, which maps each local symbolic configuration in its domain to a set of fragments. A global configuration satisfies a symbolic representation Ψ if the view of each thread th satisfies some local symbolic configuration in the domain of Ψ, which is mapped to a set of fragments, which represents the heap that is accessible to th.

Quy: After this, you must provide a nice example
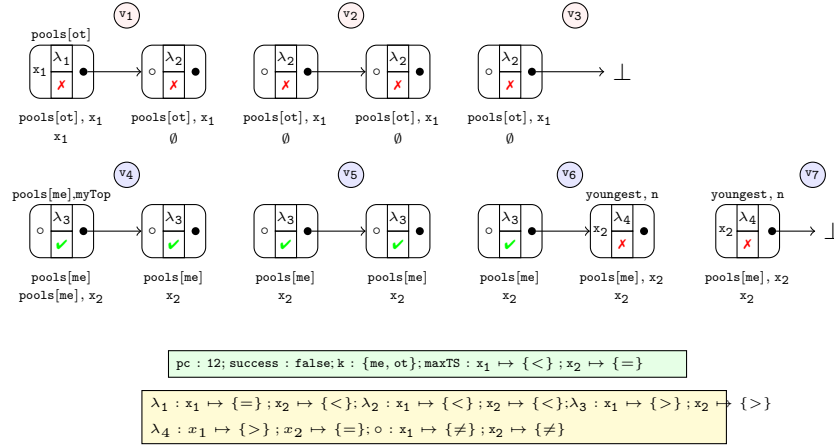
Fig. 6: Fragment abstraction

Figure 6 shows a set of fragments that is satisfied wrp. to th_2 by the configuration in Figure **??**. There are 7 fragments, named $v_1, \ldots, v_7$. Two of these (v_3 and v_7) consist of a tag that points to $\bot$, and the other consist of a pair of pointer-connected tags. Consider the tag which occurs in fragment v_7; it is the bottom-rightmost tag. This tag is an abstraction of the bottom-rightmost heap cell in Figure **??**, using the same layout for fields as Figure **??**. The different non-pointer fields are represented as follows.

- The data field of the tag (to the left) abstracts the data value 2 to the set of observer registers with that value: in this case x_2.
- The ts field (at the top) abstracts the timer value 15 to the possible relations with ts-fields of heap cells with the same data value as each observer registers. Recall that observer registers x_1 and x_2 have values 4 and 2, respectively. There are three heap cells with data field value 4, all with a ts value less than 15. There is one

heap cell with `data` field value 2, having `ts` value 15. Consequently, the abstraction of the `ts` field maps x_1 to $\{>\}$ and x_2 to $\{=\}$: this is shown as the mapping λ_4 in Figure 6.

– The `mark` field assumes values from a small finite domain and is represented precisely as in concrete heap cells

Above the top, the tag contains the thread-local and global pointer variables that point to the cell, in this case `youngest` and `n`. At the bottom of the tag, the first row contains the global variables pointing to cells from which the cell can be reached, in this case `pools`[3], as well as observer registers whose value is equal to the `data` field of a cell from which the cell can be reached, in this case x_2 (since the cell itself has the same data value as x_2). The second row contains dual information: now for cells that can be reached from the cell itself (this is again x_2).

> Fix the issue with `pools`[3]

Each cell in the heap state of Figure **??** now satisfies some tag in Figure **??**. Moreoever, each pair of pointer-connected cells (where the pointed-to "cell" can also be $\bot$) satisfies some fragment in Figure **??** in the obvious way. Conversely, the set of fragments in Figure **??** represents the set of heaps in which each pair of pointer-connected cells satisfies one of its fragments. For instance, the list pointed to by `pools`[3] is represented by the sequence of fragments $v_4 v_5 v_6 v_7$.

In order to obtain a complete representation of reachable program configurations, we must also represent the local states of a thread. This is done in a standard manner, by applying the same data abstraction as for heap cells. For instance, the local state of thread th_2 corresponding to Figure **??** is represented by a *local symbolic configuration* that contains the values of the program counter and variable `success`, abstracts the value of `k` into the set $\{me, ot\}$ and applies the timestamp abstraction to `maxTS`.

In the verification, we must compute a symbolic representation that is satisfied by all reachable program configurations (recall that program configurations include the state of the observer). This invariant is obtained by an abstract-interpretation-based fixpoint procedure, which starts from a representation of the set of initial configurations, and thereafter repeatedly performs postcondition computations that extend the symbolic representation by the effect of any execution step of the program, until convergence. This procedure is presented in Section 4.1.

3 Concurrent Data Structure Implementations

In this section, we introduce our representation of concurrent data structure implementations, we define the correctness criterion of linearizability, we introduce observers and how to use them for specifying linearizability.

3.1 Concurrent Data Structure Implementations

We begin by introducing (sequential) data structures. A *data structure* DS is a pair $\langle \mathbb{D}, \mathbb{M} \rangle$, where $\mathbb{D}$ is a (possibly infinite) *data domain* and $\mathbb{M}$ is an alphabet of *method names*. An *operation op* is of the form $m(d^{in}, d^{out})$ where $m \in \mathbb{M}$ is a method name

and d^{in} are the *input* resp. *output* values, each of which is either in $\mathbb{D}$ or in some fixed finite domain (such as the booleans). For some method names, the input or output value is absent from the operation. A *trace* of DS is a sequence of operations. The (sequential) semantics of a data structure DS is given by a set $[\![DS]\!]$ of allowed traces, called the *legal traces* of DS.

Quy: replace the below by an example of a set trace.

For example, for the `Stack` data structure, the method names are `push` and `pop`. The operation `push(3)`, where 3 is an input value, pushes the value 3, whereas `pop(4)`, where 4 is an output value, pops the value 4.

A *concurrent data structure implementation* operates on a shared state consisting of shared global variables and a shared heap. It assigns, to each method name, a method which performs operations on the shared state. Each data structure implementation also comes with an initialization method, named `init`, which initializes its shared state.

We assume that all global variables are pointer variables. Heap cells have a fixed set $\mathcal{F}$ of fields, namely data fields that assume values in $\mathbb{D}$ or $\mathbb{F}$, and possibly lock fields. We use the term $\mathbb{D}$-field for a data field that assumes values in $\mathbb{D}$, and the terms $\mathbb{F}$-field and lock field with analogous meaning. Furthermore, each cell has one or several named pointer fields. For instance, in data structure implementations based on singly linked lists each heap cell has a pointer field named `next`; in implementations based on skiplists there is an array of pointer fields named `next[k]` where `k` ranges from 1 to the maximum level of the skiplist.

Each method declares local variables and a method body. The set of local variables include the input parameter of the method and the program counter `pc`. The global variables can be accessed by all threads, whereas local variables can be accessed only by the thread which is invoking the corresponding method. Variables are either pointer variables (to heap cells) or data variables, assuming values from $\mathbb{D}$ or from some finite set $\mathbb{F}$ that includes the boolean values. The body is built in the standard way from atomic commands, using standard control flow constructs (sequential composition, selection, and loop constructs). Method execution is terminated by executing a `return` command, which may return a value. Atomic commands include assignments between data variables, pointer variables, or fields of cells pointed to by a pointer variable. The command `new Node()` allocates a new structure of type `Node` on the heap, and returns a reference to it. The compare-and-swap command `CAS(&a,b,c)` atomically compares the values of `a` and `b`. If equal, it assigns the value of `c` to `a` and returns `true`, otherwise, it leaves `a` unchanged and returns `false`. We assume a memory management mechanism, which automatically collects garbage, and ensures that a new cell is fresh, i.e., has not been used before; this avoids the so-called ABA problem (e.g., [28]).

We define a *program* (over a concurrent data structure) to consist of an arbitrary number of concurrently executing threads, each of which executes a method that performs an operation on the data structure. We assume concurrent execution according to sequentially consistent memory model. We assume that the data structure has been initialized by the `init` method prior to the start of program execution.

3.2 Linearizability

In a concurrent data structure implementation, we represent the calling of a method by a *call action* $\mathtt{call_o}\ \mathtt{m}\left(d^{in}\right)$, and the return of a method by a *return action* $\mathtt{ret_o}\ \mathtt{m}\left(d^{out}\right)$, where $\mathtt{o} \in \mathbb{N}$ is an *action identifier*, which links the call and return of each method invocation. A *history* h is a sequence of actions such that (i) different occurrences of return actions have different action identifiers, and (ii) for each return action a_2 in h there is a unique *matching* call action a_1 with the same action identifier and method name, which occurs before a_2 in h. A call action which does not match any return action in h is said to be *pending*. A history without pending call actions is said to be *complete*. A *completed extension* of h is a complete history h' obtained from h by appending (at the end) zero or more return actions that are matched by pending call actions in h, and thereafter removing the call actions that are still pending. For action identifiers $\mathtt{o_1}, \mathtt{o_2}$, we write $\mathtt{o_1} \preceq_\mathtt{h} \mathtt{o_2}$ to denote that the return action with identifier $\mathtt{o_1}$ occurs before the call action with identifier $\mathtt{o_2}$ in h. A complete history is *sequential* if it is of the form $a_1 a_1' a_2 a_2' \cdots a_n a_n'$ where a_i' is the matching action of a_i for all $i : 1 \le i \le n$, i.e., each call action is immediately followed by the matching return action. We identify a sequential history of the above form with the corresponding trace $op_1 op_2 \cdots op_n$ where $op_i = \mathtt{m}(d_i^{in}, d_i^{out})$, $a_i = \mathtt{call_{o_i}}\ \mathtt{m}\left(d_i^{in}\right)$, and $a_i = \mathtt{ret_{o_i}}\ \mathtt{m}\left(d_i^{out}\right)$, i.e., we merge each call action together with the matching return action into one operation. A complete history h' is a *linearization* of h if (i) h' is a permutation of h, (ii) h' is sequential, and (iii) $\mathtt{o_1} \preceq_{\mathtt{h}'} \mathtt{o_2}$ if $\mathtt{o_1} \preceq_\mathtt{h} \mathtt{o_2}$ for each pair of action identifiers $\mathtt{o_1}$ and $\mathtt{o_2}$. A sequential history h' is *valid* wrt. DS if the corresponding trace is in $[\![\mathtt{DS}]\!]$. We say that h is *linearizable* wrt. DS if there is a completed extension of h, which has a linearization that is valid wrt. DS. We say that a program $\mathcal{P}$ is linearizable wrt. DS if, in each possible execution, the sequence of call and return actions is *linearizable* wrt. DS.

3.3 Specification by Observers

To verify correctness of a data structure implementation, we must verify that any history, i.e., sequence of call and return actions, of any program execution satisfies the linearizability criterion. It can be shown [IN WHICH CITATION?] that this is equivalent to requiring that each operation on the data structure can be considered as being performed atomically at some point (called the *linearization point (LP)*) between its invocation and return. To check linearizability of set implementations, the user first instruments each method so that it generates a corresponding operation precisely when the method executes its LP, using the technique of linearization policies [2]. In the case of the skiplist algorithm, this instrumentation simply consists of associating LPs to fixed statements in the code. [Quy: Please illustrate here how this is done for the skiplist] Thereafter, it should be checked that the concurrent execution of any program generates (through its instrumentation) a sequence of operations which satisfies the semantics of the data structure. This check is performed by an *observer*, which monitors the sequence of operations that is announced by the instrumentation, and report when it violates the semantics of the set data structure.

Formally, an observer $\mathcal{O}$ is a tuple $\left\langle S^\mathcal{O}, s_{\mathtt{init}}^\mathcal{O}, \mathtt{X}^\mathcal{O}, \Delta^\mathcal{O}, s_{\mathtt{acc}}^\mathcal{O} \right\rangle$ where $S^\mathcal{O}$ is a finite set of *observer states* including the *initial state* $s_{\mathtt{init}}^\mathcal{O}$ and the *accepting state* $s_{\mathtt{acc}}^\mathcal{O}$, a

finite set $X^{\mathcal{O}}$ of *registers*, and $\Delta^{\mathcal{O}}$ is a finite set of *transitions*. Transitions are of the form $\langle s_1, \mathtt{m}(d^{in}, d^{out}), s_2 \rangle$, $\mathtt{m} \in \mathbb{M}$ is a method name and x^{in} and x^{out} are either registers or constants, i.e., transitions are labeled by operations whose input or output data may be parameterized on registers. The observer processes a sequence of operations one operation at a time. If there is a transition, whose label (after replacing registers by their values) matches the operation, such a transition is performed. If there is no such transition, the observer remains in its current state. The observer accepts a sequence if it can be processed in such a way that an accepting state is reached. The observer is defined in such a way that it accepts precisely those sequences that are *not* in $[\![\mathtt{DS}]\!]$. Fig. 3 depicts an observer for the set data structure.

For stacks and queues, it was recently established [5, 20] that the set of linearizable histories (i.e., sequences of call and return actions) can be exactly specified by an observer. Thus, we check linearizability for queue and stack implementations without any user-supplied instrumentation, by using an observer which accepts precisely the set of linearizable histories. We illustrate this in Section XXX.

> It remains to take care of the criterion that the call of pop already has a return value

3.4 Formal Semantics

For preciseness, we outline how we formalize the semantics of programs and observers. We assume a program $\mathcal{P}$ and a specification given by an observer $\mathcal{O}$. We assume that each thread $\mathtt{th}$ executes one method denoted $\mathtt{Method}(\mathtt{th})$.

For a function $f : A \mapsto B$ from a set A to a set B, we use $f[a_1 \leftarrow b_1, \ldots, a_n \leftarrow b_n]$ to denote the function f' such that $f'(a_i) = b_i$ and $f'(a) = f(a)$ if $a \notin \{a_1, \ldots, a_n\}$.

Heaps. A *heap (state)* is a tuple $\mathcal{H} = \langle \mathbb{C}, \mathtt{ptr}_1, \ldots, \mathtt{ptr}_k, \mathtt{Val}^{\mathtt{gl}}, \mathtt{Val}^{\mathbb{C}} \rangle$, where (i) $\mathbb{C}$ is a finite set of cells, including the two special cells $\mathtt{null}$ and $\bot$ (dangling); we define $\mathbb{C}^- = \mathbb{C} \setminus \{\mathtt{null}, \bot\}$, (ii) for each pointer field $\mathtt{ptr}_i$ there is a total function $\mathtt{ptr}_i : \mathbb{C}^- \to \mathbb{C}$ that defines where that pointer field points, (iii) $\mathtt{Val}^{\mathtt{gl}} : X^{\mathtt{gl}} \to \mathbb{C}$ maps the global (pointer) variables $X^{\mathtt{gl}}$ to their values, and (iv) $\mathtt{Val}^{\mathbb{C}} : \mathbb{C} \times \mathcal{F} \to \mathbb{F} \cup \mathbb{D}$ maps data and lock fields of each cell to their values. We let $\mathcal{H}_{\mathtt{init}}$ denote the initial heap produced by the $\mathtt{init}$ method.

Threads. A *local state* $\mathtt{loc}$ of a thread $\mathtt{th}$ wrt. a heap $\mathcal{H}$ defines the values of its local variables, including the program counter $\mathtt{pc}$ and the input parameter for the method executed by $\mathtt{th}$. In addition, there is the special initial state $\mathtt{idle}$, and terminated state $\mathtt{term}$. We formalize the behavior of a thread $\mathtt{th}$ by a labeled transition relation $\to_{\mathtt{th}}$ on pairs $\langle \mathtt{loc}, \mathcal{H} \rangle$ consisting of a local state $\mathtt{loc}$ and a heap $\mathcal{H}$, with transitions of the form: $\langle \mathtt{loc}, \mathcal{H} \rangle \xrightarrow{\lambda}_{\mathtt{th}} \langle \mathtt{loc}', \mathcal{H}' \rangle$, which denote execution of method statements, labeled by λ. The label λ is empty, except when the executed statement is annotated as a linearization point, in which case it is labeled by the corresponding operation (of form $\mathtt{m}(d^{in}, d^{out})$).

Programs. A *configuration* of a program $\mathcal{P}$ is a tuple $\langle \mathtt{T}, \mathtt{LOC}, \mathcal{H} \rangle$ where $\mathtt{T}$ is a set of threads, $\mathcal{H}$ is a heap, and $\mathtt{LOC}$ maps each thread $\mathtt{th} \in \mathtt{T}$ to its local state

LOC (th) wrt. $\mathcal{H}$. The initial configuration $c_{\texttt{init}}^{\mathcal{P}}$ is the pair $\langle \texttt{LOC}_{\texttt{init}}, \mathcal{H}_{\texttt{init}} \rangle$, where $\texttt{LOC}_{\texttt{init}}$ (th) = idle for each th $\in$ T, A program $\mathcal{P}$ induces a transition relation $\to_{\mathcal{P}}$ where each step corresponds to one move of a single thread. I.e., there is a transition of form $\langle \texttt{T}, \texttt{LOC}, \mathcal{H} \rangle \xrightarrow{\lambda}_{\mathcal{P}} \langle \texttt{T}, \texttt{LOC}[\texttt{th} \leftarrow \texttt{loc}'], \mathcal{H}' \rangle$ whenever the transition relation $\to_{\texttt{th}}$ has a transition $\langle \texttt{loc}, \mathcal{H} \rangle \xrightarrow{\lambda}_{\texttt{th}} \langle \texttt{loc}', \mathcal{H}' \rangle$, where the label λ is either a call or return action or the empty label.

Cross-Product of Program and Observer We use $\mathcal{S} = \mathcal{P} \otimes \mathcal{O}$ to denote the cross-product obtained by running $\mathcal{P}$ and $\mathcal{O}$ together. The initial configuration of $\mathcal{S}$ is $\langle c_{\texttt{init}}^{\mathcal{P}}, s_{\texttt{init}}^{\mathcal{O}} \rangle$. Transitions of $\mathcal{S}$ are of the form $\langle c^{\mathcal{P}}, s \rangle, \to_{\mathcal{S}}, \langle c^{\mathcal{P}'}, s' \rangle$, obtained from a transition $c^{\mathcal{P}} \xrightarrow{\lambda}_{\mathcal{P}} c^{\mathcal{P}'}$ of the program with some (possibly empty) label λ, where the observer makes a transition if it can perform a matching transition $\langle s, \texttt{ret m} (l), s' \rangle$. The verification problem is now to check that $\mathcal{S}$ cannot reach a configuration $\langle c^{\mathcal{P}}, s_{\texttt{acc}}^{\mathcal{O}} \rangle$ with an accepting observer state.

> Here, we need to state a the theorem that we have reduced linearizability checking to reachability

(a)   (b)

(c)
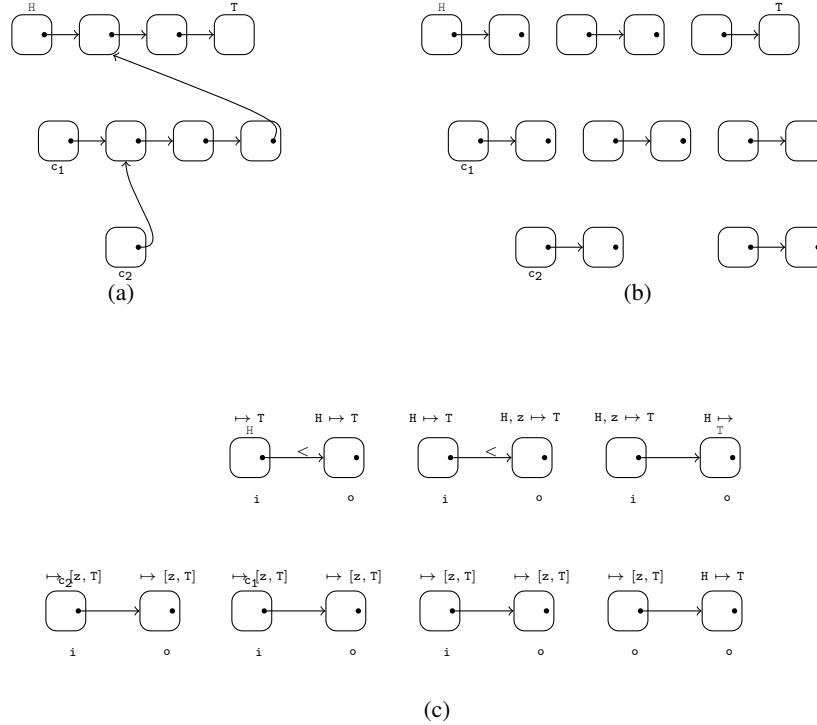
Fig. 7: Example of Fragment Abstraction

4 Fragment Abstraction: Singly Linked Lists

In the previous section, we reduced the problem of verifying linearizability to the problem of verifying that, in any execution of the cross-product of a program and an observer, the observer cannot reach an accepting state. In this section, we describe our technique for performing this verification. The main novel contribution is our fragment abstraction for representing the possible heap configurations of a concurrent program. In the following subsection, we describe our symbolic representation and verification technique, using fragment abstraction, for programs that operate on singly-linked lists (SLLs). This representation is also the basis for our representation for programs operating on skiplists, described in Subsection **??** and programs operating on arrays of SLLs, in Subsection **??**.

We consider a program with global variables X^{gl} and thread-local variables X^{loc}. We assume that all global variables are pointer variables.

4.1 Fragment Abstraction for Singly-Linked List-Based Programs

[Say that for now, we ignore timers?] [Introduce notation for the set of local $\mathbb{D}$-variables?. Maybe define which threads we talk about? For now, we skip timestamps.]

In this subsection, we present our fragment abstraction for programs that operate on singly-linked lists. This means that each cell has exactly one pointer field, named `next`. In order to simplify the presentation in this section, we assume that each heap cell has at most one $\mathbb{D}$-field, named `data`. Our verification technique is based on a combination of a thread abstraction, a shape abstraction, and a data abstraction.

Assume some global configuration c and thread `th`. We say that a heap cell $\mathbb{c}$ is *accessible* to a thread `th` in c, if $\mathbb{c}$ is reachable (directly or via sequence of `next`-pointers) from a global pointer variable or local pointer variable of `th`. For a pointer variable p, let a p-cell be the cell to which p points. For an observer register x_i, define a x_i-*cell* to be a heap cell whose `data` field has the same value as x_i. A heap cell is *globally significant* if it is either a p-cell for a global pointer variable p, or a x_i-cell for some observer register x_i.

Thread Abstraction. Our thread abstraction adapts the thread-modular approach by representing only the view of a single, but arbitrary, thread `th`. Such a view consists of (i) the local state of thread `th`, (ii) the state of the observer, (iii) the cells of the heap that are accessible to thread `th`, and (iv) a predicate `private` over heap cells accessible to `th`, which is true for cells that are not accessible to any other thread.

Shape Abstraction. We represent the part of the heap that is accessible to `th` by a set of *fragments*. A fragment consists of a pair of heap cells that are connected by a pointer, in which each of the two heap cell is labeled by (i) the pointer variables (either global or local to `th`) that point to it, and (ii) globally significant cells that can

reach it and be reached from it (by following a chain of `next`-pointers); these cells are represented by the corresponding global pointer variables and observer registers. A set of fragments represents the set of heap views in which each pair of pointer-connected nodes is represented by some fragment in the set. Inuitively, a set of fragments describes the set of heap views that can be formed by "piecing together" fragments in the set. This "piecing together" must be both locally consistent (connecting only fragments that agree on their common node), and globally consistent (respecting the reachability information wrp. to globally significant cells).

Data Abstraction. We apply a natural data abstraction to the local state of a thread, the state of the observer, and to each fragment. This abstraction represents small finite domains exactly, and abstracts a set of data values in $\mathbb{D}$ by their relative ordering. The data abstraction can be conveniently represented by defining an abstract domain for each concrete domain of data values.

- For small concrete domains (including that of the program counter), the abstract domain is the same as the concrete one.
- For locks, the abstract domain is $\{me, other, free\}$, where me means that the lock is held by `th`, where $other$ means that the lock is held by some other thread, and $free$ means that the lock is free.
- For the concrete domain $\mathbb{D}$ of data values, the abstract domain is the set of mappings from local variables ranging over $\mathbb{D}$ and from observer registers to the set $\{<, =, >\}$. An element in the abstract domain represents a concrete data value $\mathtt{d}$ if it maps each local variable and observer register with value $\mathtt{d}'$ to a set which includes a relation $\sim$ such that $\mathtt{d} \sim \mathtt{d}'$.

We are now ready to define our abstract symbolic representation.

Define a *local symbolic configuration* as a mapping from local variables (including the program counter) to their corresponding abstract domains. We use $c \models^{loc}_{\mathtt{th}} \sigma$ to denote that in the global configuration c, the local configuration of thread `th` satisfies the local symbolic configuration σ, defined in the natural way.

Let us next define the combination of our data abstraction and fragment abstraction. Define a *tag* as a tuple $\mathtt{tag} = \langle \mathtt{dabs}, \mathtt{pvars}, \mathtt{reachfrom}, \mathtt{reachto}, \mathtt{private} \rangle$, where

- `dabs` is a mapping from non-pointer fields to their corresponding abstract domains,
- `pvars` is a set of (global or local) pointer variables,
- `reachfrom` and `reachto` are sets of global pointer variables and observer registers, and
- `private` is a boolean value.

For a heap cell $\mathbb{c}$ that is accessible to thread `th` in a configuration c, and a tag $\mathtt{tag} = \langle \mathtt{dabs}, \mathtt{pvars}, \mathtt{reachfrom}, \mathtt{reachto}, \mathtt{private} \rangle$, we write $\mathbb{c} \lhd_{\mathtt{th}} \mathtt{tag}$ to denote that

- `dabs` is an abstraction of the concrete values of the non-pointer fields of $\mathbb{c}$,
- `pvars` is the set of global pointer variables and local pointer variables of `th` that point to $\mathbb{c}$,
- `reachfrom` is the set of (i) global pointer variables from which $\mathbb{c}$ is reachable via a (possibly empty) sequence of `next` pointers, and (ii) observer registers $\mathtt{x}_i$ such that $\mathbb{c}$ is reachable from some $\mathtt{x}_i$-cell.

- `reachto` is the set of (i) global pointer variables that point to a cell which is reachable from $\mathbb{c}$, and (ii) observer registers $\mathbb{x}_i$ such that some $\mathbb{x}_i$-cell is reachable from $\mathbb{c}$.
- `private` is `true` only if $\mathbb{c}$ is not accessible to any other thread than `th` [This needs fixing].

Definition 1 (SLL-fragment). *An* SLL-fragment v *(or just fragment) is a triple of of form* $\langle \mathtt{v.i}, \mathtt{v.o}, \mathtt{v.}\phi \rangle$, *of form* $\langle \mathtt{v.i}, \mathtt{null} \rangle$, *or of form* $\langle \mathtt{v.i}, \bot \rangle$, *where* v.i *and* v.o *are tags, and where* v.ϕ *is a subset of* $\{<, =, >\}$.

For a cell $\mathbb{c}$ which is accessible to thread `th`, and a fragment v of form $\langle \mathtt{v.i}, \mathtt{v.o}, \mathtt{v.}\phi \rangle$, we write $\mathbb{c} \lhd_{\mathtt{th}} \mathtt{v}$ to denote that $\mathbb{c} \lhd_{\mathtt{th}} \mathtt{v.i}$, and that the `next` field of $\mathbb{c}$ points to a cell $\mathbb{c}'$ such that $\mathbb{c}' \lhd_{\mathtt{th}} \mathtt{v.o}$ and $\mathbb{c}.\mathtt{data} \sim \mathbb{c}'.\mathtt{data}$ for some $\sim \in \mathtt{v.}\phi$. For a fragment of form $\mathtt{v} = \langle \mathtt{v.i}, \mathtt{null} \rangle$, let $\mathbb{c} \lhd \mathtt{v}$ denote that $\mathbb{c} \lhd \mathtt{v.i}$ and $\mathtt{next}(\mathbb{c}) = \mathtt{null}$. Define $\mathbb{c} \lhd \mathtt{v}$ for v of form $\langle \mathtt{v.i}, \bot \rangle$ analogously.

Let V be a set of fragments. A global configuration satisfies a set V of fragments wrp. to `th`, denoted $c \models_{\mathtt{th}}^{heap} V$, if for any cell $\mathbb{c}$ that is accessible to `th`, there is a fragment $\mathtt{v} \in V$ such that $\mathbb{c} \lhd_{\mathtt{th}} \mathtt{v}$. For a local symbolic configuration σ and set V of fragments, we write $c \models_{\mathtt{th}} \langle \sigma, V \rangle$ to denote that that $c \models_{\mathtt{th}}^{loc} \sigma$ and $c \models_{\mathtt{th}}^{heap} V$.

A *symbolic representation* Ψ is a mapping from local symbolic configurations to sets of fragments. A configuration c of a program satisfies a symbolic representation if for each thread `th`, the domain of Ψ contains a local symbolic configuration σ such that $c \models_{\mathtt{th}} \langle \sigma, \Psi(\sigma) \rangle$.

Skip the following example. Do we need one?

Example: Let us show an example of how a singly linked list (SLL) is split into a set of fragments. Fig. 7(a) shows an example of a concrete shape of a singly linked list. Each cell contains the values of `val`, `mark`, and `lock` from top to bottom, where ✔ denotes `true`, and ✘ denotes `false` (or `free` for `lock`) and the value of `val` is denoted by a pair of the observer register z and a subset of $\{<, =, >\}$. There are two threads 1 and 2 with two local variables c_1 and c_2. There are two global variables H and T pointing to the head and tail of the list. The observer register z has value 8. Fig. 7(b) shows the result of splitting the list in Fig. 7(a) into fragments where each heap fragment is a small list of two nodes. Fig. 7(c) shows the abstraction of fragments in Fig. 7(b) where for each cell $\mathbb{c}$, the value of *val* is abstracted to a subset in $\{< \mathtt{z}, = \mathtt{z}, > \mathtt{z}\}$, the reachability relation between $\mathbb{c}$ and global variables and observer registers is abstracted to the predicate X $\mapsto$ Y where X, Y are sets of global variables and observer registers, i or o states that $\mathbb{c}$ is an input or output cell.

Computing Postconditions In the verification, we must compute a symbolic representation that is satisfied by all reachable program configurations. This invariant is obtained by an abstract-interpretation-based fixpoint procedure, which starts from a representation of the set of initial configurations, and thereafter repeatedly performs postcondition computations that extend the symbolic representation by the effect of any execution step of the program, until convergence. In this subsection, we describe the symbolic postcondition computation, which is the key step in this procedure.

The symbolic postcondition computation must ensure that the symbolic representation of the reachable configurations of a program is closed under execution of a statement by some thread. More precisely, assume that a global configuration c satisfies a symbolic representation Ψ. Let th be an arbitrary thread. Assume that there is a local symbolic configuration $\sigma \in Dom(\Psi)$ such that $c \models_{\text{th}} \langle \sigma, \Psi(\sigma) \rangle$. We must ensure that this property still hols after any execution of a statement by some thread. In the thread-modular approach, we must consider two cases:

– *Local Steps:* The thread th itself executes some statement, which may change its local state and the state of the heap. In this case, we compute a local symbolic configuration σ' and set V' such that the resulting configuration c' satisfies $c' \models_{\text{th}} \langle \sigma', V' \rangle$, and (if necessary) extend Ψ so that $\sigma' \in Dom(\Psi)$ and $V' \in \Psi(\sigma')$.
– *Interference Steps:* Another thread th_2, which satisfies a local symbolic configuration σ_2 in $Dom(\Psi)$ with $c \models_{\text{th}_2} \langle \sigma_2, \Psi(\sigma_2) \rangle$ performs a computation step, which affects the state of the heap in such a way that makes it necessary to extend $\Psi(\sigma)$. We must then compute a set V' of fragments such that the resulting configuration c' satisfies $c' \models_{\text{th}}^{heap} V'$ and make sure that $V' \in \Psi(\sigma)$. To do this, we first combine the the local symbolic configurations σ and σ_2 and the sets of fragments $\Psi(\sigma)$ and $\Psi(\sigma_2)$, using an operation, called *intersection*, into a joint local symbolic configuration of th and th_2 and a set $V_{1,2}$ of fragments that represents the cells accessible to either th or th_2. We thereafter symbolically compute the postcondition of the statement execution of th_2, as in the local case, finally project back the set of fragments onto th in the natural way, to obtain V'.

In the following, we first describe the symbolic postcondition computation for local steps, and thereafter the intersection operation.

Symbolic Postcondition Computation for Local Steps Let th be an arbitrary thread, and assume that $\sigma \in Dom(\Psi)$ with For each statement that th can execute in a configuration c with $c \models_{\text{th}} \langle \sigma, \Psi(\sigma) \rangle$, we must compute a local symbolic configuration σ' and a set V' of fragments such that such that the resulting configuration c' satisfies $c' \models_{\text{th}} \langle \sigma', V' \rangle$. This computation has do be done differently for each statement. For statements that do not affect the heap or pointer variables, this computation is standard, and affects only the local symbolic configuration and data abstraction part of fragments. We therefore here describe how to compute the effect of statements that update pointer variables or the heap, since these are the most interesting cases.

The main difficulty in the postcondition computation is to update the reachability information provided in the fields reachfrom and reachto in each tag of a fragment. For instance, considerthat a statement g := p, which assigns the value of a local pointer variable p to a global pointer variable g. In the postcondition computation, we must for each fragment determine how to update the field reachfrom in its tags, and in particular whether g should be in this set after the statement (the same problem occurs for the set reachto), If v would have been a global variable, this information could be obtained by checking whether v is in the set before the operation. However, since the reachfrom field does not include local variables, we start the postcondition computation by computing a number of transitive-closure-like relations between fragments, which will allow to determine whether g should be in the reachfrom field after the statement with

rather good accuracy. Note that if our procedure can not determine whether g should be in a `reachfrom` field, then it generates fragments for both possibilities.

First, we say that two tags $\texttt{tag} = \langle \texttt{dabs}, \texttt{pvars}, \texttt{reachfrom}, \texttt{reachto}, \texttt{private} \rangle$ and $\texttt{tag}' = \langle \texttt{dabs}', \texttt{pvars}', \texttt{reachfrom}', \texttt{reachto}', \texttt{private}' \rangle$ are *consistent* if if there is some concrete valuation of non-pointer fields represented by both `dabs` and `dabs`′, and if $\texttt{pvars} = \texttt{pvars}'$, $\texttt{reachfrom} = \texttt{reachfrom}'$, $\texttt{reachto} = \texttt{reachto}'$, and $\texttt{private} = \texttt{private}'$. Intuitively, `tag` and `tag`′ are consistent if there can exist a cell c accessible to `th` with $c \lhd_{\texttt{th}} \texttt{tag}$ and $c \lhd_{\texttt{th}} \texttt{tag}'$.

> To Quy: Can you really require that the data abstractions must be the same?

Let v_1 and v_2 be two fragments in a set V of fragments.

- Let $v_1 \hookrightarrow_V v_2$ denote that $v_1.\texttt{o}$ and $v_2.\texttt{i}$ are consistent.
- Let $v_1 \leftrightarrow_V v_2$ denote that $v_1.\texttt{o} = v_2.\texttt{o}$ are consistent, and that either $v_1.\texttt{i}.\texttt{pvars} \cap v_2.\texttt{i}.\texttt{pvars} = \emptyset$ or that the global variables in $v_1.\texttt{i}.\texttt{reachfrom}$ are disjoint from those in $v_2.\texttt{i}.\texttt{reachfrom}$.

> Question: Is this correct?

Intuitively, $v_1 \hookrightarrow_V v_2$ denotes that it is possible that $\texttt{next}(c_1) = c_2$ for some cells with $c_1 \lhd v_1$ and $c_2 \lhd v_2$. Intuitively, $v_1 \leftrightarrow_V v_2$ denotes that it is possible that $\texttt{next}(c_1) = \texttt{next}(c_2)$. for different cells c_1 and c_2 with $c_1 \lhd v_1$ and $c_2 \lhd v_2$. Note that the above definitions also work for the cases that the output tag is `null` or $\bot$.

We use the above relations to define several derived relations:

- Let $\overset{+}{\hookrightarrow}_V$ denote the transitive closure of $\hookrightarrow_V$, and $\overset{*}{\hookrightarrow}_V$ the reflexive transitive closure of $\hookrightarrow_V$.
- Let $v_1 \overset{**}{\leftrightarrow}_V v_2$ denote that there are $v_1', v_2' \in V$ with $v_1' \leftrightarrow_V v_2'$ such that $v_1 \overset{*}{\hookrightarrow}_V v_1'$ and $v_2 \overset{*}{\hookrightarrow}_V v_2'$.
- Let $v_1 \overset{*+}{\leftrightarrow}_V v_2$ denote that there are $v_1', v_2' \in V$ with $v_1' \leftrightarrow_V v_2'$ such that $v_1 \overset{*}{\hookrightarrow}_V v_1'$ and $v_2 \overset{+}{\hookrightarrow}_V v_2'$.
- Let $v_1 \overset{*\circ}{\leftrightarrow}_V v_2$ denote that is a $v_1' \in V$ with $v_1' \leftrightarrow_V v_2$ such that $v_1 \overset{*}{\hookrightarrow}_V v_1'$.
- Let $v_1 \overset{++}{\leftrightarrow}_V v_2$ denote that there are $v_1', v_2' \in V$ with $v_1' \leftrightarrow_V v_2'$ such that $v_1 \overset{+}{\hookrightarrow}_V v_1'$ and $v_2 \overset{+}{\hookrightarrow}_V v_2'$.
- Let $v_1 \overset{+*}{\leftrightarrow}_V v_2$ denote that there are $v_1', v_2' \in V$ with $v_1' \leftrightarrow_V v_2'$ such that $v_1 \overset{+}{\hookrightarrow}_V v_1'$ and $v_2 \overset{*}{\hookrightarrow}_V v_2'$.
- Let $v_1 \overset{+\circ}{\leftrightarrow}_V v_2$ denote that is a $v_1' \in V$ with $v_1' \leftrightarrow_V v_2$ such that $v_1 \overset{+}{\hookrightarrow}_V v_1'$.

We say that v_1 and v_2 are *compatible* if $v_x \overset{*}{\hookrightarrow} v_y$ or $v_y \overset{*}{\hookrightarrow} v_x$ or $v_x \overset{**}{\leftrightarrow} v_y$. Intuitively, this means that v_1 and v_2 can be satisfied by two cells in the same heap state. Figure 8 illustrates the above relations for a heap state with 13 heap cells. The figure shows 4 fragments that are satisfied by heap cells, as denoted by green boxes, and how the relationship between heap cells is reflect by relations between the corresponding fragments.

We can now describe how to perform the symbolic postcondition computations for statements that assign to a pointer variable.
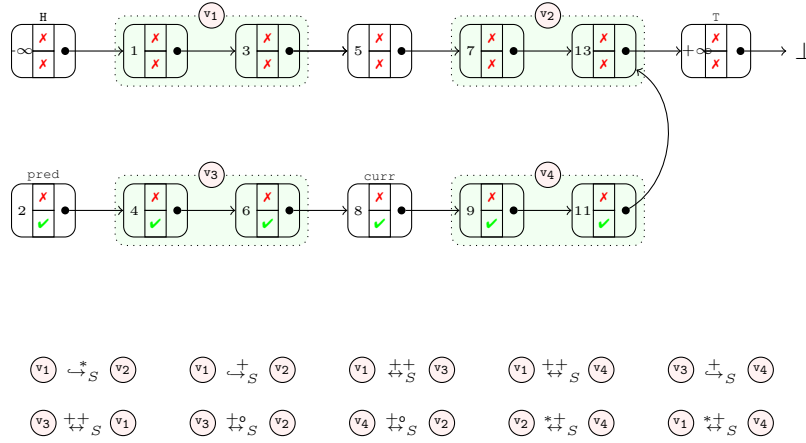
Consider a statement of form $x := y$, where x and y are global or local (to thread th) pointer variables. We must compute a set V' of fragments which are satisfied by the configuration after the statement. We must ensure that any cell c which is accessible to th after the statement satisfies some fragement in V'. The cell c must satisfy some fragment v in V, and must be in the same heap state as the cell pointed to by y. This means that we can make a case analysis on the possible relationships between v and any fragment $v_y \in V$ such that $y \in v_y.i.pvars$. Thus, for each fragment $v_y \in V$ such that $y \in v_y.i.pvars$ we let V' contain the fragments obtained by the following transformations on fragments in V.

1. First, for the fragment v_y itself, we let V' contain v', which is the same as v_y, except that
   – $v'.i.pvars = v_y.i.pvars \cup \{x\}$ and $v'.o.pvars = v.o.pvars \setminus \{x\}$
   and furthermore, if x is a global variable, then
   – $v'.i.reachto = v.i.reachto \cup \{x\}$ and $v'.i.reachfrom = v.i.reachfrom \cup \{x\}$,
   – $v'.o.reachfrom = v.o.reachfrom \cup \{x\}$ and $v'.o.reachto = v.o.reachto \setminus \{x\}$.

2. for each fragment v with $v \hookrightarrow_V v_y$, let V' contain v' which is same as v except that
   – $v'.i.pvars = v.i.pvars \setminus \{x\}$,
   – $v'.o.pvars = v.o.pvars \cup \{x\}$,
   – $v'.i.reachfrom = v.i.reachfrom \setminus \{x\}$ if x is a global variable,
   – $v'.i.reachto = v.i.reachto \cup \{x\}$ if x is a global variable,
   – $v'.o.reachfrom = v.o.reachfrom \cup \{x\}$ if x is a global variable,
   – $v'.o.reachto = v.o.reachto \cup \{x\}$ if x is a global variable,

3. We perform analogous inclusions for fragments v with $v \overset{+}{\hookrightarrow}_V v_y$, $v_y \overset{*}{\hookrightarrow}_V v$, $v_y \overset{*+}{\leftrightarrow}_V v$, and $v_y \overset{*o}{\leftrightarrow}_V v$. For space reasons, we show only the case of $v_y \overset{*+}{\leftrightarrow}_V v$, in which case we let V' contain v' which is same as v except that

- $\mathtt{v}'.\mathtt{i.pvars} = \mathtt{v.i.pvars} \setminus \{x\}$,
- $\mathtt{v}'.\mathtt{o.pvars} = \mathtt{v.o.pvars} \setminus \{x\}$,
- $\mathtt{v}'.\mathtt{i.reachfrom} = \mathtt{v.i.reachfrom} \setminus \{x\}$ if x is a global variable,
- $\mathtt{v}'.\mathtt{i.reachto} = \mathtt{v.i.reachto} \setminus \{x\}$ if x is a global variable,
- $\mathtt{v}'.\mathtt{o.reachfrom} = \mathtt{v.o.reachfrom} \setminus \{x\}$ if x is a global variable,
- $\mathtt{v}'.\mathtt{o.reachto} = \mathtt{v.o.reachto} \setminus \{x\}$ if x is a global variable,

The statement $\mathtt{x} := \mathtt{y.next}$ is handled rather similarly to the preceding case. Let us therefore describe the computation for statements of the form $\mathtt{x.next} := \mathtt{y}$. This is the most difficult statement, since it is a destructive update of the heap. The statement affects reachability relations for both $\mathtt{x}$ and $\mathtt{y}$. This means that we can make a case analysis on how a fragment in V is related to some pair of compatible fragments $\mathtt{v}_x$, $\mathtt{v}_y$ in V such that $\mathtt{x} \in \mathtt{v}_x.\mathtt{i.pvars}$, $\mathtt{y} \in \mathtt{v}_y.\mathtt{i.pvars}$. Thus, for each pair of compatible fragments $\mathtt{v}_x$, $\mathtt{v}_y$ in V such that $\mathtt{x} \in \mathtt{v}_x.\mathtt{i.pvars}$, $\mathtt{y} \in \mathtt{v}_y.\mathtt{i.pvars}$, we let V' contain the fragments obtained by the following transformations on fragments in V.

1. First, let V' contain a new fragment $\mathtt{v}_{new}$ of form $\langle \mathtt{v}_{new}.\mathtt{i}, \mathtt{v}_{new}.\mathtt{o}, \mathtt{v}_{new}.\phi \rangle$ $\mathtt{v}_{new}.\mathtt{i.tag} = \mathtt{v}_x.\mathtt{i.tag}$ and $\mathtt{v}_{new}.\mathtt{o.tag} = \mathtt{v}_y.\mathtt{i.tag}$ except that $\mathtt{v}_{new}.\mathtt{o.reachfrom} = \mathtt{v}_y.\mathtt{i.reachfrom} \cup \mathtt{v}_x.\mathtt{i.reachfrom}$, and $\mathtt{v}_{new}.\phi = \{<, =, >\}$. Thereafter, we add all possible fragments that can result from a transformation of some fragment $\mathtt{v}$ which is in $\mathtt{v}$. This is done by an exhaustive case analysis on the possible relationship between $\mathtt{v}$, $\mathtt{v}_x$ and $\mathtt{v}_y$. Let us consider an interesting case, in which $\mathtt{v}_x \overset{*}{\hookrightarrow}_V \mathtt{v}$ and either $\mathtt{v} \overset{+}{\hookrightarrow}_V \mathtt{v}_y$ or $\mathtt{v}_y \overset{*+}{\leftrightarrow} \mathtt{v}$. In this case,

   (a) for each subset $\mathtt{regset}$ of observer registers in $\mathtt{v.i.reachfrom} \cap \mathtt{v}_x.\mathtt{i.reachfrom}$, we first create a fragment $\mathtt{v}'$ which is same as $\mathtt{v}$, except that $\mathtt{v}'.\mathtt{i.reachfrom} = (\mathtt{v.i.reachfrom} \setminus \mathtt{v}_x.\mathtt{i.reachfrom}) \cup \mathtt{regset}$.

   (b) Thereafter, for each set $\mathtt{regset}'$ of observer registers in $\mathtt{v}'.\mathtt{o.reachfrom} \cap \mathtt{v}_x.\mathtt{i.reachfrom}$, we let V' contain a fragment $\mathtt{v}''$ which is same as $\mathtt{v}'$, except that $\mathtt{v}''.\mathtt{o.reachfrom} = (\mathtt{v}'.\mathtt{o.reachfrom} \setminus \mathtt{v}_x.\mathtt{i.reachfrom}) \cup \mathtt{regset}'$.

> We should include an argument why the last case above is correct. Quy, could you produce one?

Symbolic Postcondition Computation for Interference Steps. The key step in this computation is to form the intersection of two sets of fragments V_1 and V_2, such that for the configuration c we have $c \models^{heap}_{\mathtt{th}_i} V_i$ for $i = 1, 2$. In order to distinguish between local variables of $\mathtt{th}_1$ and $\mathtt{th}_2$, we assume that local variable x of thread $\mathtt{th}_i$ is named as $\mathtt{x}[\mathtt{i}]$. We must compute a set V which for each heap cell accessible to either $\mathtt{th}_1$ or $\mathtt{th}_2$, the set V must contain a fragment $\mathtt{v}$ with $\mathbb{c} \lhd_{1,2} \mathtt{v}$. [This notation to be defined] There are here two possibilities.

- If $\mathbb{c}$ is accessible to both $\mathtt{th}_1$ and $\mathtt{th}_2$, then there are fragments $\mathtt{v}_1 \in V_1$ and $\mathtt{v}_2 \in V_2$ such that $\mathbb{c} \lhd_1 \mathtt{v}_1$ and $\mathbb{c} \lhd_2 \mathtt{v}_2$. We use the notation $\mathtt{v}_1 \sqcap \mathtt{v}_2$ to denote a set of views such that whenever $\mathbb{c} \lhd_1 \mathtt{v}_1$ and $\mathbb{c} \lhd_2 \mathtt{v}_2$ then $\mathbb{c} \lhd_{1,2} \mathtt{v}$ for some $\mathtt{v} \in (\mathtt{v}_1 \sqcap \mathtt{v}_2)$.
- If $\mathbb{c}$ is accessible to only one of $\mathtt{th}_1$ and $\mathtt{th}_2$, say $\mathtt{th}_1$, then V should contain some fragment $\mathtt{v}_1 \in V_1$ with $\mathbb{c} \lhd_{1,2} \mathtt{v}_1$ [Check that we need not change $\mathtt{v}_1$]

For a fragment v, define `v.i.greachfrom` as the set of global variables in `v.i.reachfrom`. Define `v.i.greachto`, `v.o.greachfrom`, `v.o.greachto`, `v.i.gpvars`, and `v.o.gpvars` analogously. Define `v.o.gtag` as the tuple $\langle$`v.o.gpvars, v.o.dabs, v.o.greachfrom, v.o.greachto, v.o.private`$\rangle$.

Let us now describe how to compute $v_1 \sqcap v_2$ for two views $v_1 \in V_1$ and $v_2 \in V_2$. Firstly, we consider the case where both v_1 and v_2 have size 2. Let us consider some different cases. They all take into account the observation that if a cell $\mathbb{c}$ satisfies $\mathbb{c} \lhd_1 v_1$ and $\mathbb{c} \lhd_2 v_2$, then the information about global variables in v_1 and v_2 must coincide.

- if v_1.i.greachfrom $\neq \emptyset$ and v_2.i.greachfrom $\neq \emptyset$ then the global information in v_1 and v_2 must coincide. We hence obtain:
  - if v_1.i.gtag $= v_2$.i.gtag and v_1.o.gtag $= v_2$.o.gtag then $v_1 \sqcap v_2 = \{v_{12}\}$ where v_{12} is identical to v_1 except that
    * v_{12}.i.pvars $= v_1$.i.pvars $\cup v_2$.i.pvars
    * v_{12}.o.pvars $= v_1$.o.pvars $\cup v_2$.o.pvars
    * v_{12}.i.reachfrom $= v_1$.i.reachfrom $\cup v_2$.i.reachfrom
    * v_{12}.o.reachfrom $= v_1$.o.reachfrom $\cup v_2$.o.reachfrom

- if v_1.i.greachfrom $= \emptyset$, v_2.i.greachfrom $= \emptyset$, v_1.o.greachfrom $\neq \emptyset$ and v_2.o.greachfrom $\neq \emptyset$ then
  - if v_1.o.gtag $= v_2$.o.gtag, v_1.i.private $=$ false and v_2.i.private $=$ false then $v_1 \sqcap v_2 = \{v_1', v_2', v_{12}\}$ where
    * v_1' is same as v_1 except that
      · v_1'.o.pvars $= v_1$.o.pvars $\cup v_2$.o.pvars
      · v_1'.o.reachfrom $= v_1$.o.reachfrom $\cup v_2$.o.reachfrom
    * v_2' is same as v_2 except that
      · v_2'.o.pvars $= v_1$.o.pvars $\cup v_2$.o.pvars
      · v_2'.o.reachfrom $= v_1$.o.reachfrom $\cup v_2$.o.reachfrom
    * v_{12} is as in the previous case. [to Quy: I added this, is it correct?]
  - if v_1.o.gtag $= v_2$.o.gtag and v_1.i.private $=$ true or v_2.i.private $=$ true then $v_1 \sqcap v_2 = \{v_1', v_2'\}$ where v_1' and v_2' are as above.
- if v_1.i.greachfrom $= \emptyset$, v_2.i.greachfrom $= \emptyset$, v_1.o.greachfrom $= \emptyset$ and v_2.o.greachfrom $= \emptyset$ then
  - if $\text{gtag}(v_1, o) = \text{gtag}(v_2, o)$, v_1.i.private $= false$, v_1.o.private $= false$, v_1.o.private $= false$ and v_2.o.private $= false$ then $v_1 \sqcap v_2 = \{v_1, v_2, v_1', v_2', v_{12}\}$

- if $\texttt{gtag}(v_1, o) = \texttt{gtag}(v_2, o)$, ($v_1.\texttt{i.private} = true$ or $v_2.\texttt{i.private} = true$) and $v_1.\texttt{o.private} = false$ and $v_2.\texttt{o.private} = false$ then $v_1 \sqcap v_2 = \{v_1, v_2, v_1', v_2'\}$
- if $\texttt{gtag}(v_1, o) = \texttt{gtag}(v_2, o)$ and ($v_1.\texttt{o.private} = true$ or $v_1.\texttt{o.private} = true$) then $v_1 \sqcap v_2 = \{v_1, v_2\}$
- if $\texttt{gtag}(v_1, o) \neq \texttt{gtag}(v_2, o)$ then $v_1 \sqcap v_2 = \{v_1, v_2\}$

5 Fragment Abstraction for Skip-Lists

– In the fragment abstraction, $\texttt{tag}$ is define exactly same as $\texttt{tag}$ in SLL abstraction where $\texttt{reachfrom}$ and $\texttt{reachto}$ is defined based on the main level of skip-list. It means that we do not keep the reachability information in higher levels.
– Same as timestamp stacks and queues, in skip-list we keep the main level and abstract all the higher levels. It means that we do not distinguish the differences between high levels. Hence, we have two types of fragments including main level fragments and higher level fragments which are defined same as SLL fragments.
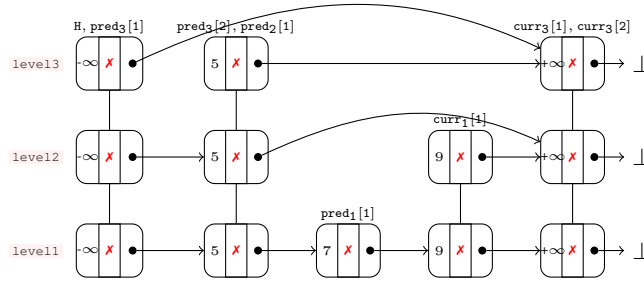
Fig. 9: A concrete shape of 3-level skipl-list with two threads

5.1 Abstract transformers for skip-lists

Let us show how to perform the abstract transformer for skip-list programs on the set of fragments V depending on the particular statement. We consider fragments of size 2 where $\texttt{I}_{\texttt{inp}} = \{i\}$, $\texttt{I}_{\texttt{out}} = \{o\}$, and $\texttt{next}(i) = o$ and fragments of size 1 where $\texttt{I}_{\texttt{inp}} = \{i\}$, $\texttt{I}_{\texttt{out}} = \emptyset$, and $\texttt{next}(i) = \texttt{null}$ or $\texttt{next}(i) = \bot$. For each fragment v, let $v.\texttt{level} \in \{1, 2\}$ be the level of v.
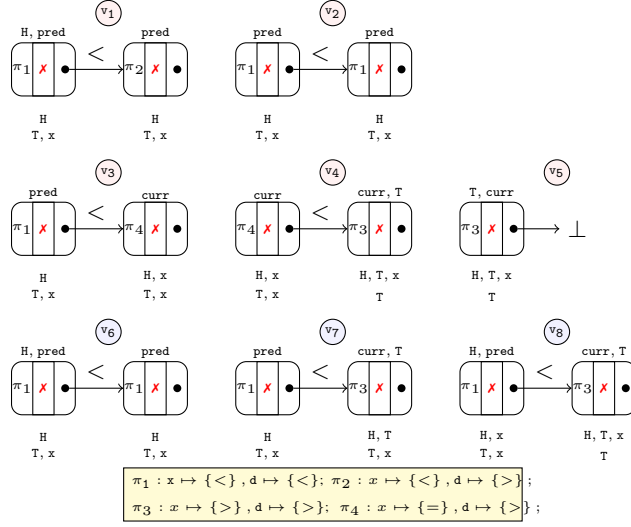
Fig. 10: skipl-list fragments [ANSWER of Quy: i am working with this figure]

Local Abstract Transformers: First, let us show the abstract transformer on the set of fragment V in the fragment of the concurrent thread. Let V_1 be set of fragments of level 1 in V, V_2 be set of fragments of level 2 in V. For each program statement, let V_{post} be the set of fragments after executing the statement. Let V_{post} be initialized as the empty set. Let R be the set of pairs of fragments. Intuitively, in each element in R, the second fragment is the transformation of the first fragment. Let R be initialized as the empty set.

- $x := y$: The transformer is performed as follows: For each fragment $v_y \in V_1$ where $y \in v_y.\mathtt{i.pvars}$,
  1. for each fragment $v \in V_1$ where $v \hookrightarrow_V v_y$, create v' which is same as v except that
     - $v'.\mathtt{i.pvars} = v.\mathtt{i.pvars} \setminus \{x\}$,
     - $v'.\mathtt{o.pvars} = v.\mathtt{o.pvars} \cup \{x\}$,
     - if
       $gvar of x$ is a global variable
       * $v'.\mathtt{i.reachfrom} = v.\mathtt{i.reachfrom} \setminus \{x\}$,
       * $v'.\mathtt{i.reachto} = v.\mathtt{i.reachto} \cup \{x\}$,
       * $v'.\mathtt{o.reachfrom} = v.\mathtt{o.reachfrom} \cup \{x\}$,
       * $v'.\mathtt{o.reachto} = v.\mathtt{o.reachto} \cup \{x\}$,
     then add v' to V_{post}, and (v, v') to R
  2. for each fragment $v \in V_1$ where $v \xrightarrow{+}_V v_y$, create v' which is same as v except that
     - $v'.\mathtt{i.pvars} = v.\mathtt{i.pvars} \setminus \{x\}$,
     - $v'.\mathtt{o.pvars} = v.\mathtt{o.pvars} \setminus \{x\}$,

- if

  $gvarofx$ is a global variable
    * `v'.i.reachfrom = v.i.reachfrom \ {x}`,
    * `v'.i.reachto = v.i.reachto ∪ {x}`,
    * `v'.o.reachfrom = v.o.reachfrom \ {x}`,
    * `v'.o.reachto = v.o.reachto ∪ {x}`,

  then add `v'` to V_{post}, and (v, v') to `R`

3. for each fragment $v \in V_1$ where $v_y \overset{*}{\hookrightarrow}_v v$, create `v'` which is same as `v` except that
   - `v'.i.pvars = v.i.pvars \ {x}`,
   - `v'.o.pvars = v.o.pvars \ {x}`,
   - if

     $gvarofx$ is a global variable
       * `v'.i.reachfrom = v.i.reachfrom ∪ {x}`,
       * `v'.i.reachto = v.i.reachto \ {x}`,
       * `v'.o.reachfrom = v.o.reachfrom ∪ {x}`,
       * `v'.o.reachto = v.o.reachto \ {x}`,

   then add `v'` to V_{post}, and (v, v') to V'

4. for each fragment `v` where $v_y \overset{*+}{\leftrightarrow}_v v$, create `v'` which is same as `v` except that
   - `v'.i.pvars = v.i.pvars \ {x}`,
   - `v'.o.pvars = v.o.pvars \ {x}`,
   - if

     $gvarofx$ is a global variable
       * `v'.i.reachfrom = v.i.reachfrom \ {x}`,
       * `v'.i.reachto = v.i.reachto \ {x}`,
       * `v'.o.reachfrom = v.o.reachfrom \ {x}`,
       * `v'.o.reachto = v.o.reachto \ {x}`,

   then add `v'` to V_{post}, and (v, v') to V'

5. for each fragment $v \in V_1$ where $v_y \overset{*\circ}{\leftrightarrow}_v v$, create `v'` which is same as `v` except that
   - `v'.i.pvars = v.i.pvars \ {x}`,
   - `v'.o.pvars = v.o.pvars \ {x}`,
   - if

     $gvarofx$ is a global variable
       * `v'.i.reachfrom = v.i.reachfrom \ {x}`,
       * `v'.i.reachto = v.i.reachto \ {x}`,
       * `v'.o.reachfrom = v.o.reachfrom ∪ {x}`,
       * `v'.o.reachto = v.o.reachto \ {x}`,

   then add `v'` to V_{post}, and (v, v') to V'

6. create `v'` which is same as v_y except that
   - `v'.i.pvars = v.i.pvars ∪ {x}`,
   - `v'.o.pvars = v.o.pvars \ {x}`,
   - if

     $gvarofx$ is a global variable
       * `v'.i.reachto = v.i.reachto ∪ {x}`,

$*$ $\mathtt{v'.i.reachfrom} = \mathtt{v.i.reachfrom} \cup \{x\}$,

$*$ $\mathtt{v'.o.reachfrom} = \mathtt{v.o.reachfrom} \cup \{x\}$,

$*$ $\mathtt{v'.o.reachto} = \mathtt{v.o.reachto} \setminus \{x\}$,

then add $\mathtt{v'}$ to V_{post}, and $(\mathtt{v}, \mathtt{v'})$ to V'

7. for each fragment $\mathtt{v} \in V_2$ we do as follows. For each $(\mathtt{v_1}, \mathtt{v'_1})$, $(\mathtt{v_2}, \mathtt{v'_2}) \in \mathtt{R}$, for each pair of indices $\mathtt{i_1}, \mathtt{i_2}$ such that $\mathtt{i_1} \in \{\mathtt{v_1.i}, \mathtt{v_1.o}\}$, $\mathtt{i_2} \in \{\mathtt{v_2.i}, \mathtt{v_2.o}\}$, $\mathtt{tag(v,i)} = \mathtt{tag(v_1, i_1)}$, and $\mathtt{tag(v,o)} = \mathtt{tag(v_2, i_2)}$. Create $\mathtt{v'}$ which is same as $\mathtt{v}$ except that

- $\mathtt{v'.i.pvars} = \mathtt{v'_1}.i_1.vars$,
- $\mathtt{v'.o.pvars} = \mathtt{v'_2}.i_2.vars$,
- $\mathtt{v'.i.reachfrom} = \mathtt{v'_1}.i_1.reachfrom$,
- $\mathtt{v'.o.reachfrom} = \mathtt{v'_2}.i_2.reachfrom$,
- $\mathtt{v'.i.reachto} = \mathtt{v'_1}.i_1.reachto$,
- $\mathtt{v'.o.reachto} = \mathtt{v'_2}.i_2.reachto$,

then add $\mathtt{v'}$ to V_{post}.

- $\mathtt{x := y.next1}$: The local abstract transformer is quite similar to the previous case with slightly differences. For each fragment $\mathtt{v_y} \in \mathtt{S}$ where $\mathtt{y} \in \mathtt{vars(i)}$,

1. for each fragment $\mathtt{v} \in V_1$ where $\mathtt{v} \overset{*}{\hookrightarrow}_\mathtt{v} \mathtt{v_y}$, create $\mathtt{v'}$ which is same as $\mathtt{v}$ except that

- $\mathtt{v'.i.pvars} = \mathtt{v.i.pvars} \setminus \{x\}$,
- $\mathtt{v'.o.pvars} = \mathtt{v.o.pvars} \setminus \{x\}$,
- if x is a global variable

$*$ $\mathtt{v'.i.reachfrom} = \mathtt{v.i.reachfrom} \setminus \{x\}$,

$*$ $\mathtt{v'.i.reachto} = \mathtt{v.i.reachto} \cup \{x\}$,

$*$ $\mathtt{v'.o.reachfrom} = \mathtt{v.o.reachfrom} \setminus \{x\}$,

$*$ $\mathtt{v'.o.reachto} = \mathtt{v.o.reachto} \cup \{x\}$,

then add $\mathtt{v'}$ to V_{post}, and $(\mathtt{v}, \mathtt{v'})$ to $\mathtt{R}$

2. for each fragment $\mathtt{v} \in V_1$ where $\mathtt{v_y} \hookrightarrow_\mathtt{v} \mathtt{v}$, create $\mathtt{v'}$ which is same as $\mathtt{v}$ then

- $\mathtt{v'.i.pvars} = \mathtt{v.i.pvars} \cup \{x\}$,
- $\mathtt{v'.o.pvars} = \mathtt{v.o.pvars} \setminus \{x\}$,
- if x is a global variable

$*$ $\mathtt{v'.i.reachfrom} = \mathtt{v.i.reachfrom} \cup \{x\}$,

$*$ $\mathtt{v'.o.reachto} = \mathtt{v.o.reachto} \cup \{x\}$,

$*$ $\mathtt{v'.o.reachfrom} = \mathtt{v.o.reachfrom} \cup \{x\}$,

$*$ $\mathtt{v'.o.reachto} = \mathtt{v.o.reachto} \setminus \{x\}$,

then add $\mathtt{v'}$ to V_{post}, and $(\mathtt{v}, \mathtt{v'})$ to $\mathtt{R}$

3. for each fragment $\mathtt{v} \in V_1$ where $\mathtt{v_y} \leftrightarrow_\mathtt{v} \mathtt{v}$, create $\mathtt{v'}$ which is same as $\mathtt{v}$ except that

- $\mathtt{v'.i.pvars} = \mathtt{v.i.pvars} \setminus \{x\}$,
- $\mathtt{v'.o.pvars} = \mathtt{v.o.pvars} \cup \{x\}$,
- if x is a global variable

$*$ $\mathtt{v'.i.reachfrom} = \mathtt{v.i.reachfrom} \setminus \{x\}$,

$*$ $\mathtt{v'.i.reachto} = \mathtt{v.i.reachto} \cup \{x\}$,

$*$ $\mathtt{v'.o.reachfrom} = \mathtt{v.o.reachfrom} \cup \{x\}$,

$*$ $\mathtt{v'.o.reachto} = \mathtt{v.o.reachto} \cup \{x\}$,

then add $\mathtt{v'}$ to V_{post}, and $(\mathtt{v}, \mathtt{v'})$ to $\mathtt{R}$

4. for each fragment $v \in V_1$ where $v_y \xrightarrow{+}_V v$, create v' which is same as v except that
   - $v'.\mathtt{i.pvars} = v.\mathtt{i.pvars} \setminus \{x\}$,
   - $v'.\mathtt{o.pvars} = v.\mathtt{o.pvars} \setminus \{x\}$,
   - if x is a global variable
     * $v'.\mathtt{i.reachfrom} = v.\mathtt{i.reachfrom} \cup \{x\}$,
     * $v'.\mathtt{i.reachto} = v.\mathtt{i.reachto} \setminus \{x\}$,
     * $v'.\mathtt{o.reachfrom} = v.\mathtt{o.reachfrom} \cup \{x\}$,
     * $v'.\mathtt{o.reachto} = v.\mathtt{o.reachto} \setminus \{x\}$,

   then add v' to V_{post}, and (v, v') to R

5. for each fragment $v \in V_1$ where $v_y \xleftrightarrow{*\circ}_V v$, create v' which is same as v except that
   - $v'.\mathtt{i.pvars} = v.\mathtt{i.pvars} \setminus \{x\}$,
   - $v'.\mathtt{o.pvars} = v.\mathtt{o.pvars} \setminus \{x\}$,
   - if x is a global variable
     * $v'.\mathtt{i.reachfrom} = v.\mathtt{i.reachfrom} \setminus \{x\}$,
     * $v'.\mathtt{i.reachto} = v.\mathtt{i.reachto} \setminus \{x\}$,
     * $v'.\mathtt{o.reachfrom} = v.\mathtt{o.reachfrom} \cup \{x\}$,
     * $v'.\mathtt{o.reachto} = v.\mathtt{o.reachto} \setminus \{x\}$,

   then add v' to V_{post}, and (v, v') to R

6. for each fragment $v \in V_1$ where $v_y \xleftrightarrow{*+}_V v$, create v' which is same as v except that
   - $v'.\mathtt{i.pvars} = v.\mathtt{i.pvars} \setminus \{x\{$,
   - $v'.\mathtt{o.pvars} = v.\mathtt{o.pvars} \setminus \{x\}$,
   - if x is a global variable
     * $v'.\mathtt{i.reachfrom} = v.\mathtt{i.reachfrom} \setminus \{x\}$,
     * $v'.\mathtt{i.reachto} = v.\mathtt{i.reachto} \setminus \{x\}$,
     * $v'.\mathtt{o.reachfrom} = v.\mathtt{o.reachfrom} \setminus \{x\}$,
     * $v'.\mathtt{o.reachto} = v.\mathtt{o.reachto} \setminus \{x\}$,

   then add v' to V_{post}, and (v, v') to R

7. for each fragment $v \in V_1$ where $v \xleftrightarrow{+\circ}_V v_y$, create v' which is same as v except that
   - $v'.\mathtt{i.pvars} = v.\mathtt{i.pvars} \setminus \{x\}$,
   - $v'.\mathtt{o.pvars} = v.\mathtt{o.pvars} \setminus \{x\}$,
   - if x is a global variable
     * $v'.\mathtt{i.reachfrom} = v.\mathtt{i.reachfrom} \setminus \{x\}$,
     * $v'.\mathtt{i.reachto} = v.\mathtt{i.reachto} \cup \{x\}$,
     * $v'.\mathtt{o.reachfrom} = v.\mathtt{o.reachfrom} \setminus \{x\}$,
     * $v'.\mathtt{o.reachto} = v.\mathtt{o.reachto} \cup \{x\}$,

   then add v' to V_{post}, and (v, v') to R

8. create v' which is same as v_y except that
   - $v'.\mathtt{i.pvars} = v.\mathtt{i.pvars} \setminus \{x\}$,
   - $v'.\mathtt{o.pvars} = v.\mathtt{o.pvars} \cup \{x\}$,
   - if x is a global variable
     * $v'.\mathtt{i.reachfrom} = v.\mathtt{i.reachfrom} \setminus \{x\}$,

* $\text{v}'.\texttt{i.reachto} = \text{v}.\texttt{i.reachto} \cup \{x\}$,
* $\text{v}'.\texttt{o.reachfrom} = \text{v}.\texttt{o.reachfrom} \cup \{x\}$,
* $\text{v}'.\texttt{o.reachto} = \text{v}.\texttt{o.reachto} \cup \{x\}$,

    then add v' to V_{post}, and (v, v') to R

9. for each fragment $\text{v} \in V_2$ we do as follows. For each $(\text{v}_1, \text{v}'_1)$, $(\text{v}_2, \text{v}'_2) \in \text{R}$, for each pair of indices i_1, i_2 such that $\text{i}_1 \in \{\text{v}_1.\texttt{i}, \text{v}_1.\texttt{o}\}$, $\text{i}_2 \in \{\text{v}_2.\texttt{i}, \text{v}_2.\texttt{o}\}$, $\texttt{tag}(\text{v}, \texttt{i}) = \texttt{tag}(\text{v}_1, \text{i}_1)$, and $\texttt{tag}(\text{v}, \texttt{o}) = \texttt{tag}(\text{v}_2, \text{i}_2)$. Create v' which is same as v except that
   - $\text{v}'.\texttt{i.pvars} = \text{v}'_1.i_1.vars$,
   - $\text{v}'.\texttt{o.pvars} = \text{v}'_2.i_2.vars$,
   - $\text{v}'.\texttt{i.reachfrom} = \text{v}'_1.i_1.reachfrom$,
   - $\text{v}'.\texttt{o.reachfrom} = \text{v}'_2.i_2.reachfrom$,
   - $\text{v}'.\texttt{i.reachto} = \text{v}'_1.i_1.reachto$,
   - $\text{v}'.\texttt{o.reachto} = \text{v}'_2.i_2.reachto$,

   then add v' to V_{post}.

- $\texttt{x.next1} := \texttt{y}$: The local abstract transformer is performed by several steps as follows: For each pair of fragments v_x, v_y in V_1 where $\text{x} \in \text{v}_x.\texttt{i.pvars}$, $\text{y} \in \text{v}_y.\texttt{i.pvars}$, and $\text{v}_x \xhookrightarrow{*} \text{v}_y$ or $\text{v}_y \xhookrightarrow{*} \text{v}_x$ or $\text{v}_x \xleftrightarrow{**} \text{v}_y$, let $\text{R}_1, \text{R}_2, \text{R}_3, \text{R}_4, \text{R}_5, \text{R}_6$ be initialized as R,

  1. let v_{new} be the fragment of size 2 and of level 1 where $\texttt{tag}(\text{v}_{new}, \texttt{i}) = \texttt{tag}(\text{v}_x, \texttt{i})$ and $\texttt{tag}(\text{v}_{new}, \texttt{o}) = \texttt{tag}(\text{v}_y, \texttt{i})$ except that $\text{v}_{new}.\texttt{o.reachfrom} = \text{v}_y.\texttt{i.reachfrom} \cup \text{v}_x.\texttt{i.reachfrom}$,

  2. for each fragment $\text{v} \in V_1$ where $\text{v} \xhookrightarrow{*}_V \text{v}_x$, we do as follows: For each subset $\texttt{regset}$ of observer registers in $\text{v}.\texttt{i.reachto} \cap \text{v}_x.\texttt{i.reachfrom}$
     - create v' which is same as v, except that
       * $\text{v}'.\texttt{i.reachto} = (\text{v}.\texttt{i.reachto} \cap \text{v}_x.\texttt{i.reachfrom}) \cup \texttt{reachtov}_y.\texttt{i} \cup \texttt{regset}$.
       * $\text{v}'.\texttt{o.reachto} = (\text{v}.\texttt{o.reachto} \cap \text{v}_x.\texttt{i.reachfrom}) \cup \texttt{reachtov}_y.\texttt{i} \cup \texttt{regset}$.
     - add v' to V_{post}
     - add (v, v') to R_1

  3. for each fragment $\text{v} \in V_1$ where $\text{v}_y \xhookrightarrow{*}_V \text{v}$ or $\text{v}_y = \text{v}$,
     - create v' which is same as v except that
       * $\text{v}'.\texttt{i.reachfrom} = \text{v}_x.\texttt{i.reachfrom} \cup \text{v}.\texttt{i.reachfrom}$,
       * $\text{v}'.\texttt{o.reachfrom} = \text{v}_x.\texttt{i.reachfrom} \cup \text{v}.\texttt{o.reachfrom}$,
     - add v' to V_{post},
     - add (v, v') to R_2

  4. for each fragment $\text{v} \in V_1$ where $\text{v}_x \xleftrightarrow{**}_V \text{v}$ and either $\text{v} \hookrightarrow_V \text{v}_y$ or $\text{v}_y \xleftrightarrow{*o}_V \text{v}$,
     - create v' which is same as v except that $\text{v}'.\texttt{o.reachfrom} = \text{v}_x.\texttt{i.reachfrom} \cup \text{v}.\texttt{o.reachfrom}$,
     - add v' to V_{post},
     - add (v, v') to R_3

  5. for each fragment $\text{v} \in V_1$ where $\text{v}_x \xhookrightarrow{*}_V \text{v}$ and either $\text{v} \hookrightarrow_V \text{v}_y$ or $\text{v}_y \xleftrightarrow{*o}_V \text{v}$,
     - create v' which is same as v then except that $\text{v}'.\texttt{o.reachfrom} = \text{v}_x.\texttt{i.reachfrom} \cup \text{v}.\texttt{o.reachfrom}$,

- for each subset `regset` of observer registers in $v'.\texttt{i.reachfrom} \cap v_x.\texttt{i.reachfrom}$
  - $*$ create v'' which is same as v', except that $v''.\texttt{i.reachfrom} = (v'.\texttt{i.reachfrom} \setminus v_x.\texttt{i.reachfrom}) \cup \texttt{regset}$.
  - $*$ add v'' to V_{post},
  - $*$ add (v, v'') to R_4

6. for each fragment $v \in V_1$ where $v_x \overset{**}{\leftrightarrow}_V v$ and either $v \overset{+}{\hookrightarrow}_V v_y$ or $v \overset{*+}{\leftrightarrow}_V v_y$,
   - create v' which is same as v
   - add v' to V_{post},
   - add (v, v') to R_5

7. for each fragment $v \in V_1$ where $v_x \overset{*}{\hookrightarrow}_V v$ and either $v \overset{+}{\hookrightarrow}_V v_y$ or $v_y \overset{*+}{\leftrightarrow}$ v, then for each subset `regset` of observer registers in $v.\texttt{i.reachfrom} \cap v_x.\texttt{i.reachfrom}$,
   - create v' which is same as v, except that $v'.\texttt{i.reachfrom} = (v.\texttt{i.reachfrom} \setminus v_x.\texttt{i.reachfrom}) \cup \texttt{regset}$.
   - for each set `regset'` of observer registers in $v'.\texttt{o.reachfrom} \cap v_x.\texttt{i.reachfrom}$,
     - $*$ create v'' which is same as v', except that $v''.\texttt{o.reachfrom} = (v'.\texttt{o.reachfrom} \setminus v_x.\texttt{i.reachfrom}) \cup \texttt{regset'}$.
     - $*$ add v'' to V_{post}
     - $*$ add (v, v'') to R_6

8. add $v_{\texttt{new}}$ to V_{post}

- for each fragment $v \in V_2$ then we do as follows. for each $(v_1, v_1') \in R_i$, $(v_2, v_2') \in R_j$ where $i \neq j$ and $1 \leq i, j \leq 6$. For each pair of indices i_1, i_2 such that $i_1 \in \{v_1.\texttt{i}, v_1.\texttt{o}\}$, $i_2 \in \{v_2.\texttt{i}, v_2.\texttt{o}\}$, $\texttt{tag}(v, \texttt{i}) = \texttt{tag}(v_1, i_1)$, $\texttt{tag}(v, \texttt{o}) = \texttt{tag}(v_2, i_2)$. Create v' which is same as v except that
  - $v'.\texttt{i.pvars} = v_1'.i_1.vars$,
  - $v'.\texttt{o.pvars} = v_2'.i_2.vars$,
  - $v'.\texttt{i.reachfrom} = v_1'.i_1.reachfrom$,
  - $v'.\texttt{o.reachfrom} = v_2'.i_2.reachfrom$,
  - $v'.\texttt{i.reachto} = v_1'.i_1.reachto$,
  - $v'.\texttt{o.reachto} = v_2'.i_2.reachto$,

  then add v' to V_{post}.

Fragment Intersection: Let us describe the intersection of two fragments $v_1 \in V_1$ and $v_2 \in V_2$ denoted as $v_1 \sqcap v_2$. Firstly, we consider the case where both v_1 and v_2 have size 2.

- if $v_1.\texttt{greachfrom}(i, \{1, 2\}) \neq \emptyset$ and $v_2.\texttt{greachfrom}(i, \{1, 2\}) \neq \emptyset$ then
  - if $\texttt{gtag}(v_1, i) = \texttt{gtag}(v_2, i)$ and $\texttt{gtag}(v_1, o) = \texttt{gtag}(v_2, o)$ then $v_1 \sqcap v_2 = \{v_{12}\}$ where v_{12} is same as v_1 except that, for all $l \in \{1, 2\{1, 2\}\}$
    * $v_{12}.\texttt{vars}(i) = v_1.\texttt{vars}(i) \cup v_2.\texttt{vars}(i)$
    * $v_{12}.o.\texttt{pvars} = v_1.o.\texttt{pvars} \cup v_2.o.\texttt{pvars}$
    * $v_{12}.\texttt{reachfrom}(i, l) = v_1.\texttt{reachfrom}(i, l) \cup v_2.\texttt{reachfrom}(i, l)$
    * $v_{12}.\texttt{reachfrom}(i, l) = v_1.\texttt{reachfrom}(o, l) \cup v_2.\texttt{reachfrom}(o, l)$
- if $v_1.\texttt{greachfrom}(i, \{1, 2\}) = \emptyset$, $v_2.\texttt{greachfrom}(i, \{1, 2\}) = \emptyset$, $v_1.\texttt{greachfrom}(o, \{1, 2\}) \neq \emptyset$ and $v_2.\texttt{greachfrom}(o, \{1, 2\}) \neq \emptyset$ then
  - if $\texttt{gtag}(v_1, o) = \texttt{gtag}(v_2, o)$, $v_1.\texttt{private}(i) = \texttt{false}$ and $v_2.\texttt{private}(i) = \texttt{false}$ then $v_1 \sqcap v_2 = \{v_1', v_2', v_{12}\}$ where v_1' is same as v_1 except that, for all $l \in \{1, 2\{1, 2\}\}$
    * $v_1'.o.\texttt{pvars} = v_1.o.\texttt{pvars} \cup v_2.o.\texttt{pvars}$
    * $v_1'.\texttt{reachfrom}(o, l) = v_1.\texttt{reachfrom}(o, l) \cup v_2.\texttt{reachfrom}(o, l)$

    and v_2' is same as v_2 except that
    * $v_2'.o.\texttt{pvars} = v_1.o.\texttt{pvars} \cup v_2.o.\texttt{pvars}$
    * $v_2'.\texttt{reachfrom}(o, l) = v_1.\texttt{reachfrom}(o, l) \cup v_2.\texttt{reachfrom}(o, l)$
  - if $\texttt{gtag}(v_1, o) = \texttt{gtag}(v_2, o)$ and $(v_1.\texttt{private}(i) = \texttt{true}$ or $v_2.\texttt{private}(i) = \texttt{true})$ then $v_1 \sqcap v_2 = \{v_1', v_2'\}$
- if $v_1.\texttt{greachfrom}(i, \{1, 2\}) = \emptyset$, $v_2.\texttt{greachfrom}(i, \{1, 2\}) = \emptyset$, $v_1.\texttt{greachfrom}(o, \{1, 2\}) = \emptyset$ and $v_2.\texttt{greachfrom}(o, \{1, 2\}) = \emptyset$ then
  - if $\texttt{gtag}(v_1, o) = \texttt{gtag}(v_2, o)$, $v_1.\texttt{private}(i) = \texttt{false}$, $v_1.o.\texttt{private} = \texttt{false}$, $v_1.o.\texttt{private} = \texttt{false}$ and $v_2.o.\texttt{private} = \texttt{false}$ then $v_1 \sqcap v_2 = \{v_1, v_2, v_1', v_2', v_{12}\}$
  - if $\texttt{gtag}(v_1, o) = \texttt{gtag}(v_2, o)$, $(v_1.\texttt{private}(i) = \texttt{true}$ or $v_2.\texttt{private}(i) = \texttt{true})$ and $v_1.o.\texttt{private} = \texttt{false}$ and $v_2.o.\texttt{private} = \texttt{false}$ then $v_1 \sqcap v_2 = \{v_1, v_2, v_1', v_2'\}$
  - if $\texttt{gtag}(v_1, o) = \texttt{gtag}(v_2, o)$ and $(v_1.o.\texttt{private} = \texttt{true}$ or $v_1.o.\texttt{private} = \texttt{true})$ then $v_1 \sqcap v_2 = \{v_1, v_2\}$
  - if $\texttt{gtag}(v_1, o) \neq \texttt{gtag}(v_2, o)$ then $v_1 \sqcap v_2 = \{v_1, v_2\}$

6 Timestamp Stack

7 Timestamp Abstraction

7.1 View Abstraction

For timestamp data structures we have to deal with timestamp ordering and unbound number of lists. The solutions are described as follows:

– About timestamp ordering, we add timestamp ordering information for each index of a view. The order of index `i` of view `v` is of the form `v.i.ts` $\diamond$ `x` where $\diamond \in \{<, =, >\}$ and `x` is an observer register. Intuitively, `v.i.ts` $\diamond$ `x` means that $\diamond$ is the order between the timestamp of `v.i` and timestamp of an index whose data is equal to `x`.

– To deal with the problem of unbounded number of lists. We use two kind of views which are c-views v_c in a current list and o-views v_o in other lists. Note that, current list is the list where the current thread is accessing to.

7.2 Post-computation

The post-computation is quite similar to singly-linked lists with several differences as follows: Before computing the post condition of this statement, we change all o-views in the other list to c-views and previous c-views to o-views.

7.3 Intersection

The intersection between two views are computed same as in the case of singly-linked lists with several differences as follows:

– Two views of push methods should not be intersected. The reason for it is that we do not have more concurrent pushes in a same list.

– We can intersect o-views and c-views, o-views and o-views as well as c-views and c-views

8 Experimental Results

> I am working here

Based on our framework, we have implemented a tool in OCaml, and used it for verifying concurrent algorithms (both lock-based and lock-free) including timestamps stack and queue, skip-list sets and priority queues as well as singly-linked lists algorithms (stacks, queues, sets). The experiments were performed on a desktop 2.8 GHz processor with 8GB memory. The results are presented in Fig. 11, where running times are given in seconds. All experiments start from the initial heap, and end either when the analysis reaches the fixed point or when a violation of safety properties or linearizability is detected.

Running Times. As can be seen from the table, the running times vary in the different examples. This is due to the types of shapes that are produced during the analysis. For instance, skip-lists algorithm have much longer running times. This is due to the number of pointer variables and their complicated shapes. Whereas, other algorithms produce simple shape patterns and hence they have shorter running times.

Algorithms	Time (s)
TIMESTAMPS	
TS stack [28]	176
TS queue [28]	101
SKIP-LISTS	
Lock-free skip-list [22]	1992
Optimistic skip-list [28]	500
Priority queue skip-list 1 [29]	1320
Priority queue skip-list 2 [29]	599
SINGLY-LINKED LISTS	
Treiber stack [36]	18
MS lock-free queue [28]	21
DGLM queue [10]	16
Vechev-CAS set [43]	86
Vechev-DCAS set [43]	16
Michael lock-free set [26]	178
Pessimistic set [22]	30
Optimistic set [22]	25
Lazy set [18]	34
O'Hearn set [30]	88
HM lock-free set [22]	120

Fig. 11: Experimental results for verifying concurrent programs

Error Detection In addition to establishing correctness of the original versions of the benchmark algorithms, we tested our tool with intentionally inserted bugs. For example, we emitted setting time statement in line 5 of the `push` method in TS stack algorithm. The tool, as expected, successfully detected and reported the bug.

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A   Algorithms

In this section, we show several important algorithms including timestamp stack and queue, lazy set, skip-list sets and skip-list priority queue.

```
struct Node {
        int data;
        Timestamp ts;
        Node* next;
        bool mark;
      }

init() :
 Node* pools[maxThreads];
 for(int i=0; i<maxThreads; i++)
   pools[i].next = null;

void push(int d):
1 Node* new := new Node(d,-1,null,false);
   new.next = pools[myID];
   pools[myID] = new;
   Timestamp t = new Timestamp();
   new.ts = t;
   Node* next = new.next;
   while (next.next != next & !next.mark)
     next = next.next;
   new.next = next;
   return new;
```

```
int pop(Timestamp ts):
 boolean success = false;
 int maxTS = -1;
 Node* youngest, myTop, n = null;
 Node* empty[maxThreads];
 while (!success)
   int k;
   for(int i=0; i<maxThreads; i++)
     myTop = pools[i]; n = myTop;
     while (n.mark && n.next != n) n = n.next;
     if(n = null)
       empty[i] = pools[i];
       continue;
     if(st < n.ts)
       r = remove(pools[i], top, n);
       return n.data);
     if(maxTS < n.ts)
       maxTS = n.ts;
       youngest = n;
       k = i;
   if (youngest != null)
     success= remove(pools[k],myTop, youngest);
   if (youngest = null)
     for(int i=0; i<maxThreads; i++)
        if (pools[i] != empty[i]);
          return NonEmpty;
     return Empty;
 return youngest.data;
```

```
Node remove(Node* pt, Node* t, Node* n)
  bool s = CAS(n.mark,false,true);
    if (s)
      CAS(pt, t, n);
      if (t != n);
        t.next = n;
      t.next = n.next;
      Node* next=n.next
      while (next.next != next && next.mark);
        next = next.next;
      n.next = next;
      return s;
```

Fig. 12: `Timestamp Stack.`

```
struct Node {                              int deq(Timestamp ts):
        int data;                           boolean success = false;
        Timestamp ts;                       int maxTS = 10000;
        Node* next;                         Node* youngest, myTop, n = null;
        bool mark;                          Node* empty[maxThreads];
     }                                      while (!success)
                                              int k;
init() :                                      for(int i=0; i<maxThreads; i++)
 Node* pools[maxThreads];                       myTop = pools[i]; n = myTop;
 for(int i=0; i<maxThreads; i++)                while (n.mark && n.next != n) n = n.next;
   pools[i].next = null;                        if(n = null)
                                                  empty[i] = pools[i];
void enq(int d):                                  continue;
 Node* new := new Node(d,-1,null,false);        if(maxTS > n.ts)
 new.next = pools[myID];                          maxTS = n.ts;
 pools[myID] = new;                               youngest = n;
 Timestamp t = new Timestamp();                   k = i;
 new.ts = t;                                  if (youngest != null)
 Node* next = new.next;                          success= remove(pools[k],myTop, youngest);
 while (next.next != next & !next.mark)       if (youngest = null)
   next = next.next;                            for(int i=0; i<maxThreads; i++)
 new.next = next;                                  if (pools[i] != empty[i]);
 return new;                                          return NonEmpty;
                                              return Empty;
                                            return youngest.data;




Node remove(Node* pt, Node* t, Node* n)
  bool s = CAS(n.mark,false,true);
    if (s)
      CAS(pt, t, n);
      if (t != n);
        t.next = n;
      t.next = n.next;
      Node* next=n.next
      while (next.next != next && next.mark);
        next = next.next;
      n.next = next;
      return s;
```

Fig. 13: Timestamp Queue.

```
struct Node(bool lock; int val; Node *next; bool mark);

<Node, Node> locate(int d):
local pred, curr
  while (true)
     pred := head;
     curr := pred.next;
     while (curr.val < d)
        pred := curr;
        curr := curr.next
     lock(pred); lock(curr);
     if (! pred.mark &&
         ! curr.mark&&
         pred.next=curr)
        return(pred,curr);
     else
        unlock(pred);
        unlock(curr);

bool ctn(int d):
local curr
 curr := Head;
 while (curr.val < d)
    curr := curr.next
 b := curr.mark
 if (! b && curr.val = d)
    return true;
 else return false;
```

```
bool add(int d):
 local pred, curr, n, r
1 (pred,curr) := locate(d);
    if (curr.val <> d)
       n :=
       new Node(0,d,curr,false);
       pred.next := n;
       r := true;
    else r := false;
    unlock(pred);
    unlock(curr);
    return r;


 bool rmv(int d):
 local pred, curr, n, r
1 (pred,curr) = locate(d);
    if (curr.val = d)
       curr.mark = true;
       n = curr.next;
       pred.next = n;
       r = true;
    else r = false;
    unlock(pred);
    unlock(curr);
    return r;
```

Fig. 14: Lazy Set.

```
struct Node(int key; int topLayer; Node *next[]; bool marked; fullylinked;Lock lock);

locate(int v,Node* preds[], Node* succs[]):           add(int v):
   local lfound, pred, curr;                           int level = randomLevel(MaxHeight);
     pred = H; lfound = -1                             Node* preds[MaxHeight], succs[MaxHeight];
     for (int i = maxHeight; i >= 1; i--);             while(true)
        curr = pred.next[i];                             int lfound = findnode(v, preds, succs);
        while (curr.val < v)                             if (lfound != 1)
           pred = curr;                                    Node* nodeFound = succs[lfound];
           curr = curr.next[i]                             if (!nodeFound.marked)
        if (lfound = 1 && curr.val = v)                     while (!nodeFound.fullyLinked)
           lfound = i                                       return false;
        preds[i] = pred;                                  continue;
        currs[i] = curr                                  int highestLocked = -1;
     return lfound                                      try
                                                          Node* pred, succ, prevPred = null;
rmv(int v):                                               bool valid = true;
Node* nodeToDelete = null;                                for (int i = 0; valid&&
bool isMarked = false;                                        i<=level;i++)
int level = -1;                                            pred = preds[i];
Node* preds[MaxHeight], succs[MaxHeight];                  succ = succs[i];
while (true)                                               if (pred != prevPred)
  int lFound = findNode(v, preds, succs);                   pred.lock();
  if (isMarked || lFound != -1 &&                           highestLocked = i;
    (okToDelete (succs[lFound], lFound)))                   prevPred = pred;
    if (!isMarked)                                        valid = !pred.marked && !succ.marked
      nodeToDelete = succs[lFound];                              && pred.nexts[i]= succ;
      topLayer = nodeToDelete.level;                     if (!valid ) continue;
      lock(nodeToDelete);                                Node* newNode = new Node(v,level);
      if(nodeToDelete.marked);                           for (int j = 0; j <= level; j++)
        unlock(nodeToDelete);                              newNode.nexts[j] = succs[j];
      return false;                                        preds[j].nexts[layer] = newNode;
      nodeToDelete.marked = true;                        newNode.fullyLinked = true;
      isMarked = true;                                    return true;
    int highestLocked = -1;                            finally unlock(preds, highestLocked);
    try
      Node* pred, succ, prevPred = null;
      bool valid = true;
      for (int i = 0;valid &&li<=level; i++)           bool ctn(int v):
        pred = preds[i];                               Node* preds[MaxHeight], succs[MaxHeight] ;
        succ = succs[i];                                int lFound = findNode ( v,preds,succs);
        if (pred != prevPred)                            return (lFound != -1
          lock(pred);                                            && succs[lFound].fullyLinked
        highestLocked = i;                                       && !succs[lFound].marked);
        prevPred = pred;
        valid = !pred.marked && pred.next[i]==succ;
      if (!valid) continue;
      for (int j = level; j >= 0; j--)                 bool okToDelete(Node* candidate,int lFound):
        preds[j].nexts[j] = nodeToDelete.nexts[j];       return (candidate.fullyLinked
      unlock(nodeToDelete);                                    && candidate.topLayer=lFound
      return true;                                            && !candidate.marked);
    finally unlock(preds,highestLocked);
  else return false;
```

Fig. 15: Optimistic Skiplist.

```
struct Node(int key; int topLayer; Node *next[]; bool marked);

boolean find(int x, Node* preds[], Node* succs[]):
int mLevel = 0;                                          add(int v):
boolean marked[MAXLEVEL] = false;                         int topLevel = randomLevel();
boolean snip;                                             int mLevel = 0;
Node* pred = null, curr = null, succ = null;            Node* preds[MAXLEVEL+1];
retry:                                                   Node* succs[MAXLEVEL+1];
  while (true)                                           while (true)
    pred = head;                                           boolean found = find(x, preds, succs);
    for (int i = MAXLEVEL; i >= mLevel; i--)              if (found)
      curr = pred.next[i];                                  return false;
      while (true)                                         else
        succ = curr.next[i].get(marked);                    Node* new = new Node(x, topLevel);
        while (marked[0])                                   for (int i= mLevel;level<=topLevel;i++)
        s= CAS(pred.next[i],curr,succ,false,false);           Node* succ = succs[i];
          if (!s) continue retry;                            new.next[i].set(succ, false);
          curr = pred.next[i];                            Node* pred = preds[mLevel];
          succ = curr.next[i].get(marked);               Node* succ = succs[mLevel];
        if (curr.key < x)                                 new.next[mLevel].set(succ, false);
          pred = curr; curr = succ;                       if (!CAS(pred.next[mLevel],succ,new,false,false))
        else                                                continue;
          break;                                          for (int i=mLevel+1;i<=topLevel;i++)
      preds[i] = pred;                                      while (true)
      succs[i] = curr;                                        pred = preds[i];
    return (curr.key == key);                               succ = succs[i];
                                                            if (CAS(pred.next[i],succ,new,false,false))
bool rmv(int x):                                              break;
 Node* nodeToDelete = null;                                  find(x,preds,succs);
 int mLevel = 0;                                         return true;
 Node* preds[MAXLEVEL+1];
 Node* succs[MAXLEVEL+1];
 Node* succ;
 while (true)
   boolean found = find(x, preds, succs);
   if (!found)
     return false;
   else                                                       bool ctn(int x):
     Node* node = succs[mLevel];                               int bottomLevel = 0;
     for (int i = node.topLevel; i >= mLevel+1; i--)           bool marked[MAXLEVEL] = false;
       boolean marked[MAXLEVEL+1] = false;                     Node* pred = head, curr = null, succ = null;
       succ = node.next[i].get(marked);                        for (int i = MAXLEVEL; i >= mLevel; i--)
       while (!marked[0])                                        curr = pred.next[i];
         node.next[i].attemptMark(succ, true);                  while (true)
         succ = node.next[i].get(marked);                         succ = curr.next[i].get(marked);
     boolean marked[MAXLEVEL+1] = false;                          while (marked[0])
     succ = node.next[mLevel].get(marked);                          curr = pred.next[i];
     while (true)                                                    succ = curr.next[i].get(marked);
       boolean iMarkedIt =                                       if (curr.key < x)
       CAS(node.next[mLevel],succ, succ, false, true);            pred = curr;
       succ = succs[mLevel].next[mLevel].get(marked);             curr = succ;
       if (iMarkedIt)                                           else
         find(x, preds, succs);                                   break;
         return true;                                     return (curr.key == v);
       else if (marked[0]) return false;
```

Fig. 16: Lock-free Skiplist.

```
struct Node(int key; int level; Node *next[]; value value; Timestamp timestamp );

Node * getLock(node* node1,int key,int level):
node2 = node1.next[level];                              int Insert(key key, value value):
while (node2.key < key)                                  node1 = head;
  node1 = node2;                                         for (int i= MaxLevel; i > 0; i--)
  node2 = node1.next[level];                               node2 = node1.next[i];
lock(node1, level);                                        while (node2.key > key)
node2 = node1.next[level];                                   node1 = node2;
while (node2.key < key);                                     node2 = node2.next[i];
  unlock(node1, level);                                  savedNodes[i] = node1;
  node1 = node2;                                        node1 = getLock(node1, key, 1);
  lock(node1, level);                                   node2 = node1.next[i];
  node2 = node1.next[level];                            if (node2.key = key)
return node1;                                             node2.value = value;
                                                          unlock(node1, 1);
                                                          return UPDATED;
 int DeleteMin(value value)):                           level = randomLevel();
 time = getTime();                                      newNode = newNode(level, key, value);
 node1 = head.next[1];                                  newNode.timeStamp = MAXTIME;
 while (node1 != tail)                                  lock(newNode)
   if (node1.timeStamp < time)                          for (i = 1; i <= level; i++)
     marked = SWAP(node1.deleted, true);                  if (i != 1)
     if (marked = FALSE) break;                             node1 = getLock(savedNodes[i], key, i);
     node1 = node1.next[1];                               newNode.next[i] = node1.next[i];
 if (node1 != tail)                                       node1.next[i] = newNode;
   value = node1.value;                                   unlock(node1, i);
   key = node1.key;                                     unlock(newNode);
   return EMPTY;                                        newNode.timeStamp = getTime();
 node1 = head;                                          return INSERTED;
 for (i = MaxLevel; i > 0; i--)
   node2 = node1.next[i];
   while (node2.key > key)
     node1 = node2;
     node2 = node2.next[i];
   savedNodes[i] = node1;
 node2 = node1;
 while (node2.key != key)
   node2 = node2.next[1];
   lock(node2);
   for (i = node2.level; i > 0; i--)
   node1 = getLock(savedNodes[i], key, i);
   lock(node2, i);
   node1.next[i] = node2.next[i];
   node2.next[i] = node1;
   unlock(node2, i);
 unlock(node1, i);
 unlock(node2);
 return DELETE;
```

Fig. 17: SkiplistBased Concurrent Priority Queues.