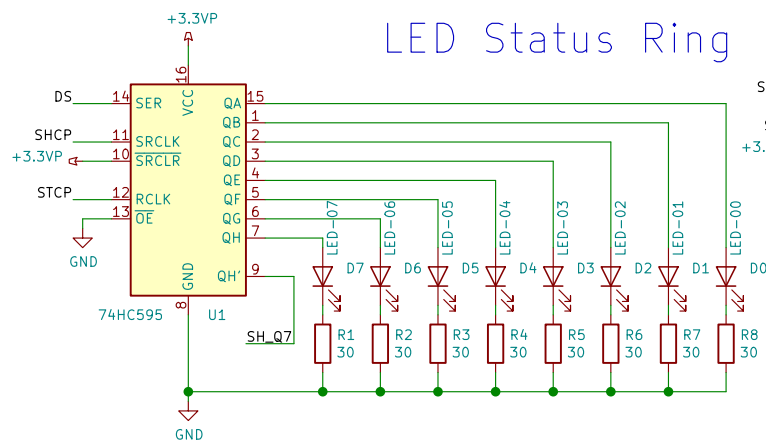
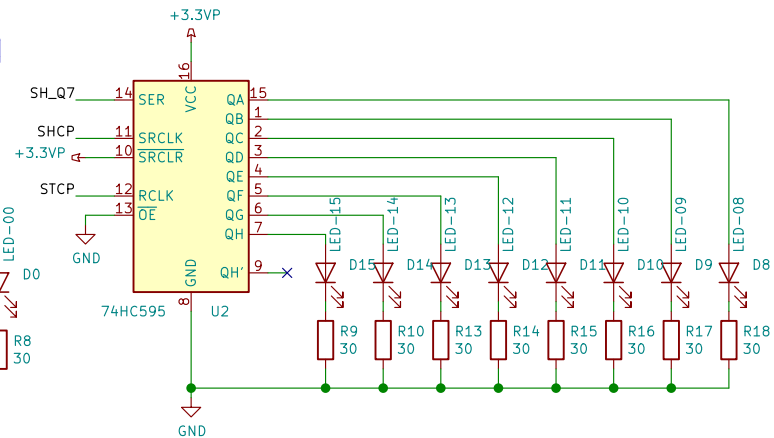
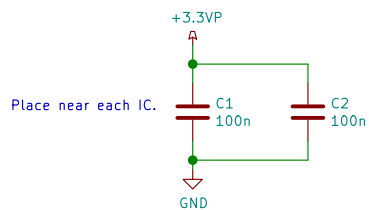


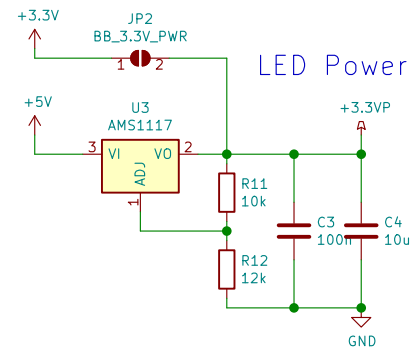
LED Status Ring



DS ——— DS
SHCP ——— SHCP
STCP ——— STCP



LED Power Supply



WARNING – NOT YET VALIDATED!

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Sheet: /LED Ring/

File: led-ring.sch

Title: LED Ring

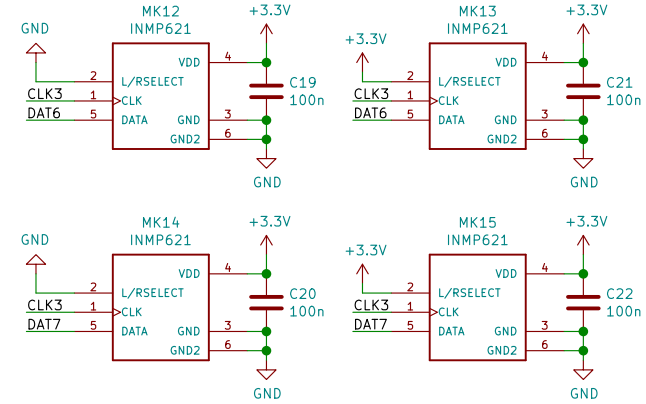
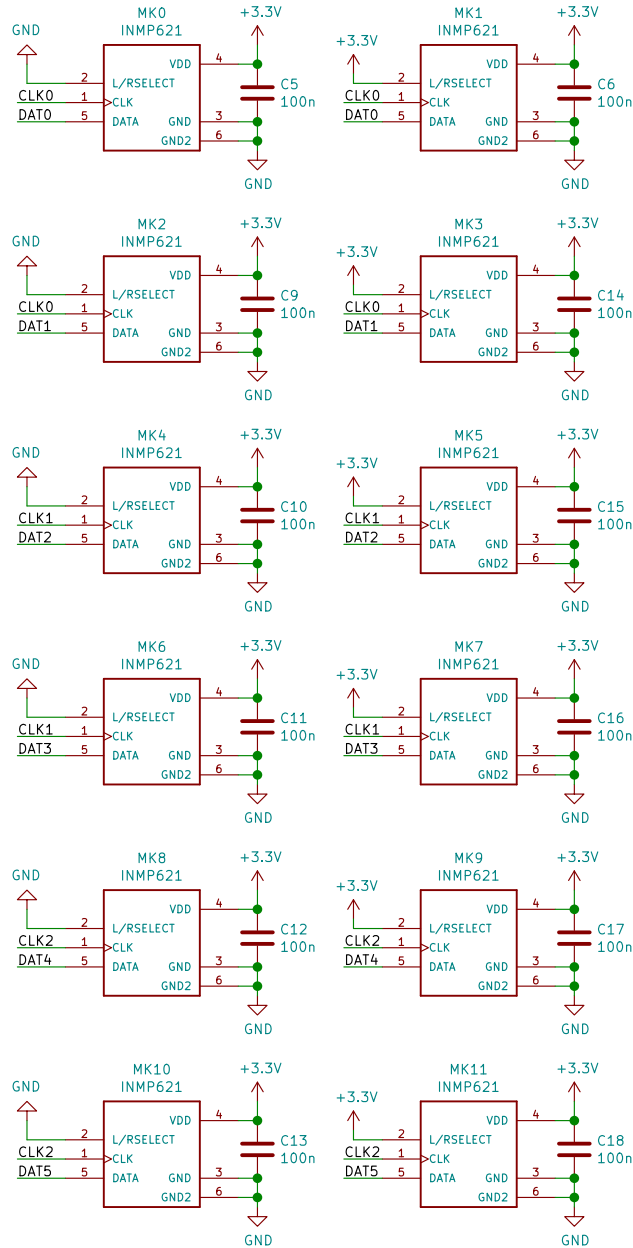
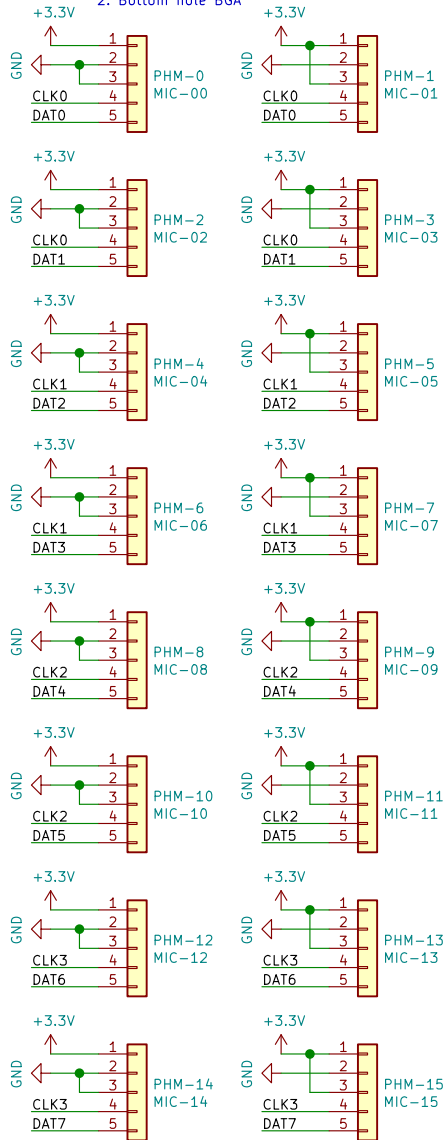
Size: A4 Date: 2020-01-26

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Rev: 1.0-rc3

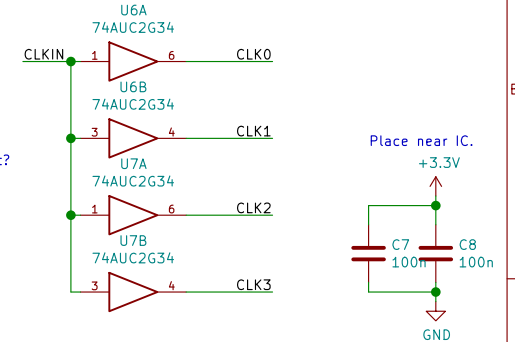
Id: 2/3

Two placement variants per microphone:
1. Pin Header
2. Bottom hole BGA

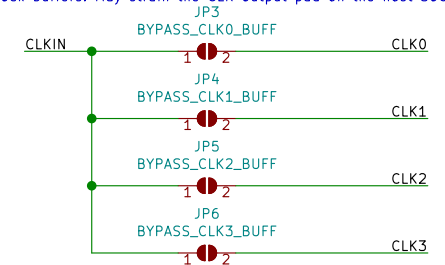


DAT0 → D00
DAT1 → D01
DAT2 → D02
DAT3 → D03
DAT4 → D04
DAT5 → D05
DAT6 → D06
DAT7 → D07
CLKIN → CLK

Buffer power supply.
Why KiCad symbol has forced a hidden VCC net?
VCC → +3.3V



Bypass the clock buffers. May strain the CLK output pad on the host SoC.



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Sheet: /Microphone Headers/
File: mic-headers.sch

Title: PDM Microphones

Size: A4 Date: 2020-01-26

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Rev: 1.0-rc3

Id: 3/3