Problem

Following is the simple implementation of RISC-V instruction subset (R-type, lw, sw, beq). Modify this diagram for the implementation to be able to also execute the following two instructions:

- jal
- addi

Fill the truth table for these two instructions

	I_6	I_5	I_4	I_3	I_2	I_1	I_0	ALUSrc	memtoreg	RegWrite	MemRead	memwrite	Branch	ALUOP1	ALUOP0
			input (opcode)							output					
R	0	1	1	0	0	1	1	0	0	1	0	0	0	1	0
lw	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0
sw	0	1	0	0	0	1	1	1	X	0	0	1	0	0	0
beq	1	1	0	0	0	1	1	0	X	0	0	0	1	0	1
jal															
addi															