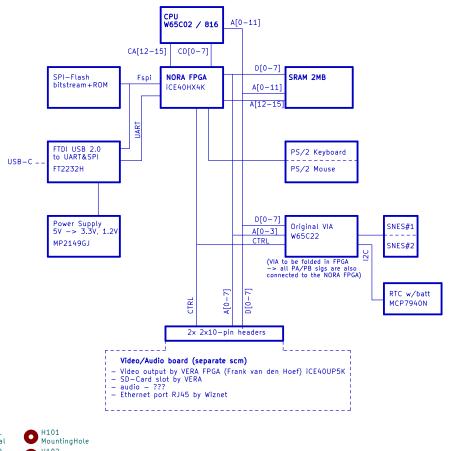
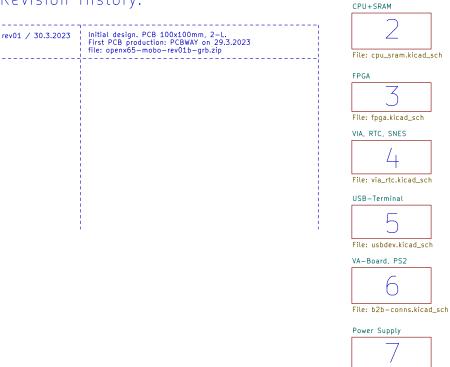
Computer OpenX65 by X65.EU

MOTHERBOARD (MO-BO) WITH THE CPU W65Cxx

Block Diagram:



Revision History:



O FID101 Fiducial O FID102 Fiducial O FID103 Fiducial

MountingHole

O FID104 Fiducial

H103 MountingHole H104 MountingHole OpenX65 MOTHERBOARD

KiCad E.D.A. kicad (6.0.7)

FOR X65.EU DESIGNED BY JSYKORA.INFO

Sheet: /

File: open65.kicad_sch

Title: OpenX65

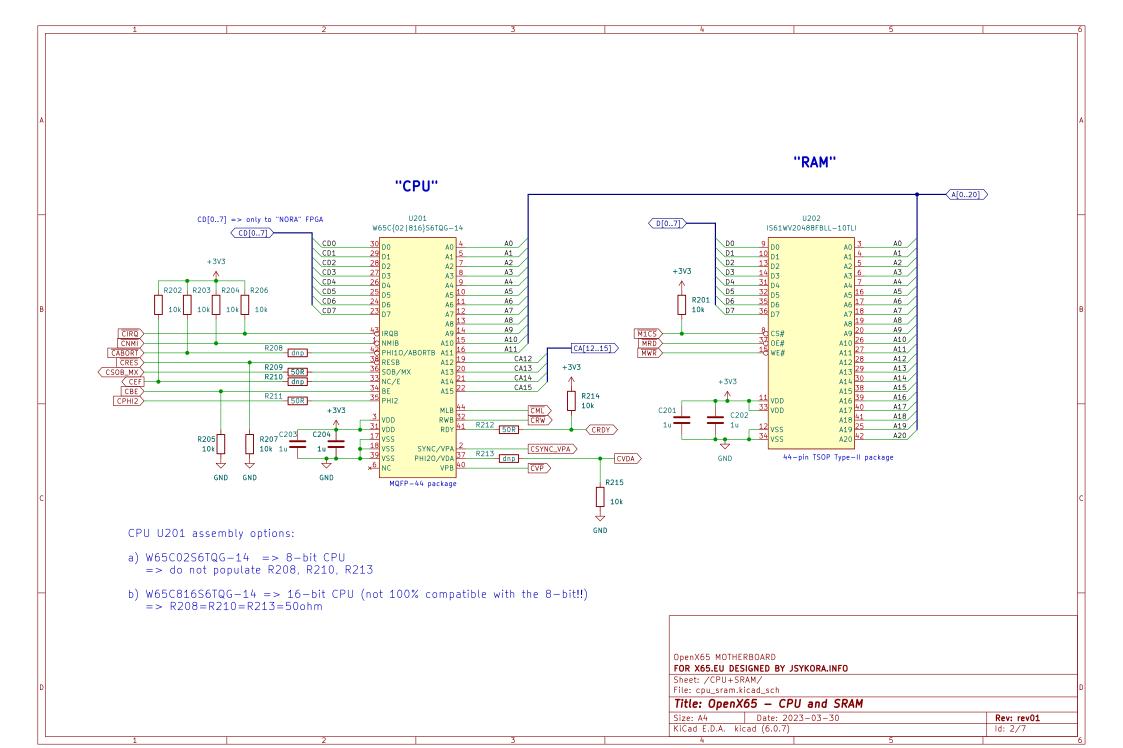
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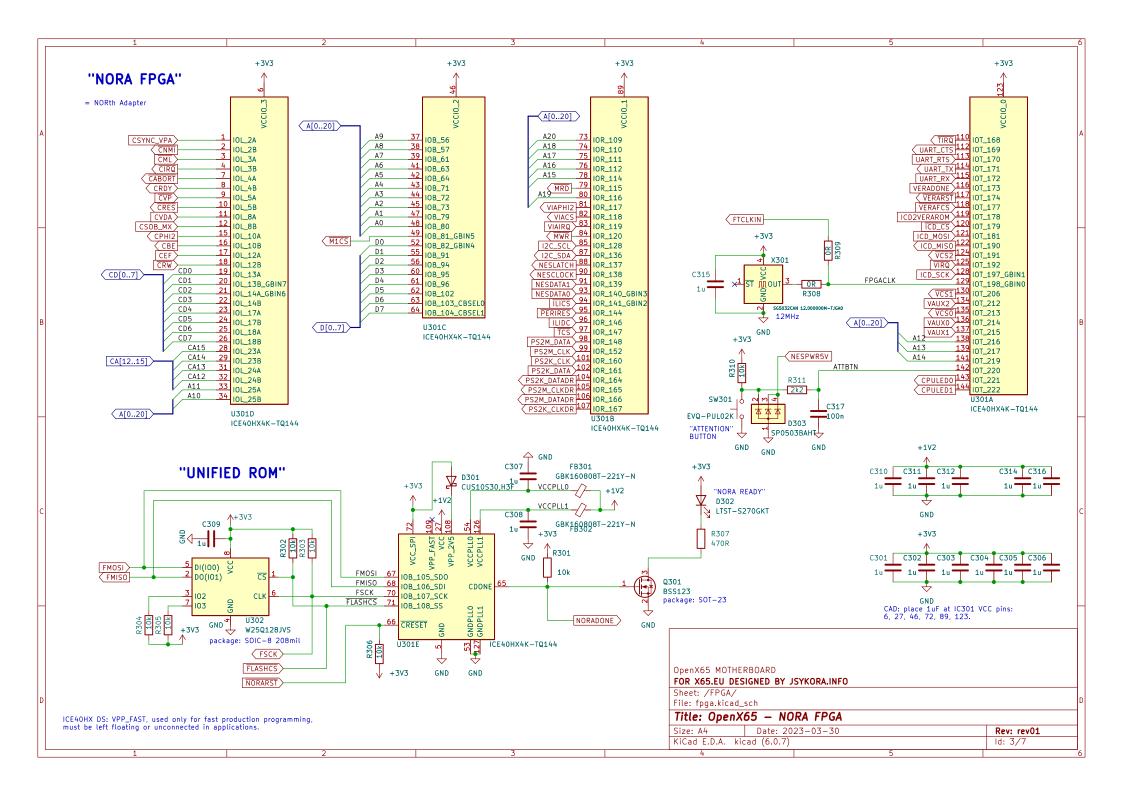
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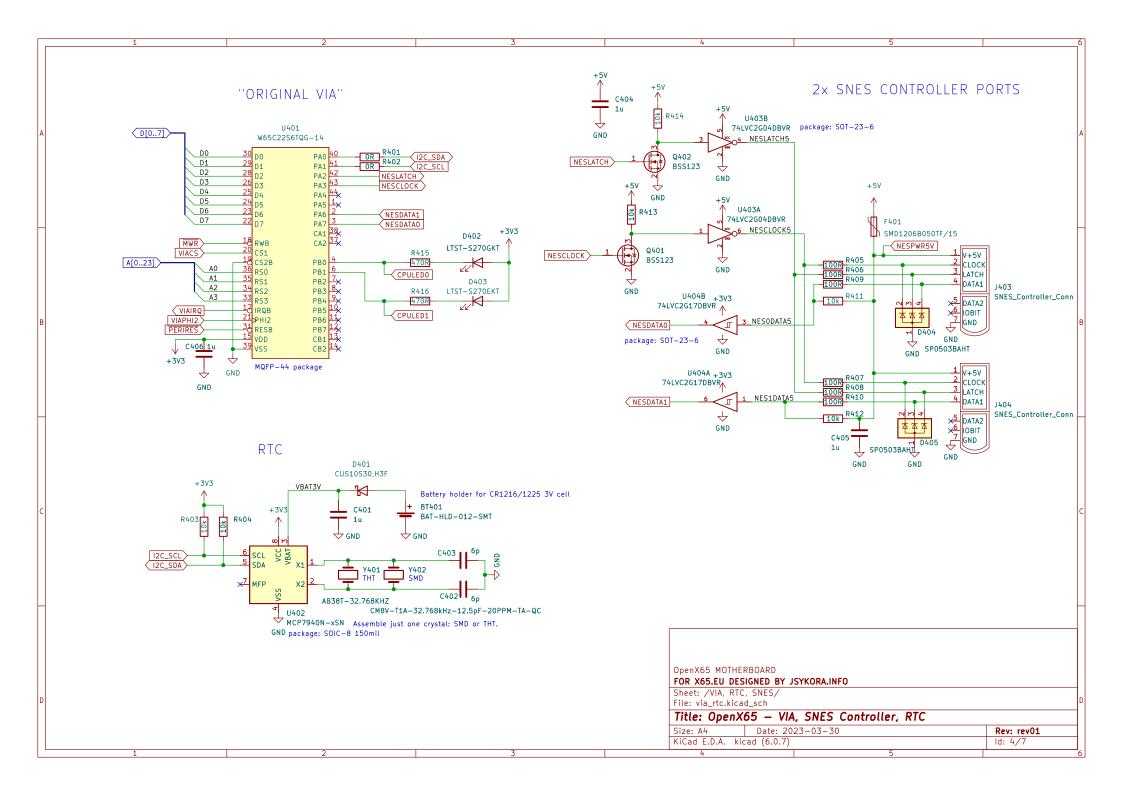
Rev: rev01

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Schematic sheets:







USB 2.0 WITH USB-C / UART TERMINAL AND ICD (In-Circuit Debugger) U503 74CBTLV3126PW,118 +3V3 GBK160808T-221Y-N FB501 GBK160808T-221Y-N 4B 11 1 10E 4 20E 10 30E 13 40E +3٧3 D501 FTDI1V8 VCC 7 D501 CUS10S30,H3F D503 SD05_S0D323 package: TSSOP-14 GND GND (dnp) ADBUS1 13 ADBUS1 14 ADBUS2 to in-circuit debug in FPGA J501 USB4085-GF-A SMD1206B050TF/15 VREGOUT ADBUS2 C502 ADBUS3 JP501 3u3 ADBUS4 umper_2_Open CC1 A5 CC1 R501 CC2 B5 CC2 5k1 GND ADBUS5 18× $\frac{1}{1} \circ \frac{1}{2} \xrightarrow{1} \frac{R521}{R} \xrightarrow{N} \frac{N}{N}$ ADBUS6 ADBUS7 20 ADBUS7 ICD2NORAROM 10k R517 GND 22 ACBUSO Alternative assembly: FT2232HL in LQFP-64 package ACBUS1 23 ACBUS2 24 USBDP ACBUS3 25 ACBUS3 GND | 12k | FTREF 6 | R504 | FTRES 11 | FTRES 11 | R505 | FTRES 11 26 ACBUS4 ACBUS4 * * * ACBUS5 SBII1= ACBUS6 SBU2- B8 ACBUS7 U504 FT2232HL GND BDBUS0 UART_RX UART_TX UART_RTS UART_CTS BDBUS1 BDBUS2 ADBUS0 16 ADBUS0 17 ADBUS1 18 ADBUS2 19 +3V3 100n FTDI1V8 49 VREGOUT BDBUS4 BDBUS5 BDBUS6 BDBUS7 ADBUS3 ADBUS4 19 21 ADBUS4 GND ADBUS5 ADBUS6 ADBUS7 ADBUS7 EECS EECLK EECS EECLK BCBUS0 42× +3V3 +3V3 BCBUS1 46× BCBUS2 47× R520 BCBUS3 48 UARTLED 470R-EDATA ACBUSO 26 ACBUSO 27p FTCLKIN D502 LTST-S270GKT USBDN 7 USBDP 8 ACBUS1 27 C505 BCBUS4 49 ACBUS2 ACBUS3 ACBUS4 ACBUS4 ACBUS4 ICD2VERAROM BCBUS5 BCBUS6 BCBUS7 FTREF 27p ACBUS5 ACBUS6 93LC56BT-I/SN UART RX/TX ORG16 - B option - necessary! package: SOIC-8 150mil FTRES 14 RESET PWREN 54× 30× Y501 ${\tt ABLS-12.000MHZ-B4-T}$ +3V3 U501 FT2232H-56Q BDBUS2 BDBUS3 BDBUS4 43 BDBUS4 C507 C510 package: VQFN-56 (!!) BDBUS5 BDBUS6 BDBUS7 EECS 63 EECLK 62 EEDATA 61 EEDATA GND GND GND GND BCBUS1 52 BCBUS3 55 BCBUS4 BCBUS4 BCBUS5 55 BCBUS6 BCBUS7 55 BCBUS6 BCBUS7 55 S8 BCBUS7 55 S8 BCBUS7 FTCLKIN 2 OSCI FTOSCO 3 OSCO PWREN 360× SUSPEND 36× package: LQFP-64 GND OpenX65 MOTHERBOARD FOR X65.EU DESIGNED BY JSYKORA.INFO File: usbdev.kicad_sch Title: OpenX65 — USB Terminal with UART and ICD Size: A3 Date: KiCad E.D.A. kicad (6.0. Date: 2023-03-30

