

Selectable analogue outputs $\sim 200\ \mu\text{m}$

Pixel Array

1152 x 576

Row selector + Pixel sequencer ($\sim 350\ \mu\text{m}$)

$\sim 13.8\ \text{mm}$

Column-level Discriminator

Zero Suppression

JTAG

Seq. Ctrl.

PLL

Memory 1

Memory 2

Bias DAC

Test Block

Pad Ring

$\sim 21.5\ \text{mm}$

$\sim 3000\ \mu\text{m}$