

From the Jumper cable

Monitoring temperature

Trigger In

JTAG

Control (CLK, Start, Reset)

Clock TTL from Oscillator

Vdd_D

Vdd_A

Pp clock dis

GND

VDD_A

VDD_D

VCC_mon

VCC

-5V

+5V

Clock in LVDS send to the ladder

Clock in LVDS not control by Pp_clock_dis

Enable External Clock

Enable Oscillator

To the DAQ system

CARTE PLUME Aux Board
02-2010 IPHC-STRASBOURG