

[illegible]

The diagram illustrates a top-layer PCB layout. It features a rectangular board with four circular mounting holes at the corners. A horizontal row of 18 vias is positioned near the top edge; the first six are red, followed by two green, and then ten more red. Below this, a series of yellow routing traces forms a complex path, connecting various points across the board. At the bottom left, there is a small rectangular area containing a row of seven vias (three red, four green). Two vertical traces extend from the bottom edge towards this area, each terminating in a yellow square pad.

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