| CS613 Homework 5  | 1          | 2    | 3    | 4   | 5       | 6   | 7  | 7    | 8    | 9 1  | 1   | 1 1  | 2 1  | 1 1  | 4 1  | 5 1  | 6 1  | 7 1  | 18 : | 9 2 | 0 2: | 1 2  | 2 23 | 24   | 25   | 26   | 27   | 28   | 29      | 30  | 31   | 32          | 33   | 34     | 35   | 3    | 5 3  | 7 3  | 8 3 | 9 4 | 0  | 11 4 | 12 | 43 | 4 . | 45            | 46 |
|---|------------|------|------|-----|---------|-----|----|------|------|------|-----|------|------|------|------|------|------|------|------|-----|------|------|------|------|------|------|------|------|---------|-----|------|-------------|------|--------|------|------|------|------|-----|-----|----|------|----|----|-----|---------------|----|
| add x1, x0, x0 // Zero Register                         | IF ID      | EX   | 0.   | AEM | WB      |     |    |      |      |      |     |      |      |      |      |      |      |      |      |     |      |      |      |      |      |      |      |      |         |     |      |             |      |        |      |      |      |      |     |     |    |      |    |    |     |               |    |
| addi x2, x1, 10 // Set counter (1cv)                    | IF.        | ID/: | i 11 | D/S | D/S S/I | D I | EX | MEM  | WB   |      |     |      |      |      |      |      |      |      |      |     |      |      |      |      |      |      |      |      |         |     |      |             |      |        |      |      |      |      |     |     |    |      |    |    |     |               |    |
| addi x5, x0, 2000 // get address of A[]'s first element |            |      |      |     | IF      |     | ID | EX   | MEM  | WB   |     |      |      |      |      |      |      |      |      |     |      |      |      |      |      |      |      |      |         |     |      |             |      |        |      |      |      |      |     |     |    |      |    |    |     |               |    |
| Strt: ld x7, 0(x5) // get A[i]                          |            |      |      |     |         |     | IF | ID/S | ID/S | ID/S | SAD | EX   | MEM  | WB   |      |      |      |      |      |     |      |      |      |      |      |      |      |      |         |     |      |             |      |        |      |      |      |      |     |     |    |      |    |    |     |               |    |
| addi x7, x7, 12   |            |      |      |     |         |     |    |      |      |      | IF  | ID/S | ID/S | ID/S | S/ID | EX   | MEM  | WB   |      |     |      |      |      |      |      |      |      |      |         |     |      |             |      |        |      |      |      |      |     |     |    |      |    |    |     |               |    |
| sd x7, 0(x5)  |            |      |      |     |         |     |    |      |      |      |     |      |      |      | IF.  | ID/S | ID/S | ID/S | S/ID | EX  | MEM  | W8   |      |      |      |      |      |      |         |     |      |             |      |        |      |      |      |      |     |     |    |      |    |    |     | $\overline{}$ |    |
| addi x5, x5, 8  |            |      |      |     |         |     |    |      |      |      |     |      |      |      |      |      |      |      | IF   | ID  | EX   | MEM  | WB   |      |      |      |      |      |         |     |      |             |      |        |      |      |      |      |     |     |    |      |    |    |     |               |    |
| subi x2, x2, 1  |            |      |      |     |         |     |    |      |      |      |     |      |      |      |      |      |      |      |      | IF. | ID   | EX   | MEM  | WB   |      |      |      |      |         |     |      |             |      |        |      |      |      |      |     |     |    |      |    |    |     |               |    |
| bne x2, x1, Strt  |            |      |      |     |         |     |    |      |      |      |     |      |      |      |      |      |      |      |      |     | IF.  | ID/S | ID/S | ID/S | S/ID | EX   | MEM  | WB   |         |     |      |             |      |        |      |      |      |      |     |     |    |      |    |    |     |               |    |
| End: xor x4, x1 // first loop, don't reach here         |            |      |      |     |         |     |    |      |      |      |     |      |      |      |      |      |      |      |      |     |      |      |      |      | IF/S | IF/S | IF/S | IF/S | WB/NOOP |     |      |             |      |        |      |      |      |      |     |     |    |      |    |    |     |               |    |
| Strt: ld x7, 0(x5)                                      |            |      |      |     |         |     |    |      |      |      |     |      |      |      |      |      |      |      |      |     |      |      |      |      |      |      |      |      | IF.     | ID  | EX   | MEM         | WB   |        |      |      |      |      |     |     |    |      |    |    |     | $\overline{}$ |    |
| addi x7, x7, 12   |            |      |      |     |         |     |    |      |      |      |     |      |      |      |      |      |      |      |      |     |      |      |      |      |      |      |      |      |         | IF. | ID/S | MEM<br>ID/S | ID/S | 5/1D 8 | EX   | MEM  | WB   |      |     |     |    |      |    |    |     |               |    |
| sd x7, 0(x5)  |            |      |      |     |         |     |    |      |      |      |     |      |      |      |      |      |      |      |      |     |      |      |      |      |      |      |      |      |         |     |      |             |      | IF I   | ID/S | ID/S | ID/S | S/ID | EX  | MEM | WB |      |    |    |     |               |    |
|   |            |      |      |     |         |     |    |      |      |      |     |      |      |      |      |      |      |      |      |     |      |      |      |      |      |      |      |      |         |     |      |             |      |        |      |      | 1    | 1    |     |     |    |      |    |    |     |               |    |
| CBI - clock curles of program / just purtipe count      | 2.41666667 | _    |      |     |         |     |    |      |      |      |     |      |      |      |      |      |      |      |      |     |      |      |      |      |      |      |      |      |         |     |      |             |      |        |      |      |      |      |     |     |    |      |    |    |     | _             | -  |