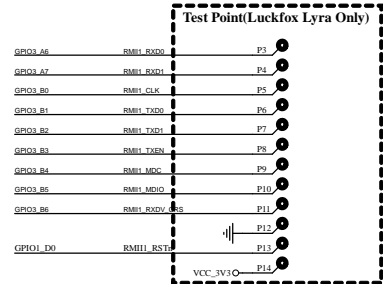
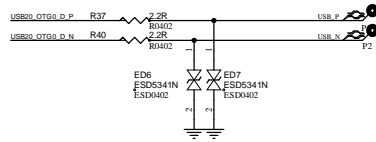
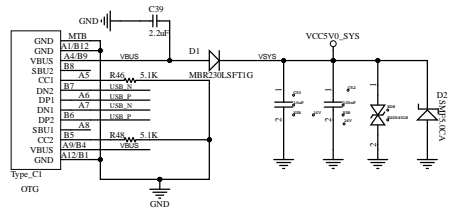
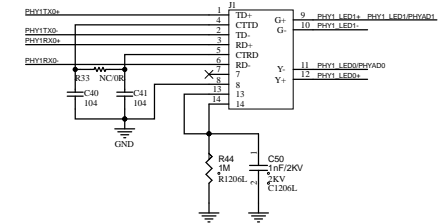
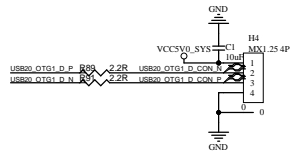


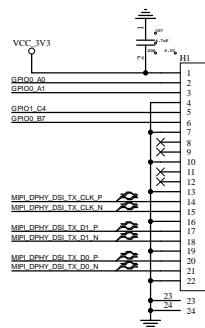
A



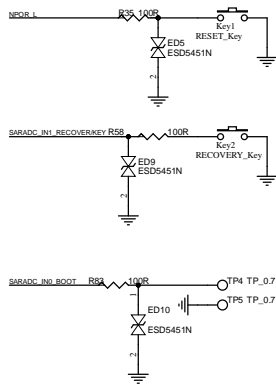
A

**B**

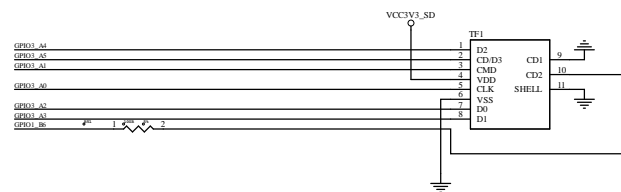
3



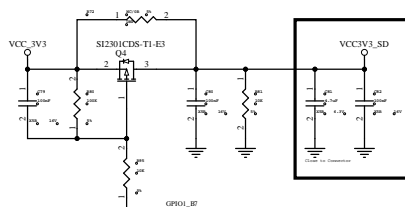
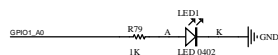
C



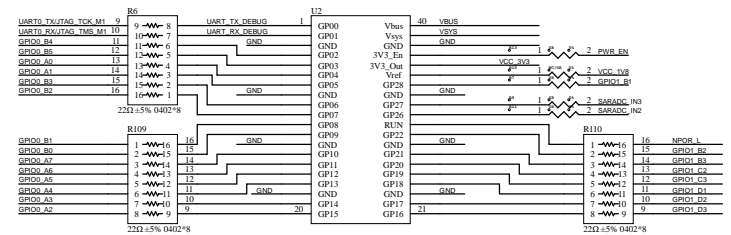
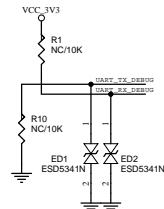
c



D



D



ID	function	ID	function	ID	function	ID	function
0	NC	26	CAN1_TX	51	SAI0_MCLK	77	TSADC_CTRL
1	UART1_TX	27	CAN1_RX	52	SAI0_SCLK	78	PWR_CTRL0(PMU_SLEEP)
2	UART1_RX	28	CAN0_TX	53	SAI0_LCKC	79	PWR_CTRL1(CORE_POWER_OFF)
3	UART2_TX	29	CAN0_RX	54	SAI0_SDI0	80	SPDIF_TX
4	UART2_RX	30	PWM0_CH0	55	SAI0_SDI1	81	SPDIF_RX
5	UART3_TX	31	PWM0_CH1	56	SAI0_SDI2	82	BIPCNTR_A0
6	UART3_RX	32	PWM0_CH2	57	SAI0_SDI3	83	BIPCNTR_A1
7	UART3_CTSN	33	PWM0_CH3	58	SAI0_SDO	84	BIPCNTR_A2
8	UART3_RTSN	34	PWM1_CH0	59	SAI1_MCLK	85	BIPCNTR_A3
9	UART4_TX	35	PWM1_CH1	60	SAI1_SCLK	86	BIPCNTR_A4
10	UART4_RX	36	PWM1_CH2	61	SAI1_LCKC	87	BIPCNTR_A5
11	UART4_CTSN	37	PWM1_CH3	62	SAI1_SDI	88	BIPCNTR_B0
12	UART4_RTSN	38	PWM1_CH4	63	SAI1_SDO0	89	BIPCNTR_B1
13	MIPI_DPHY_DSI_TX_TE	39	PWM1_CH5	64	SAI1_SDI01	90	BIPCNTR_B2
14	CLK_32K	40	PWM1_CH6	65	SAI1_SDO2	91	BIPCNTR_B3
15	I2C0_SCL	41	PWM1_CH7	66	SAI1_SDO3	92	BIPCNTR_B4
16	I2C0_SDA	42	TOUCH_KEYDRIVE	67	SPI0_CLK	93	BIPCNTR_B5
17	I2C1_SCL	43	TOUCH_KEY_IN0	68	SPI0_MOSI	94	PDM_CLK1
18	I2C1_SDA	44	TOUCH_KEY_IN1	69	SPI0_MISO	95	ETH_RMII0_PPSCCLK
19	I2C2_SCL	45	TOUCH_KEY_IN2	70	SPI0_CSN0	96	ETH_RMII0_PPSTRIG
20	I2C2_SDA	46	TOUCH_KEY_IN3	71	SPI0_CSN1	97	ETH_RMII1_PPSCCLK
21	PDM_CLK0	47	TOUCH_KEY_IN4	72	SPI1_CLK	98	ETH_RMII1_PPSTRIG
22	PDM_SDI0	48	TOUCH_KEY_IN5	73	SPI1_MOSI		
23	PDM_SDI1	49	TOUCH_KEY_IN6	74	SPI1_MISO		
24	PDM_SDI2	50	TOUCH_KEY_IN7	75	SPI1_CSN0		
25	PDM_SDI3			76	SPI1_CSN1		