

A Full State Feedback Controller for Dynamic Capacitive Wireless Power Transfer Systems

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Abstract—This digest presents a novel full-state feedback (FSF) controller for compensating changing coupling variations in dynamic capacitive wireless power transfer (WPT) systems utilizing an active variable reactance (AVR) rectifier. The AVR rectifier enables continuous compensation, allowing the WPT system to maintain high-efficiency power transfer at a fixed frequency by appropriately distributing power between its two branches. Conventional PID control is suboptimal for the AVR rectifier due to the higher-order system dynamics, nonlinearities, and cross-coupling between control loops. In contrast, the FSF control strategy presents an optimal and robust way to control higher order multi-input multi-output systems like the AVR rectifier. A state-space model is developed for the AVR rectifier, and the FSF controller is designed based on this model. To validate the proposed approach, a 13.56 MHz 25-W capacitive WPT system with an AVR rectifier is built and tested. The controller’s effectiveness is demonstrated through both simulations and experiments on the hardware prototype.

I. INTRODUCTION

Wireless power transfer (WPT) is becoming increasingly important and has the potential to accelerate the widespread adoption of electric vehicles (EVs), especially amongst vehicles that routinely travel along fixed routes such as buses and warehouse forklifts [1], [2]. Capacitive WPT systems can be lighter, cheaper, and smaller than comparable inductive WPT systems due to the absence of ferrite cores [3]. The ability of capacitive WPT systems to transfer high power at high efficiency has been demonstrated, thus proving the viability of this technology [4].

One main challenge of WPT systems is the extreme sensitivity to the value of the coupling capacitance or mutual inductance between the receiver and the transmitter [5]. To increase the practicality of WPT systems in dynamic applications, it is desirable to design and control them to tolerate coupling variations while maintaining full power transfer at high efficiency [6]–[9]. The recently proposed active variable reactance (AVR) rectifier addresses these challenges and efficiently provides variable compensation in high-frequency WPT systems [10]. However, a robust controller is needed in order to perform this compensation automatically and with a sufficiently high bandwidth to allow for in-motion dynamic EV charging. A conventional PID control-based decoupled-dual-loop strategy has been proposed to compensate for

changing coupling capacitance while maintaining the desired power transfer [11], [12]. However, such approaches are not well-suited to optimally control the AVR rectifier due to the higher-order system dynamics, nonlinearities, and cross-coupling between the two control loops.

This work presents a novel time-domain solution for the control of the AVR rectifier with sufficiently high bandwidth and robustness to operate the system dynamically. First, a model of the AVR rectifier is derived in state space. Then, a state-space control strategy is proposed, and an optimal full state feedback (FSF) linear quadratic regulator (LQR) controller is designed and validated in simulation. Finally, a prototype 13.56-MHz, 25-W capacitive WPT system with an AVR rectifier and a dynamically variable coupling capacitance is built and its operation demonstrated with the proposed control strategy.

II. DYNAMIC CAPACITIVE WIRELESS POWER TRANSFER SYSTEM WITH AN AVR RECTIFIER

Figure 1 presents the circuit schematic of the dynamic capacitive WPT system with the AVR rectifier. A DC input bus is connected to a full-bridge inverter. Power then flows through the tank formed by L_{comp} and C_s . C_s is the coupling capacitance between the roadway-side and vehicle-side coupling plates; L_{comp} is designed such that the impedance of the series-resonant LC tank is zero when C_s assumes its nominal value of $C_{s,nom}$.

The AVR rectifier has two branches: a top branch containing a boost converter attached to an inductive reactance $+jX$, and a bottom branch containing a buck converter attached to a capacitive reactance $-jX$. By independently varying the amount of power processed in each branch, the AVR rectifier can maintain the desired constant output power (P_{out}) while compensating for coupling variations by modulating the impedance seen at the input of the AVR rectifier (z_r). Throughout this paper, the following notation is used: p_{out} is the time-varying measured variable, P_{out} is the constant nominal DC value, and \hat{p}_{out} is the time-varying small-signal variable. They are related as $p_{out}(t) \equiv P_{out} + \hat{p}_{out}(t)$. Equations 1a-1b give p_{out} and z_r as a function of the large-signal circuit states as labeled in Fig. 1 [10]:

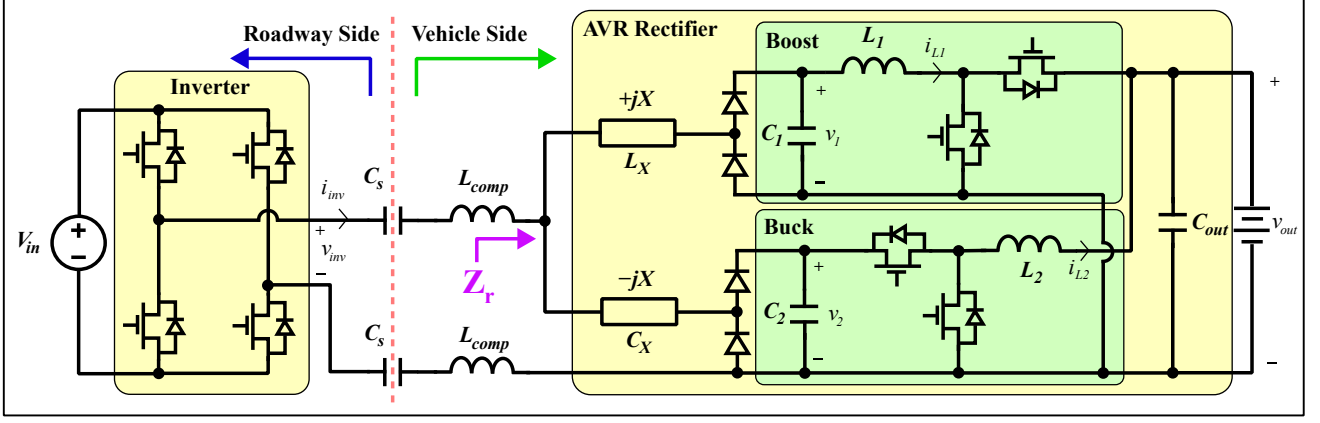


Fig. 1: Dynamic capacitive wireless power transfer system with active variable reactance rectifier schematic.

$$p_{out} = p_1 + p_2 = v_1 i_{L1} + v_{out} i_{L2} \quad (1a)$$

$$z_r = \text{Re}(z_r) + j \cdot \text{Im}(z_r) = r_r + jx_r = \frac{k_{rec}^2 v_1^2 v_2^2 + v_1 v_{out} i_{L1} i_{L2} X^2}{k_{rec} (v_1 v_2^2 i_{L1} + v_{out} v_1^2 i_{L2})} + jX \frac{v_1 i_{L1} v_2^2 - v_{out} i_{L2} v_1^2}{v_1 i_{L1} v_2^2 + v_{out} i_{L2} v_1^2} \quad (1b)$$

k_{rec} is a gain associated with the half-bridge rectifiers under sinusoidal approximation, and X is the differential reactance $\pm jX$ attached to the two branches. When the coupling capacitance C_s changes by an amount ΔC_s , Eqn. 1b is utilized to adjust the operation of the AVR rectifier such that x_r perfectly compensates ΔC_s while maintaining $r_r = R_r$. This also guarantees $p_{out} = P_{out}$, as well as resistive loading on the inverter.

A frequency-based controller has been proposed in [12] which utilizes two control loops: one maintains $p_{out} = P_{out}$, and the other maintains $r_r = R_r$. The two loops run at the same frequency, but use constant decoupling gains to minimize cross-coupling between the loops. However, the controller does not have sufficient performance to operate the system in dynamic charging scenarios. The AVR rectifier is a highly nonlinear, heavily-coupled multi-input multi-output (MIMO) system; FSF controllers are typically able to handle such plants more robustly than conventional frequency-based controllers. The FSF controller designed in this work follows the same control strategy of attempting to hold p_{out} and r_r constant at their nominal values P_{out} and R_r .

III. STATE SPACE MODELING OF THE AVR RECTIFIER

In this section, a model for the AVR rectifier is developed in state space using a method similar to that employed in [13], [14]. Sec. IV uses this model for the design of a robust FSF controller. Figure 2 contains the small-signal linearized model of the AVR rectifier. Of note is the inclusion of the input capacitance on both of the converters; these capacitors are necessary in modeling the dynamics of the converter as the input voltages v_1 and v_2 vary.

In general, a continuous-time state-space model can be written as $\frac{d}{dt} \vec{x} = A\vec{x} + B\vec{u}$, where \vec{x} is the state vector, \vec{u} is the input vector, A is the system dynamics matrix, and B is the input dynamics matrix. The AVR rectifier state-space model is derived from its small-signal model, so the state vector \vec{x} contains the small-signal voltages and currents $\hat{v}_1, \hat{v}_2, \hat{i}_{L1}, \hat{i}_{L2}$, and \hat{v}_{out} , as labeled in Fig. 2. The input vector \vec{u} contains the small-signal control handles \hat{d}_1 and \hat{d}_2 , which correspond to the small-signal duty cycles of the boost and the buck converters, respectively.

First-order differential equations for $\frac{d}{dt} \vec{x}$ are written using time-domain circuit analysis techniques. These are arranged into a matrix, resulting in a valid five-state un-augmented state space model of the AVR rectifier. However, the controller design requires additional augmented integral states $z_1 \equiv \int_0^t \hat{p}_{out}(\tau) d\tau$ and $z_2 \equiv \int_0^t \hat{r}_r(\tau) d\tau$ which track the integral of the error in output power $p_{out}(t)$ and error in the real part of the input impedance $r_r(t)$, respectively.

To incorporate z_1 and z_2 into the state-space model, the quantities $\frac{d}{dt} z_1 = \hat{p}_{out}$ and $\frac{d}{dt} z_2 = \hat{r}_r$ must be written as linear combinations of the elements of \vec{x} . This can be done by taking partial derivatives of Eqns. 1a-1b, with results shown in Table I. Thus, an augmented state vector $\vec{x}_{aug} = [\vec{x} \ z_1 \ z_2]^T$ can be constructed, and the complete state-space model is presented in Eqn. 2. This model is the first significant contribution of this paper.

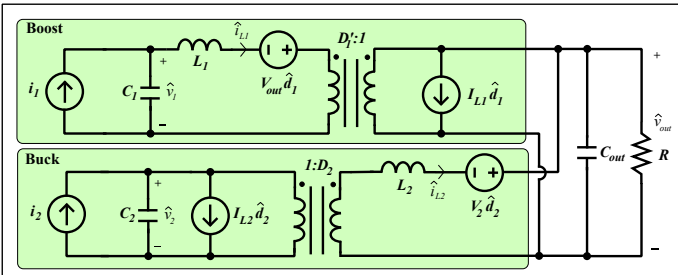


Fig. 2: Schematic of the AVR rectifier's linearized small-signal model.

$$\frac{d}{dt} \begin{bmatrix} \hat{v}_{out} \\ \hat{v}_1 \\ \hat{v}_2 \\ \hat{i}_{L1} \\ \hat{i}_{L2} \\ z_1 \\ z_2 \end{bmatrix} = \begin{bmatrix} -\frac{1}{RC_{out}} & 0 & 0 & \frac{D'_1}{C_{out}} & \frac{1}{C_{out}} & 0 & 0 \\ 0 & 0 & 0 & -\frac{1}{C_1} & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & -\frac{D_2}{C_2} & 0 & 0 \\ -\frac{D'_1}{L_1} & \frac{1}{L_1} & 0 & 0 & 0 & 0 & 0 \\ -\frac{1}{L_2} & 0 & \frac{D_2}{L_2} & 0 & 0 & 0 & 0 \\ -\frac{\partial p_{out}}{\partial v_{out}} & -\frac{\partial p_{out}}{\partial v_1} & -\frac{\partial p_{out}}{\partial v_2} & -\frac{\partial p_{out}}{\partial i_{L1}} & -\frac{\partial p_{out}}{\partial i_{L2}} & 0 & 0 \\ -\frac{\partial v_{out}}{\partial r_r} & -\frac{\partial v_1}{\partial r_r} & -\frac{\partial v_2}{\partial r_r} & -\frac{\partial i_{L1}}{\partial r_r} & -\frac{\partial i_{L2}}{\partial r_r} & 0 & 0 \end{bmatrix} \begin{bmatrix} \hat{v}_{out} \\ \hat{v}_1 \\ \hat{v}_2 \\ \hat{i}_{L1} \\ \hat{i}_{L2} \\ z_1 \\ z_2 \end{bmatrix} + \begin{bmatrix} -\frac{I_{L1}}{C_{out}} & 0 \\ 0 & 0 \\ 0 & -\frac{I_{L2}}{C_2} \\ \frac{V_{out}}{L_1} & 0 \\ 0 & \frac{V_2}{L_2} \\ 0 & 0 \\ 0 & 0 \end{bmatrix} \begin{bmatrix} \hat{d}_1 \\ \hat{d}_2 \end{bmatrix} \quad (2)$$

TABLE I: Table of partial derivatives for Eqns. 1a-1b. n and d are the numerator and denominator of the expression for r_r in Eqn. 1b, respectively. They are evaluated at the nominal DC value of the system.

$\frac{\partial p_{out}}{\partial v_{out}} = I_{L2}$	$\frac{\partial r_r}{\partial v_{out}} = d^{-2} \left(d(V_1 I_{L1} I_{L2} X^2) - nk_{rec}(V_1^2 I_{L2}) \right)$
$\frac{\partial p_{out}}{\partial v_1} = I_{L1}$	$\frac{\partial r_r}{\partial v_1} = d^{-2} \left(d(2k_{rec}^2 V_2^2 V_1 + V_{out} I_{L1} I_{L2} X^2) - nk_{rec}(V_2^2 I_{L1} + 2V_{out} V_1 I_{L2}) \right)$
$\frac{\partial p_{out}}{\partial v_2} = 0$	$\frac{\partial r_r}{\partial v_2} = d^{-2} \left(d(2k_{rec}^2 V_1^2 V_2) - nk_{rec}(2V_1 V_2 I_{L1}) \right)$
$\frac{\partial p_{out}}{\partial i_{L1}} = V_1$	$\frac{\partial r_r}{\partial i_{L1}} = d^{-2} \left(d(V_1 V_{out} I_{L2} X^2) - nk_{rec}(V_1 V_2^2) \right)$
$\frac{\partial p_{out}}{\partial i_{L2}} = V_{out}$	$\frac{\partial r_r}{\partial i_{L2}} = d^{-2} \left(d(V_1 V_{out} I_{L1} X^2) - nk_{rec}(V_{out} V_1^2) \right)$

IV. FULL STATE FEEDBACK CONTROLLER DESIGN AND SIMULATION VALIDATION

Figure 3 shows the complete block diagram of the system under closed-loop control. The controller design consists of finding a gain matrix K such that setting the input as $\vec{u} = \vec{D} - K\vec{x}_{aug}$ results in the closed-loop system driving p_{out} and r_r to their nominal values P_{out} and R_r regardless of the value of C_s . To find an optimal K , heuristic techniques presented in [15] are used to design a linear quadratic regulator (LQR) with reasonable values for the diagonal elements of the Q and R cost-defining matrices. Then, the `lqr()` function in MATLAB is utilized to solve the corresponding Algebraic Riccati Equation for the optimal gain matrix K .

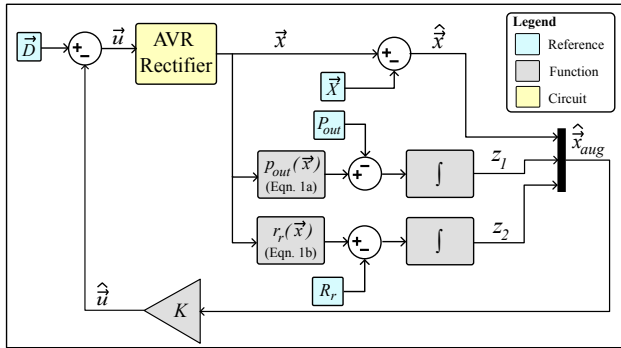


Fig. 3: Simplified block diagram of the state-space control loop. $p_{out}(\vec{x})$ is given by Eqn. 1a; $r_r(\vec{x})$ is given by Eqn. 1b, \vec{D} is the vector of nominal duty ratios for the buck and boost converters.

Q is diagonal, with the elements along the diagonal quantifying the cost assigned to each element of the augmented state vector \vec{x}_{aug} . R is also diagonal, with the elements along the diagonal quantifying the cost assigned to each element of the input vector \vec{u} . Since the controller is only required to drive the two augmented states z_1 and z_2 to zero and should not respond to DC deviations in any of the elements of \vec{x} , only the weights corresponding to z_1 and z_2 should be nonzero in Q . Both diagonal elements of R should be nonzero. The structure of these matrices is shown below:

$$Q = \begin{bmatrix} 0_{5 \times 5} & 0_{5 \times 2} \\ 0_{2 \times 5} & \begin{bmatrix} q_{z1} & 0 \\ 0 & q_{z2} \end{bmatrix} \end{bmatrix} \quad R = \begin{bmatrix} r_{d1} & 0 \\ 0 & r_{d2} \end{bmatrix}$$

Tuning the controller consists of finding a set of strictly positive values for q_{z1} , q_{z2} , r_{d1} and r_{d2} which results in the desired performance. Bryson's Rule for tuning Q and R is used as a starting point for the tuning process.

A model is built in Simulink and PLECS to validate the proposed controller. Figure 4 shows the control objectives p_{out} and r_r being controlled to their target values of $P_{out} = 200$ W and $R_r = 50$ Ω , respectively, while the coupling reactance is disturbed by an amount $\Delta X_s \equiv \frac{1}{2\pi f_s} \left(\frac{1}{C_{s,nom}} - \frac{1}{C_s} \right)$, where f_s is the switching frequency, and $C_{s,nom}$ is the nominal coupling capacitance. The impedance seen by the inverter must be near-resistive for the converter to operate at peak efficiency. Figure 5 shows the inverter output voltage (v_{inv}) and inverter output current (i_{inv}) waveforms under a misalignment condition with and without the controller. The proposed controller is enabling the AVR rectifier to

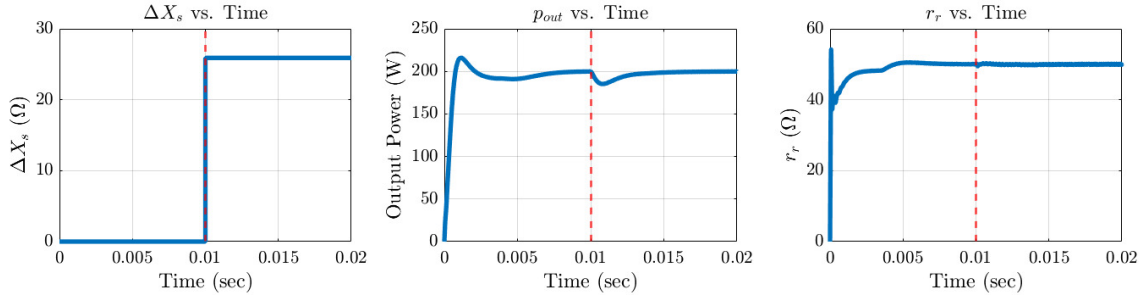


Fig. 4: p_{out} and r_r vs. time, showing the AVR rectifier's startup transient under the proposed controller for $t \in [0, 0.01)$, followed by a step change in coupling reactance ΔX_s at time $t = 0.01$.

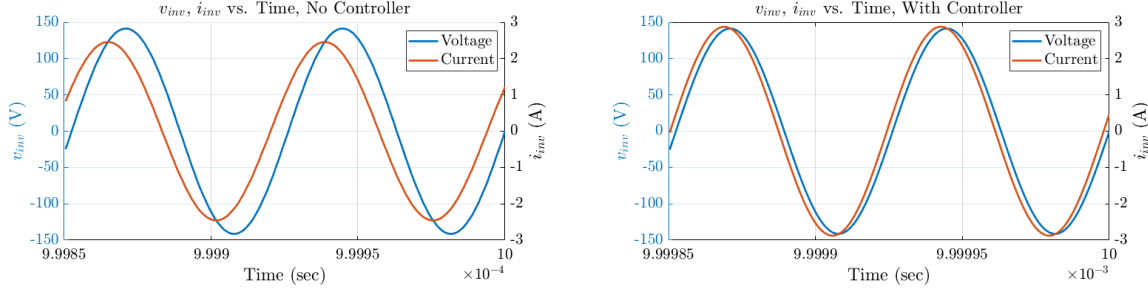


Fig. 5: Inverter output voltage v_{inv} and inverter output current i_{inv} with a change in coupling reactance $\Delta X_s = 26\Omega$, with and without the controller.

compensate for a change in coupling reactance, as shown by the inverter output voltage and current waveforms being nearly in-phase. The design of the FSF controller for the AVR rectifier and simulation results showing its effectiveness are the second significant contribution of this work.

TABLE II: Table listing system component values.

Component	Value	Component	Value
C_s	50 pF	R	39 Ω
L_{comp}	5 μ H	C_1, C_2, C_{out}	4 μ F
L_1, L_2	100 μ H	P_{out}	24 W
X	35.5 Ω	V_{in}	60 V
$C_X (-jX)$	457 pF	$L_X (+jX)$	0.58 μ H

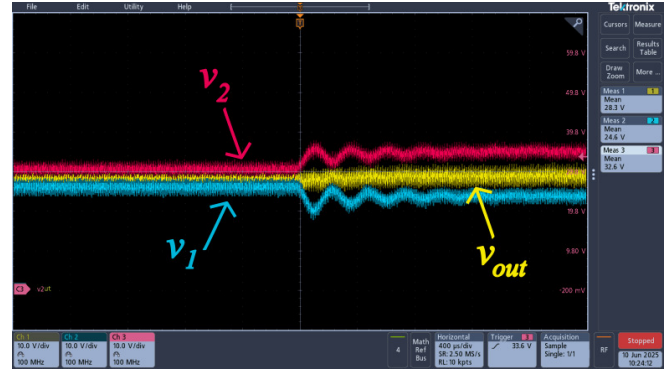


Fig. 7: Experimental transient showing v_1 , v_2 , and v_{out} trajectories when the controller is activated at time $t = 0$.

V. HARDWARE AND EXPERIMENTAL RESULTS

The proposed controller is implemented on a prototype 13.56 MHz, 25-W capacitive WPT system equipped with an AVR rectifier and an adjustable coupling capacitance. Figure 6 shows the hardware prototype and experimental setup with annotations labeling important components, whose values are listed in Table II. The system is tuned to operate in the nominal condition, with the inductor L_{comp} compensating for the coupling capacitance C_s at the desired operating frequency. Then, the system is put in a misaligned condition, where degraded performance is observed. Finally, the controller is activated, which brings the system back to full operating power with resistive loading on the inverter.

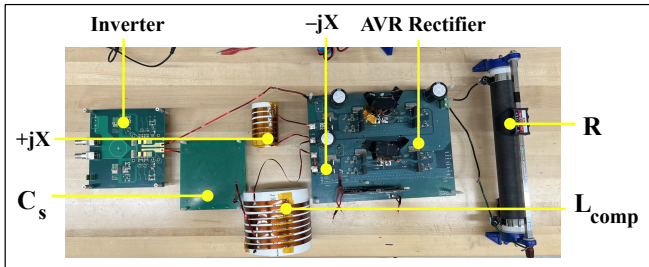


Fig. 6: Annotated photograph of the hardware prototype. A load resistor R is used in place of a battery.

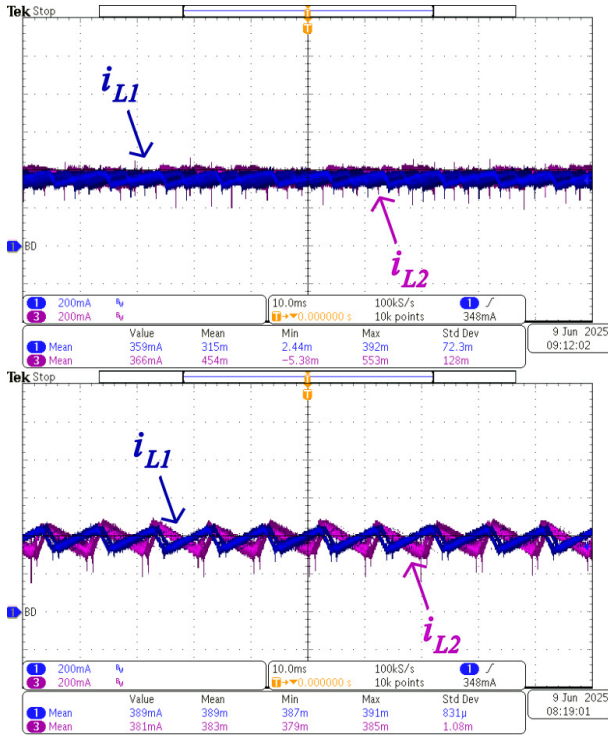


Fig. 8: Experimental waveforms showing AVR branch currents in a misaligned condition before the controller is activated (top) and after (bottom).

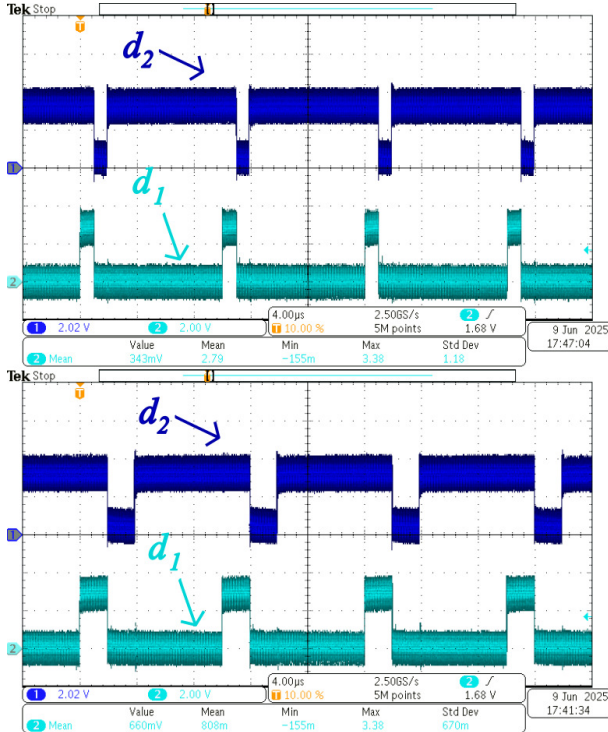


Fig. 9: Experimental waveforms showing boost (d_1) and buck (d_2) converter gating signals in a misaligned condition before the controller is activated (top) and after (bottom).

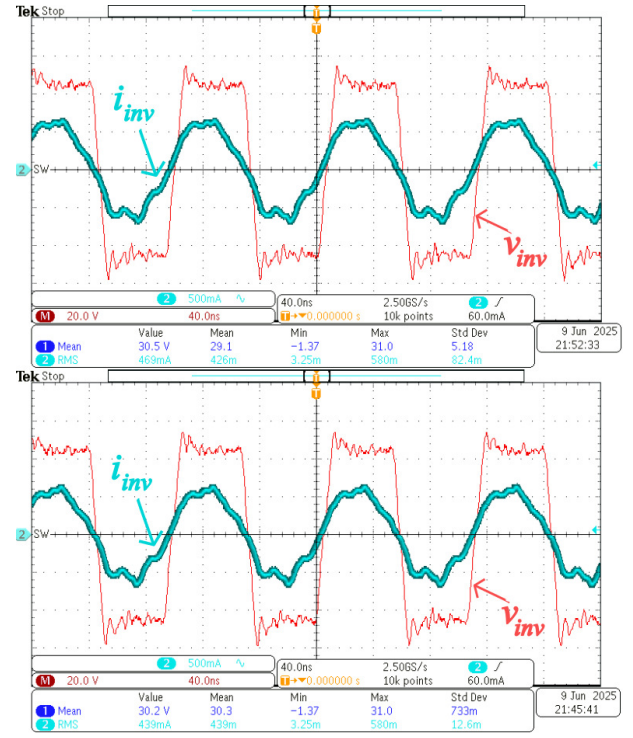


Fig. 10: Experimental waveforms showing inverter output voltage and current (v_{inv} , i_{inv}) in a misaligned condition before the controller is activated (top) and after (bottom).

Figure 9 shows the duty cycles to the two converters changing as a result of the control action.

Figure 8 shows the effect of the control action on the AVR branch currents. Note that the sum of the currents through the two branches has increased as a result of the control action, showing that the full nominal output power has been restored. The current ripple changes because the duty cycles of the two converters change with control action.

Figure 10 shows the effect of the control action on the inverter output voltage and current. Note that there is a small but noticeable phase difference between the voltage and current before the controller is activated, which may cause the inverter to lose soft switching at higher power. After the control action is taken, the voltage and current are perfectly in phase. Also note that the magnitude of the inverter output current decreases; this shows that the control action is reducing the circulating currents in the resonant tank by ensuring the AVR rectifier and tank present a resistive load to the inverter.

Figure 7 shows the transient behavior of the converter when the controller is activated. Note that the three voltages settle to new values after the control action is taken, and that the settling time is around 1 millisecond — more than enough bandwidth to track a vehicle moving over a capacitive WPT charging pad. The controller implementation and experimental results showing controller effectiveness comprise the third significant contribution of this work.

VI. CONCLUSION AND FUTURE WORK

This paper presents a novel state-space model and full state feedback (FSF) controller for capacitive WPT systems equipped with AVR rectifiers. The proposed controller allows the system to transfer power efficiently as the coupling capacitance between the transmitter and receiver is varied. A 13.56-MHz, 25-W capacitive WPT system with an AVR rectifier and a dynamically adjustable coupling capacitance was assembled. The proposed controller was validated in simulation and on the hardware prototype.

VII. ACKNOWLEDGEMENTS

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