## Phase Locked Loop

Ben Lorenzetti

Project Start Date: April 2, 2015 Report Submission Date: April 20, 2015

## ${\bf Contents}$

1	Objective	1
2	Principle of Operation	2

## 1 Objective

To measure the characteristics of a phase locked loop and to investigate and model its behavior in an FSK (frequency-shift keying) demodulator.

2 Principle of Operation