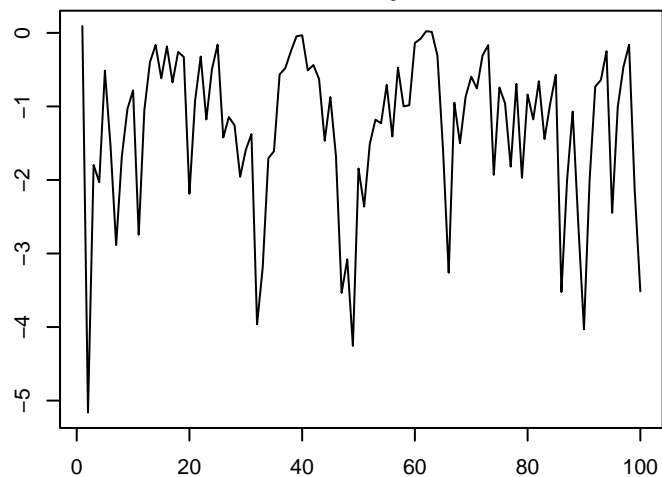
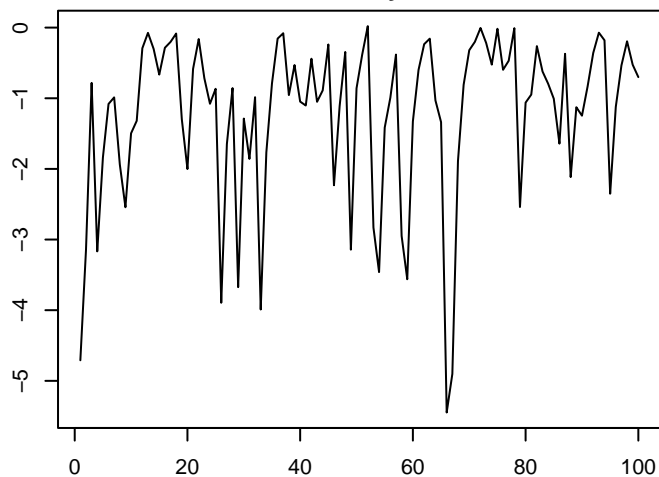


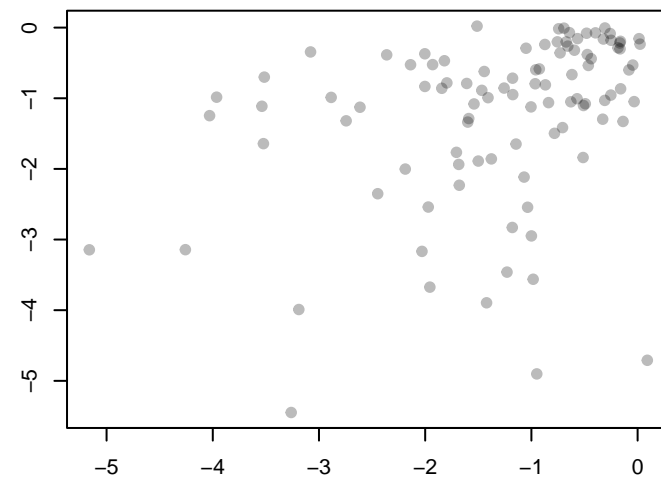
Series y1



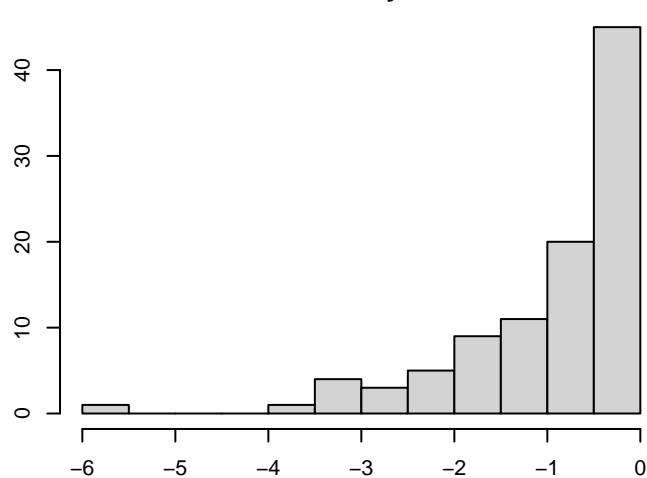
Cond 081 (chisg)\_2 Rep 32



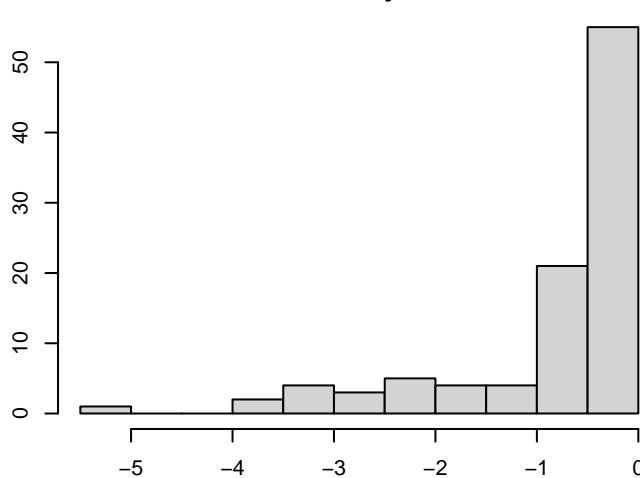
Scatter



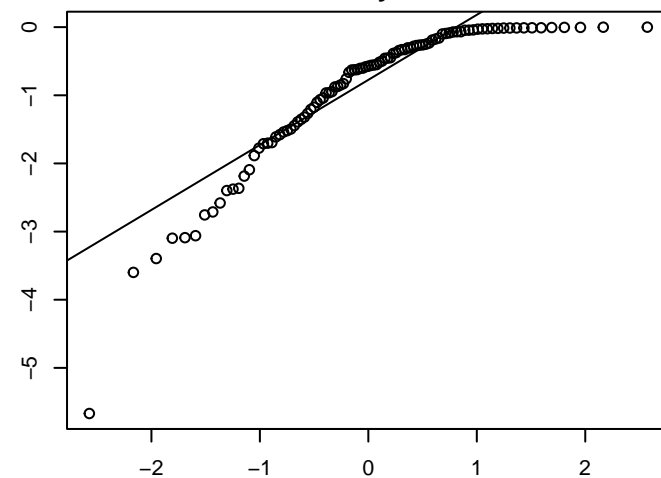
Resid y1



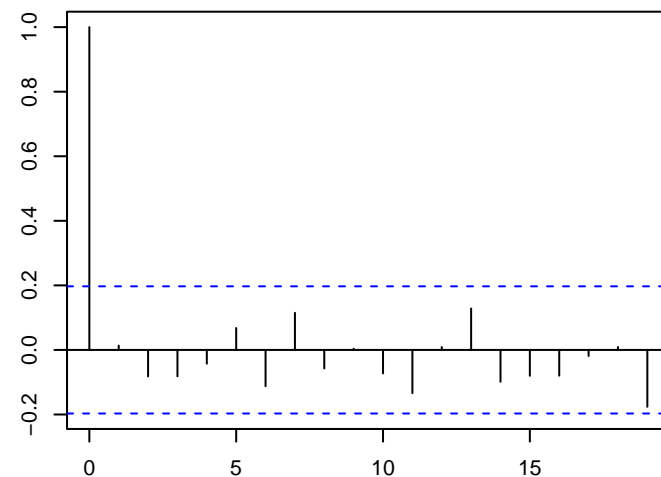
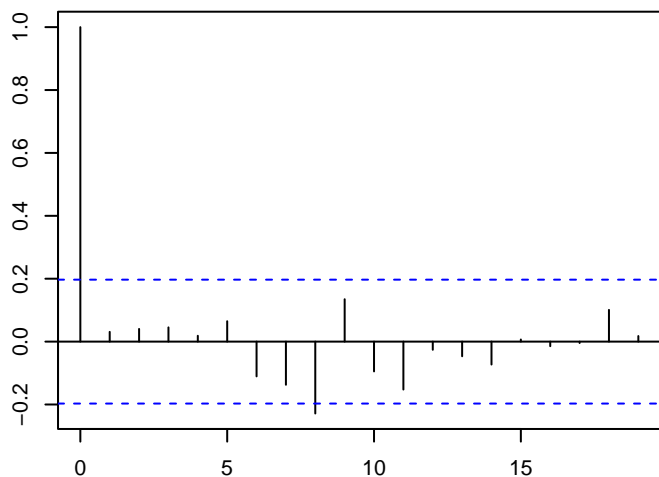
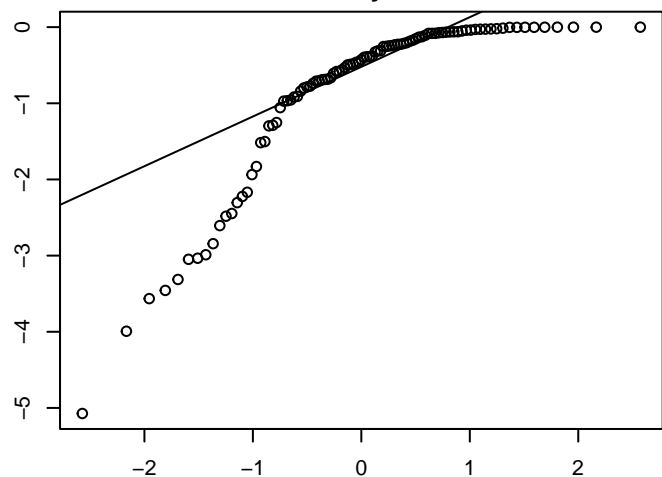
Resid y2



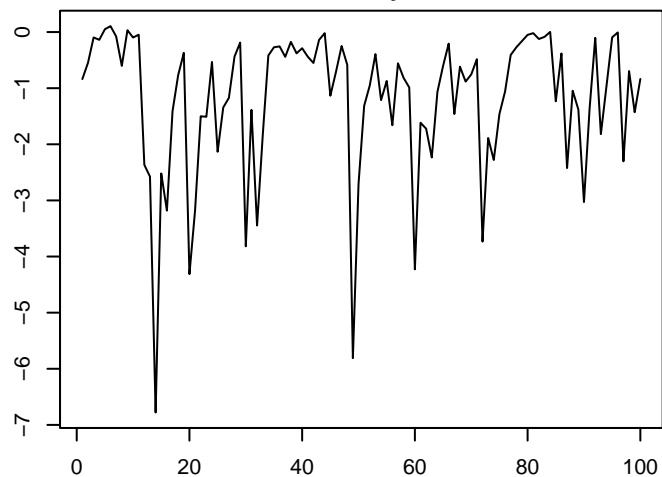
QQ y1



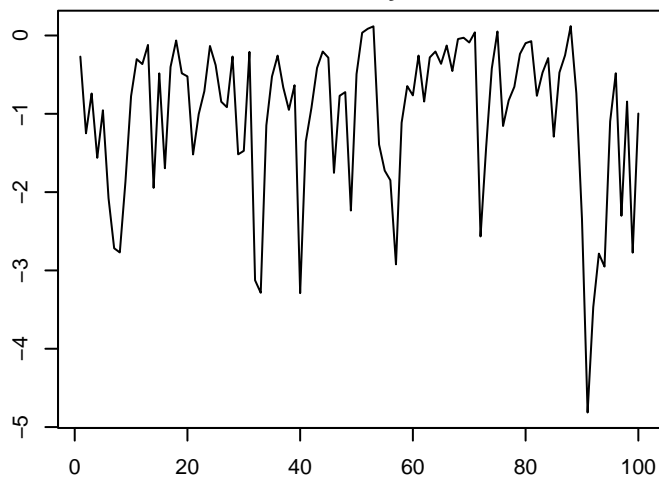
QQ y2



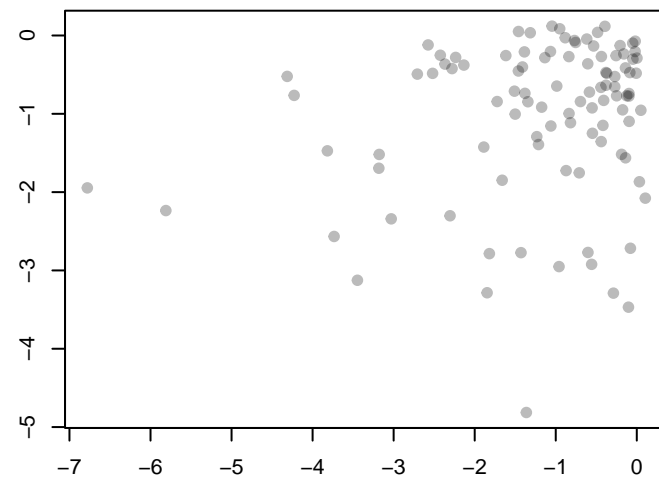
Series y1



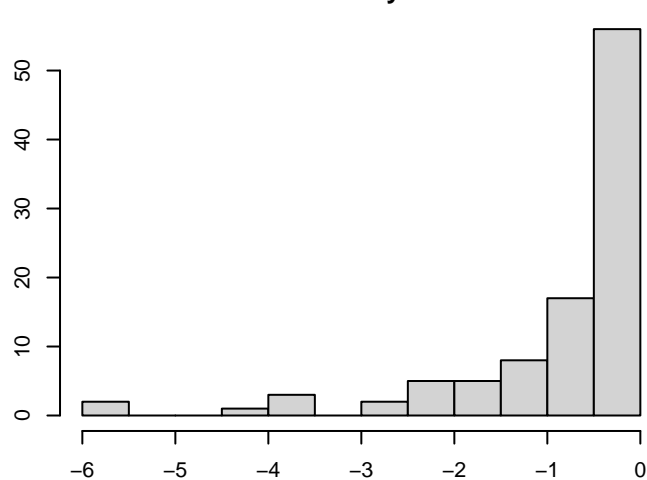
Cond 081 (chisg) | Series y2 | Rep 37



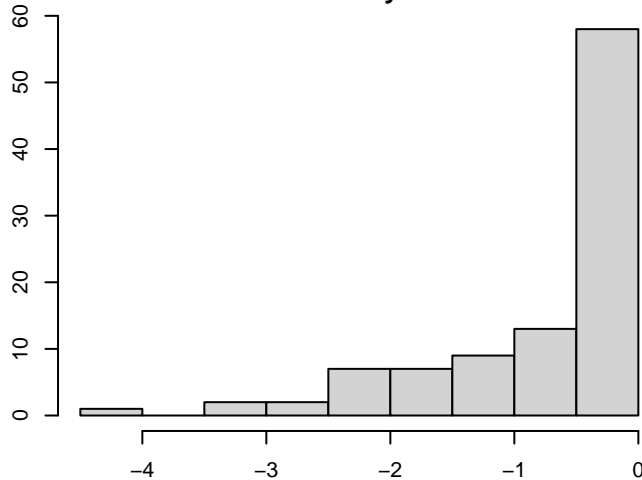
Scatter



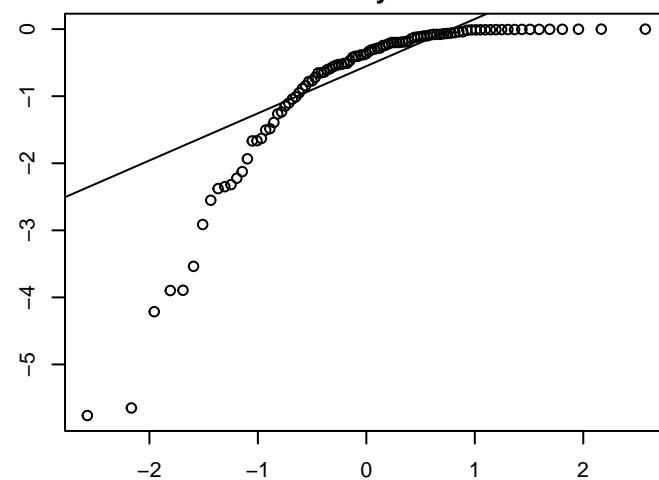
Resid y1



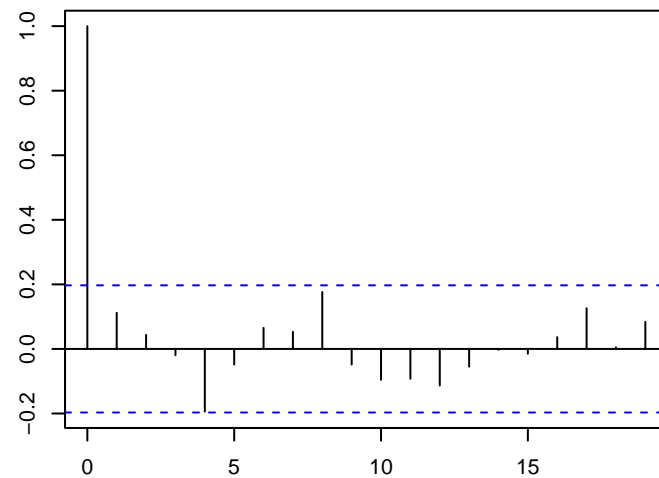
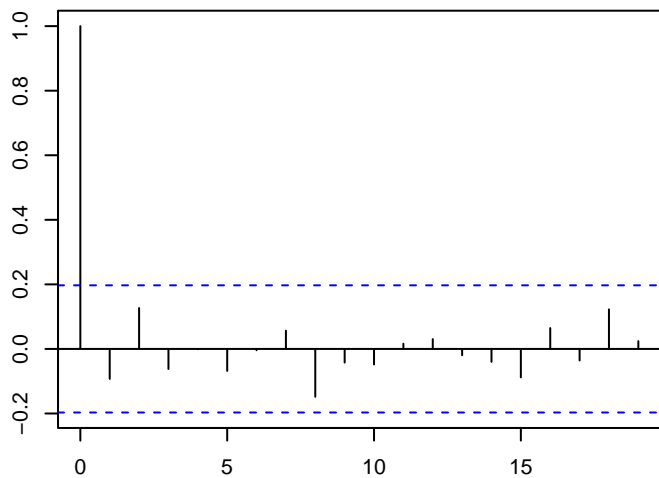
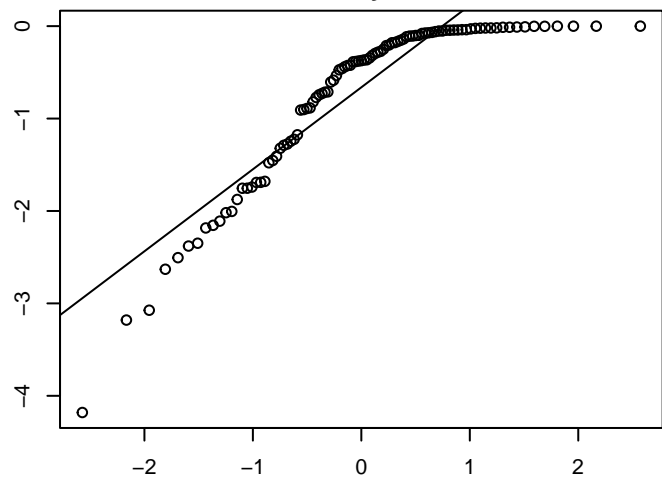
Resid y2



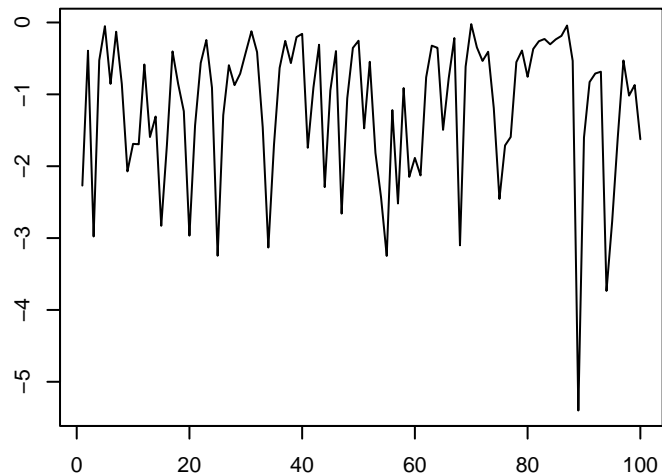
QQ y1



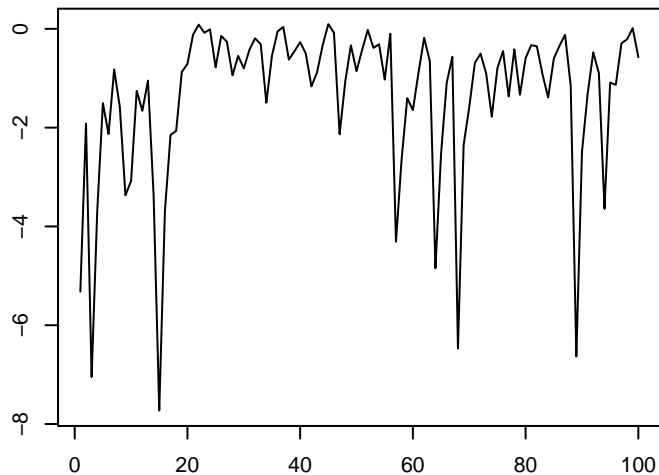
QQ y2



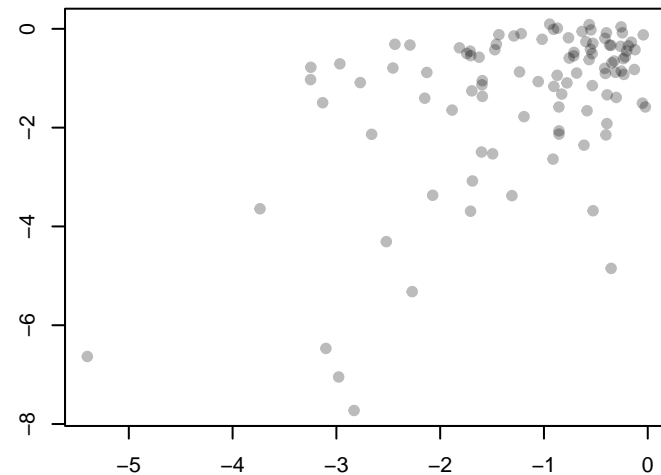
Series y1



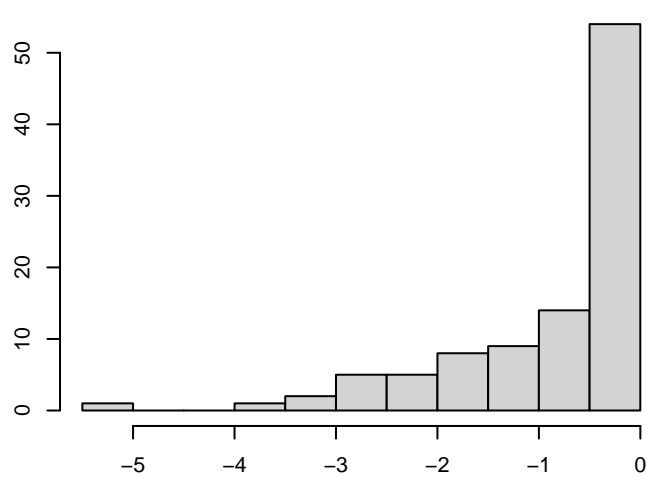
Cond 081 (chisq) | Series y2 | Rep 34



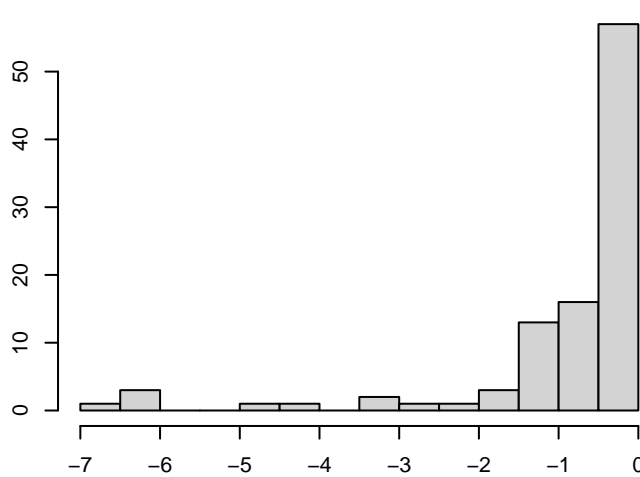
Scatter



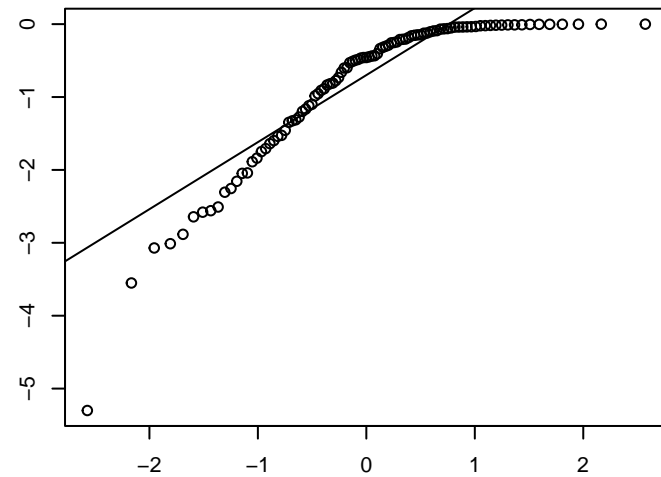
Resid y1



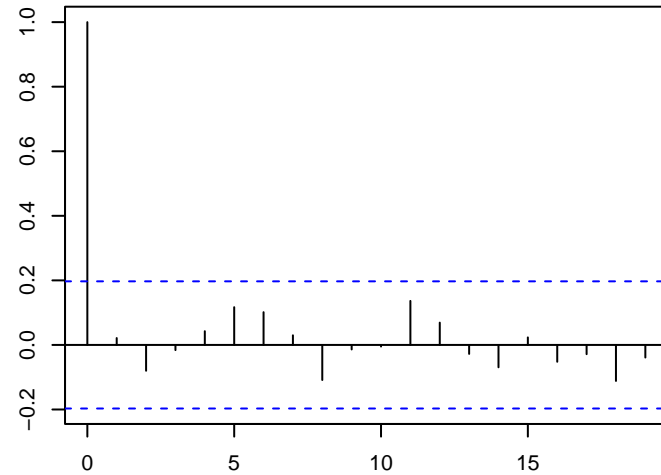
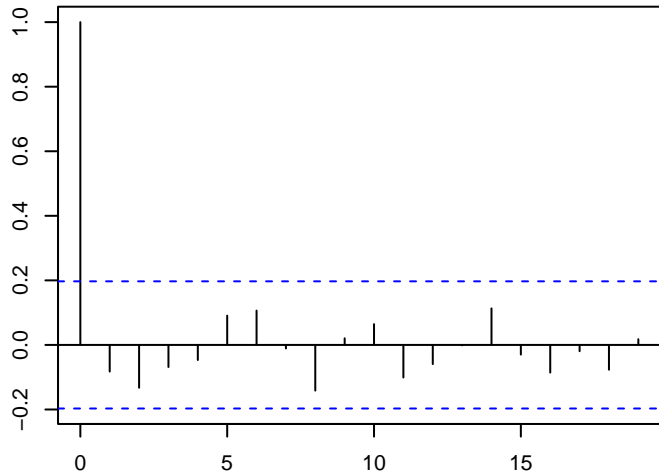
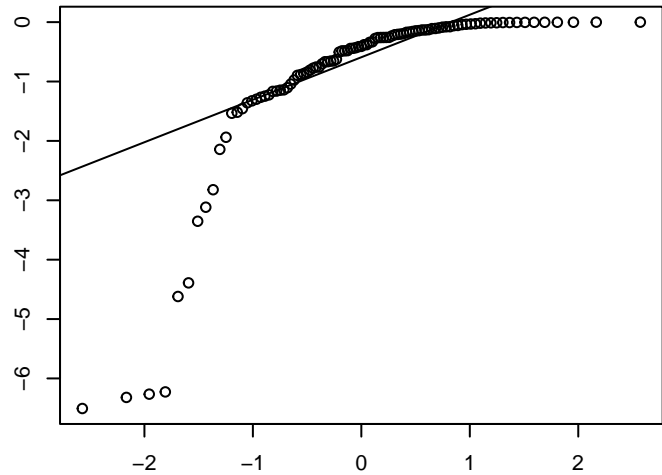
Resid y2



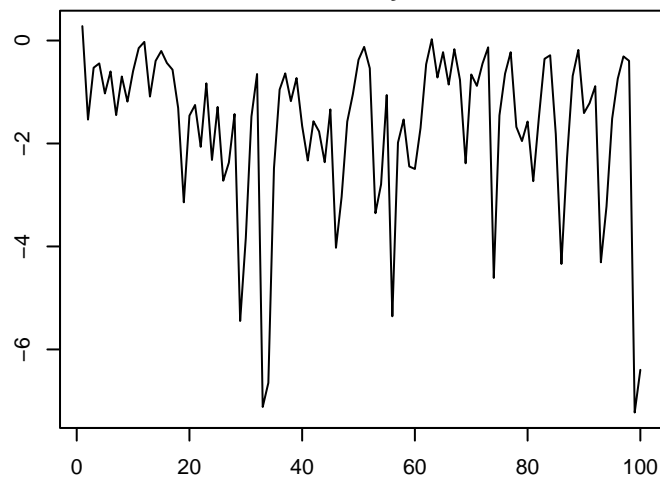
QQ y1



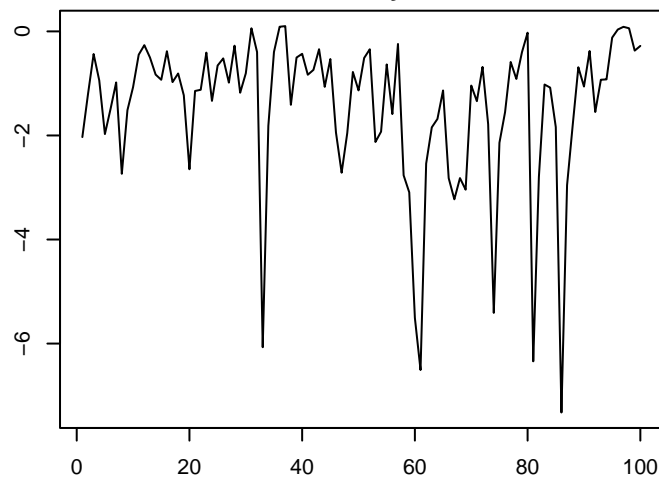
QQ y2



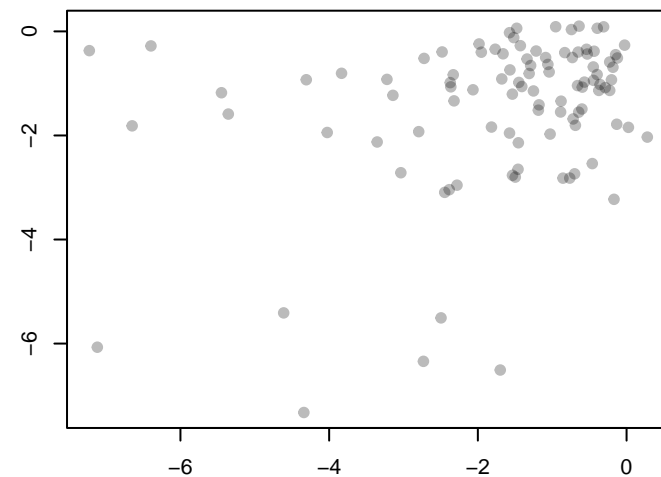
Series y1



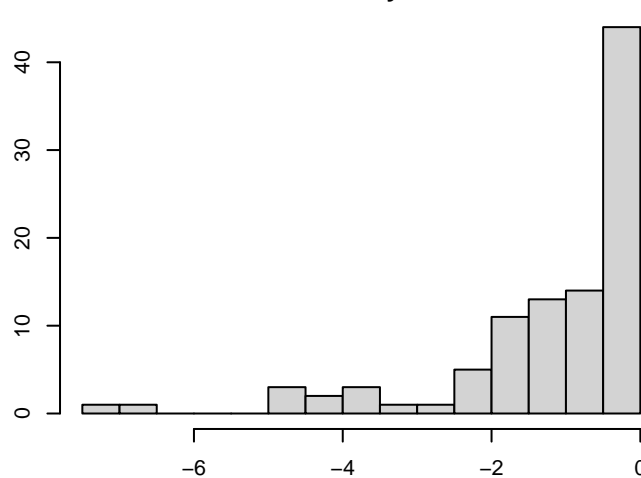
Cond 081 (chisg) Series y2 Rep 40



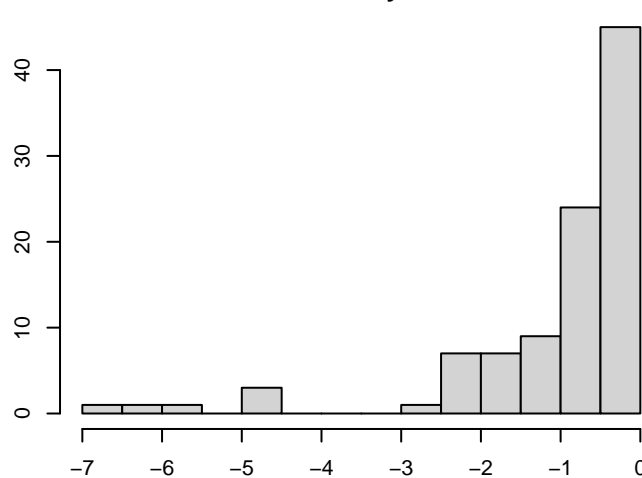
Scatter



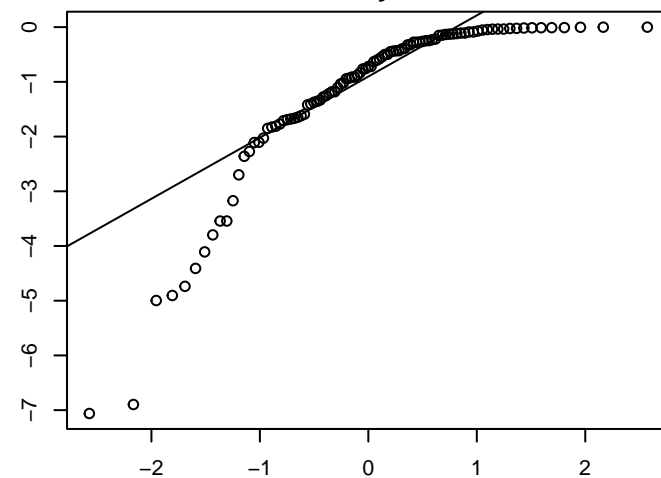
Resid y1



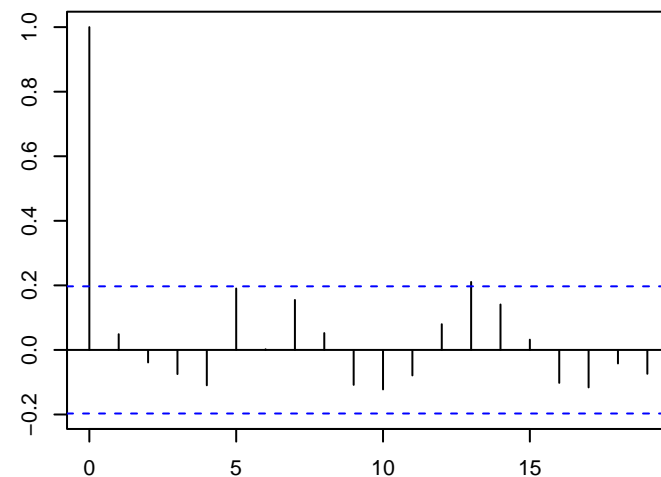
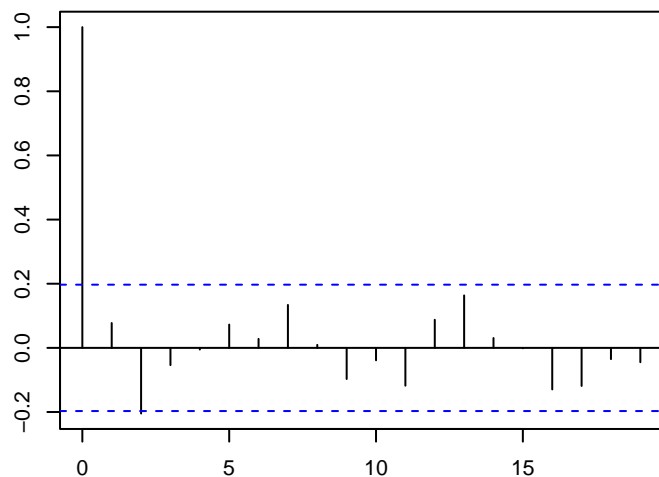
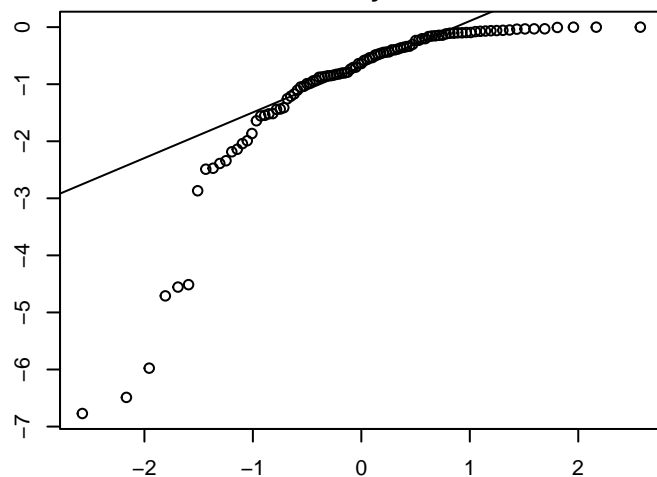
Resid y2



QQ y1

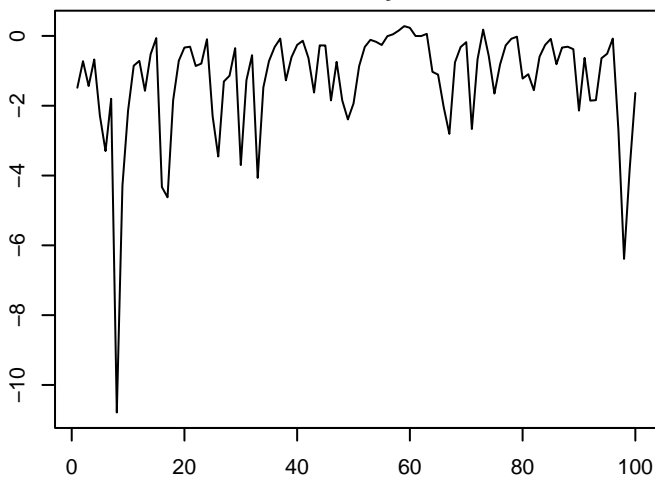


QQ y2

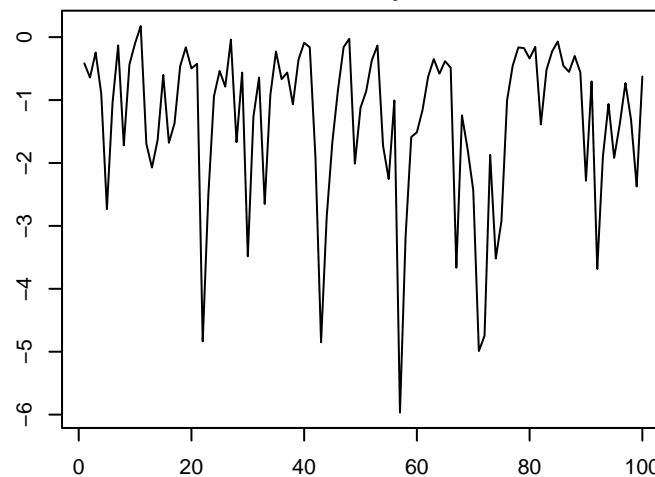


# Cond 081 (chisg)\_2 Rep 17

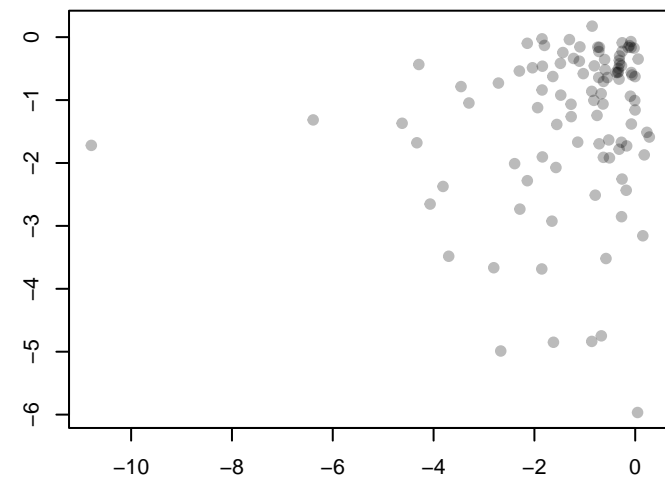
Series y1



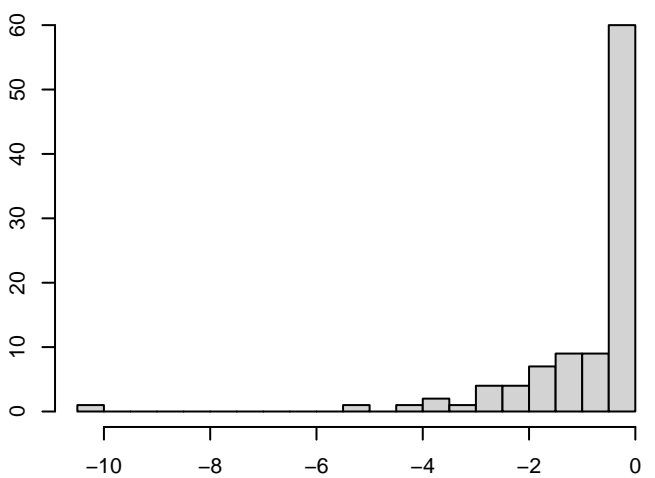
Series y2



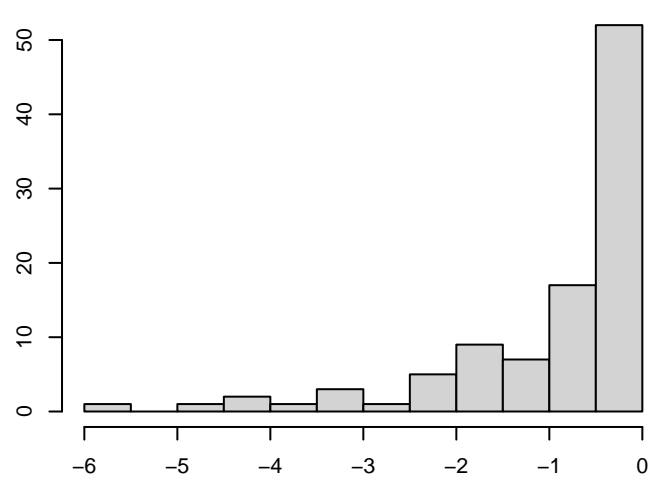
Scatter



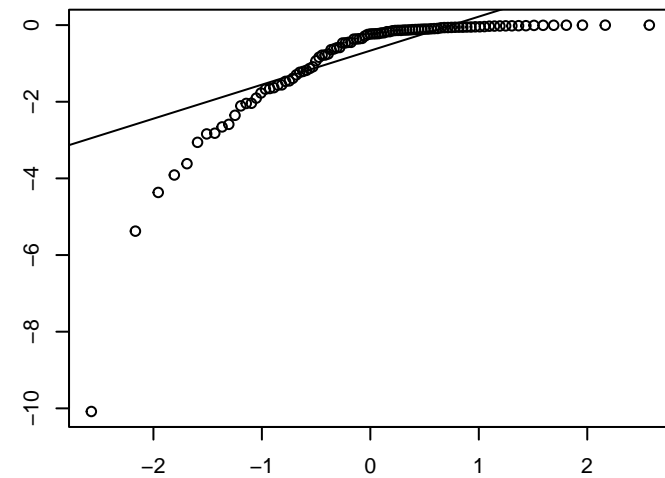
Resid y1



Resid y2



QQ y1



QQ y2

