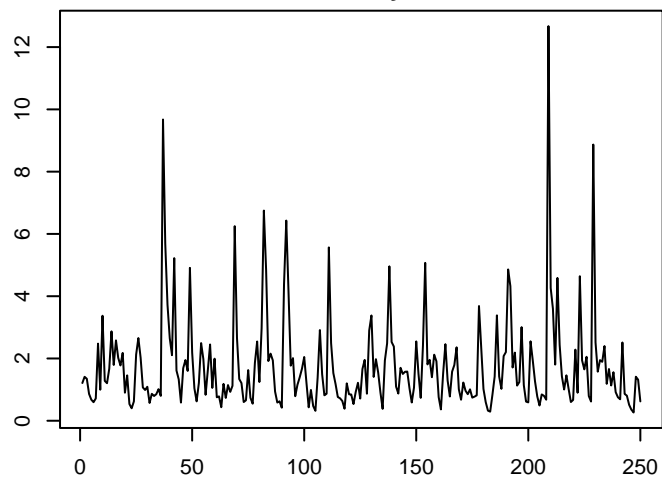
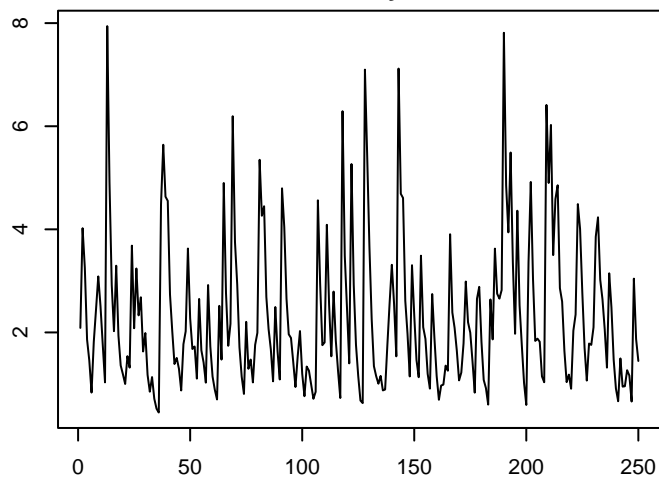


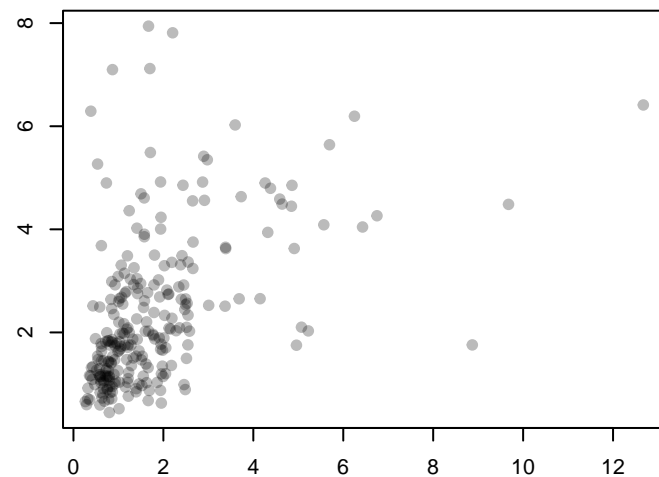
Series y1



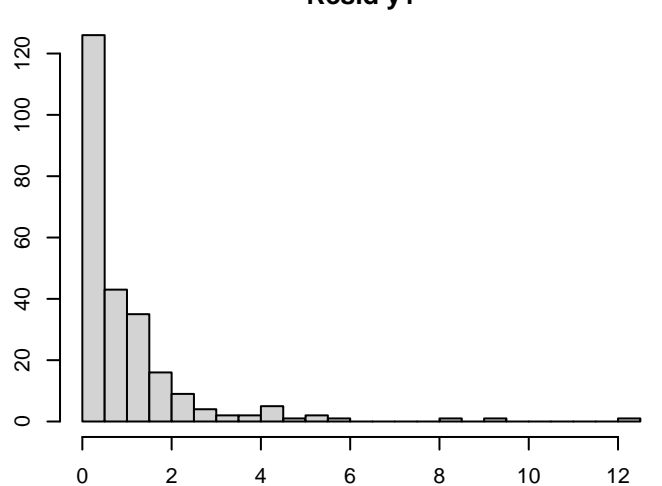
Cond 192 (chisq) | Series y2 | Rep 5



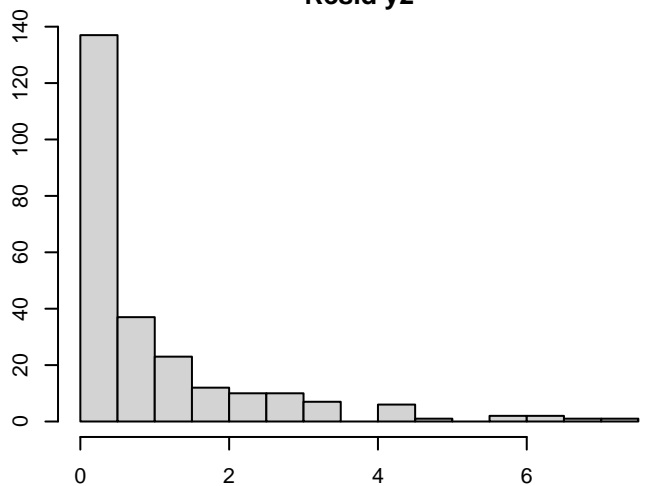
Scatter



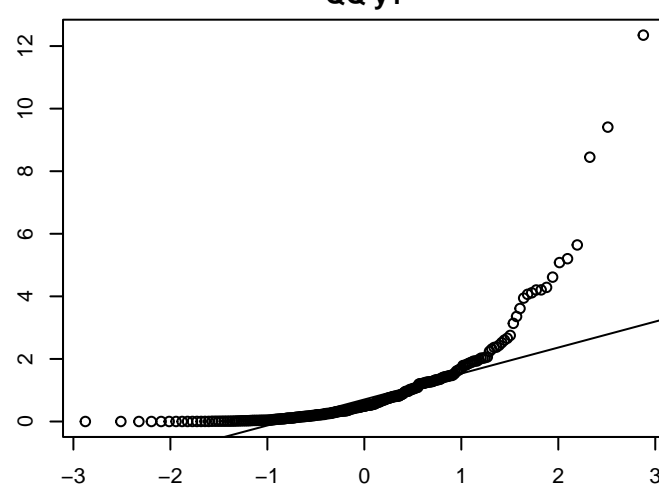
Resid y1



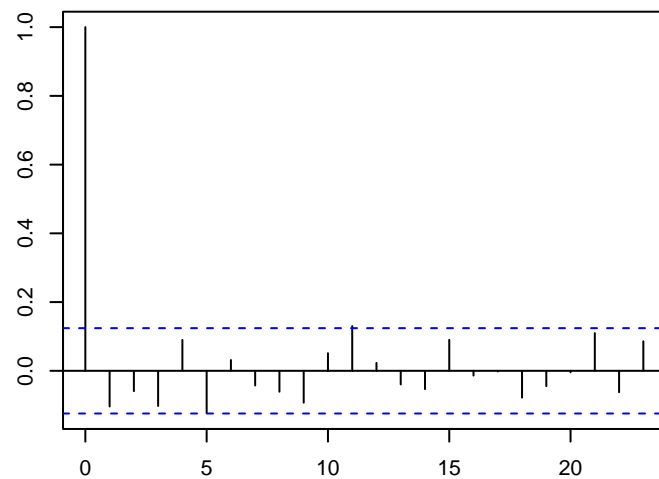
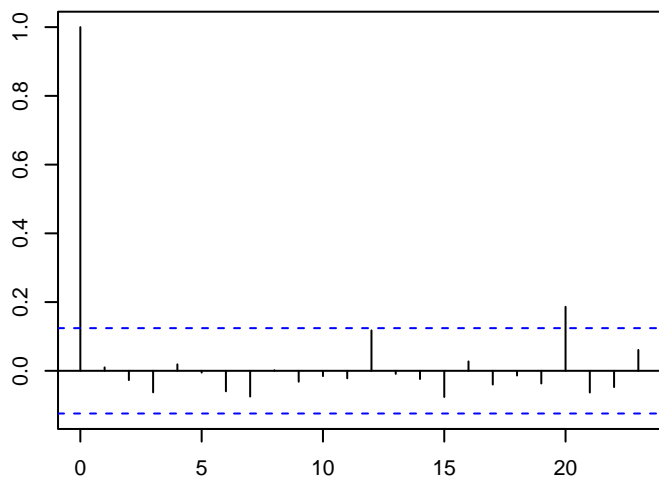
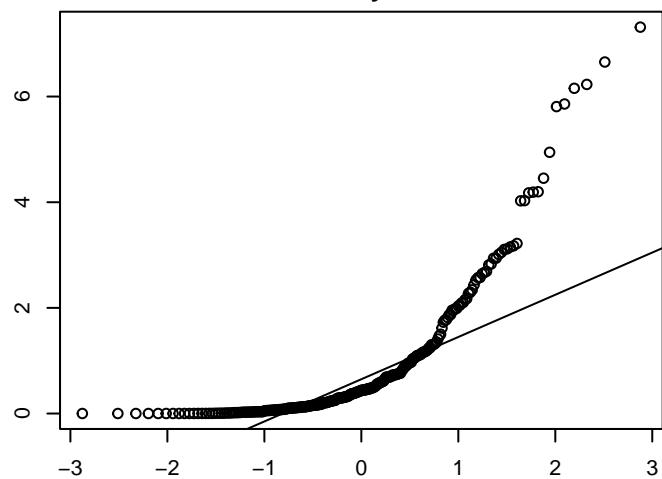
Resid y2



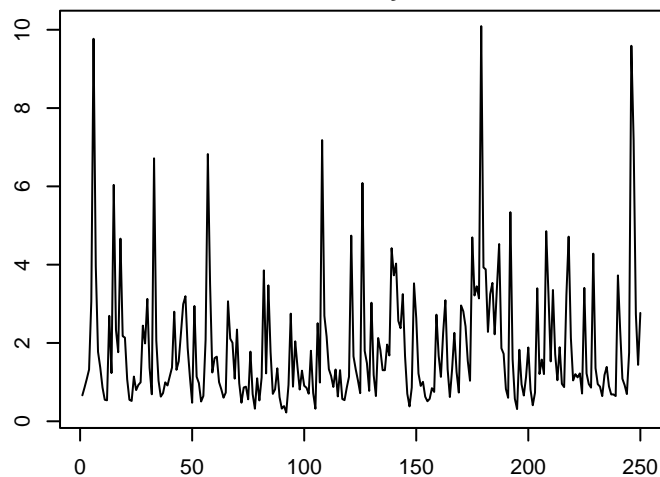
QQ y1



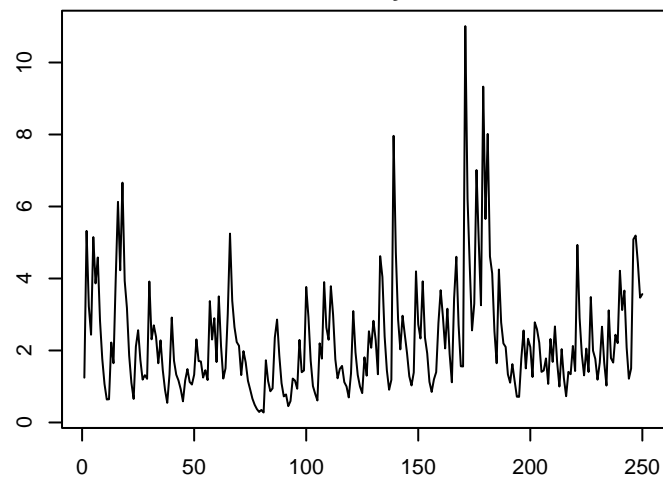
QQ y2



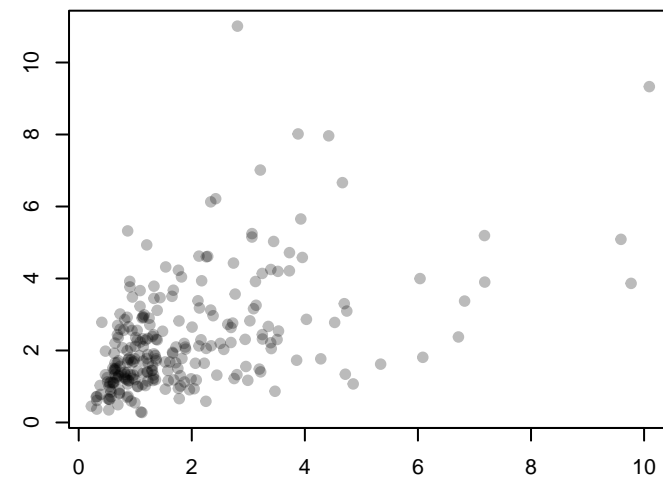
Series y1



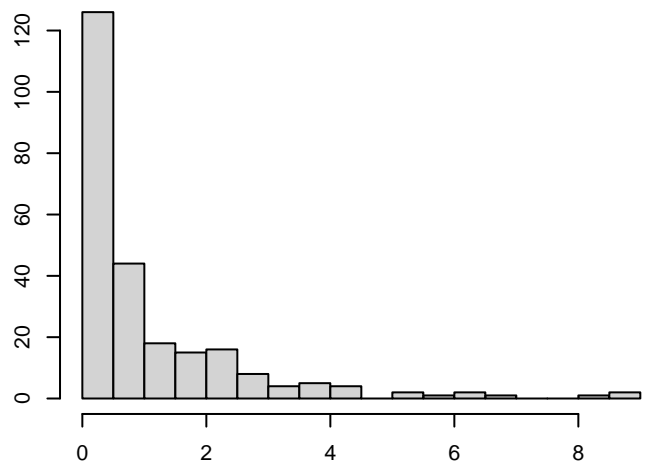
Cond 192 (chisq) | Series y2 | Rep 1



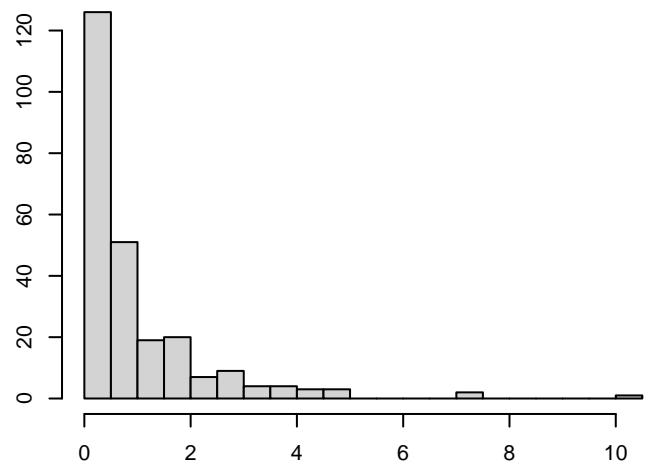
Scatter



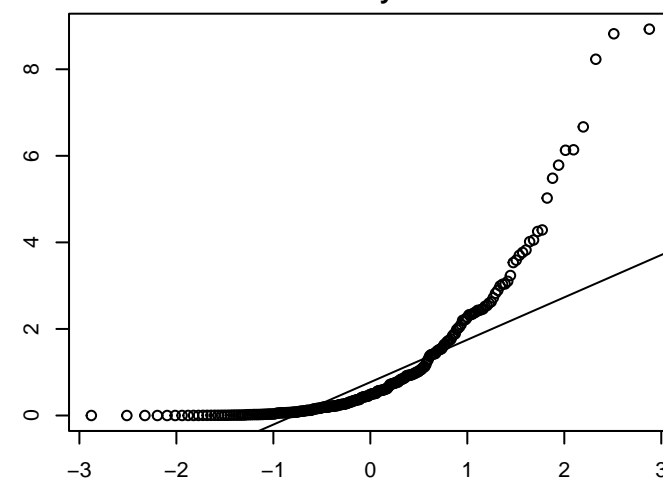
Resid y1



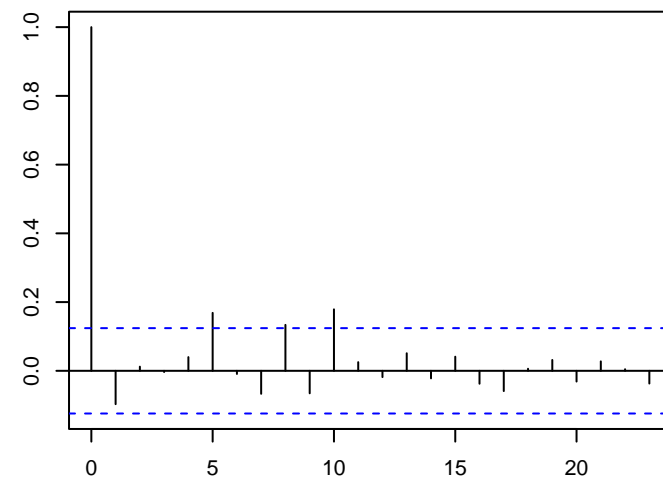
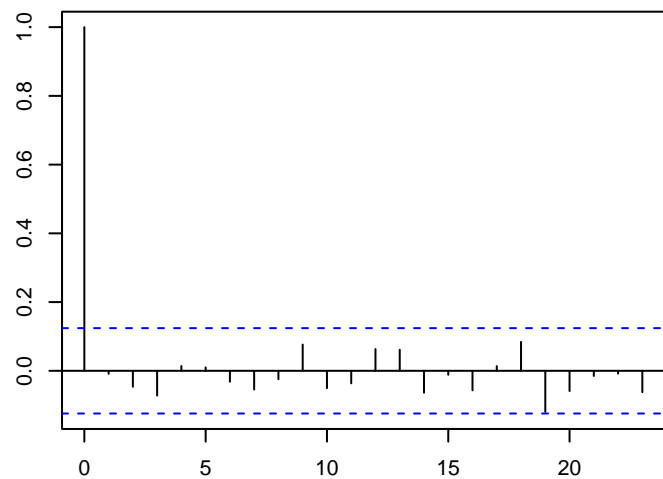
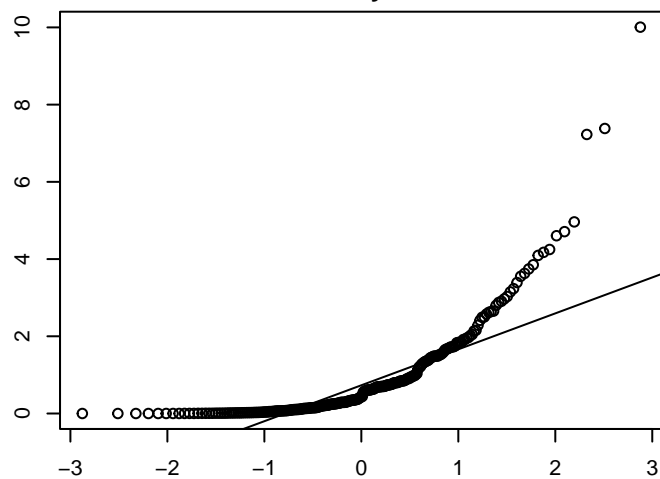
Resid y2



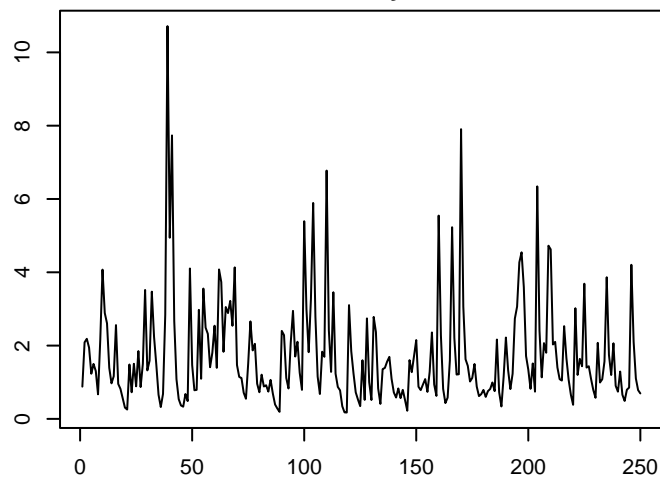
QQ y1



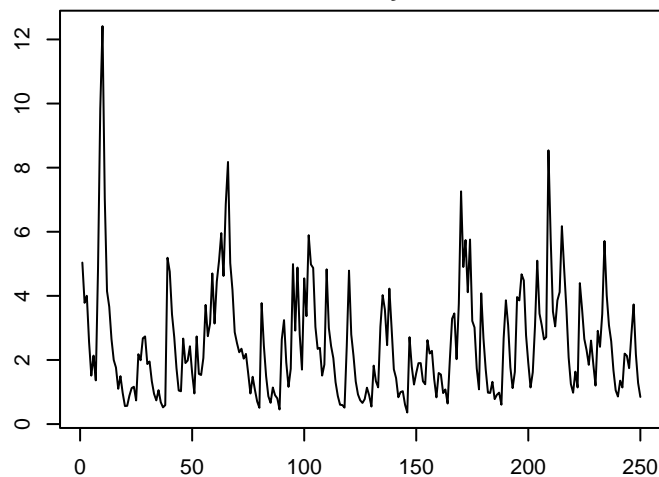
QQ y2



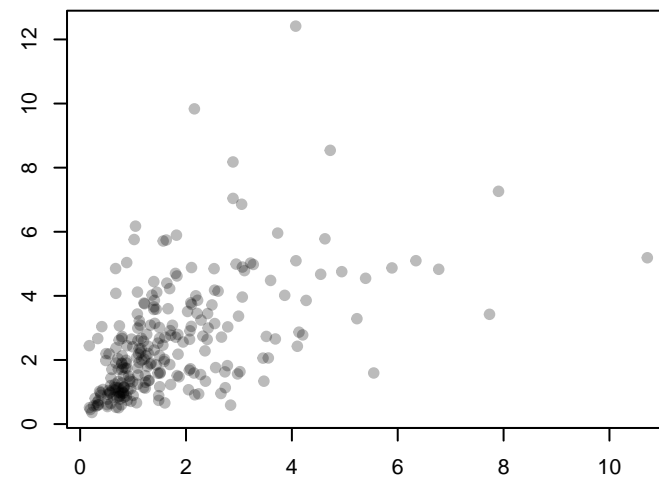
Series y1



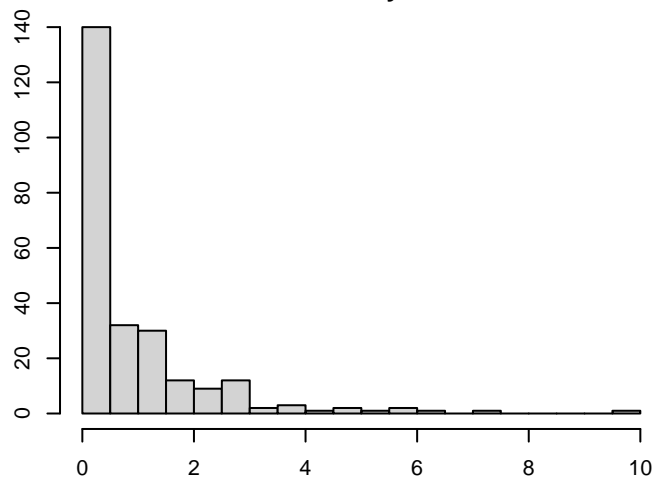
Cond 192 (chisg)_2 | Series y2 | Rep 39



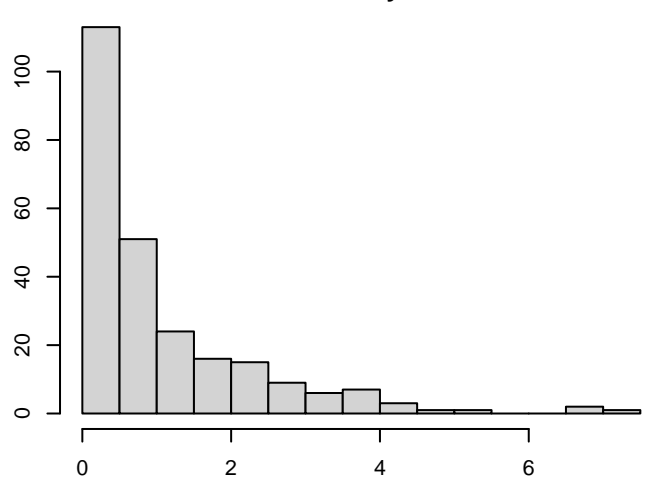
Scatter



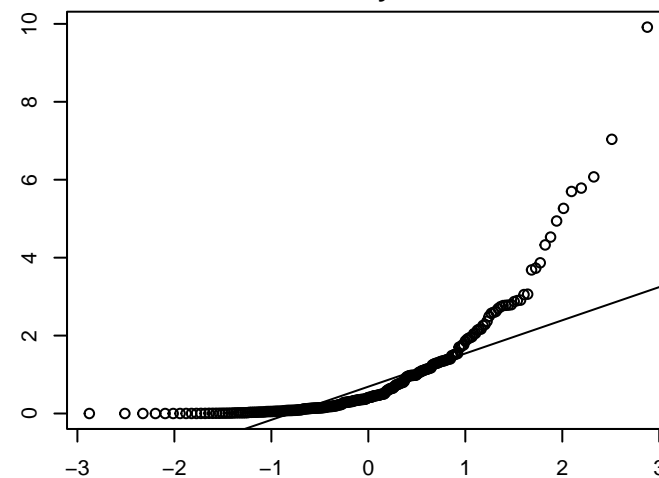
Resid y1



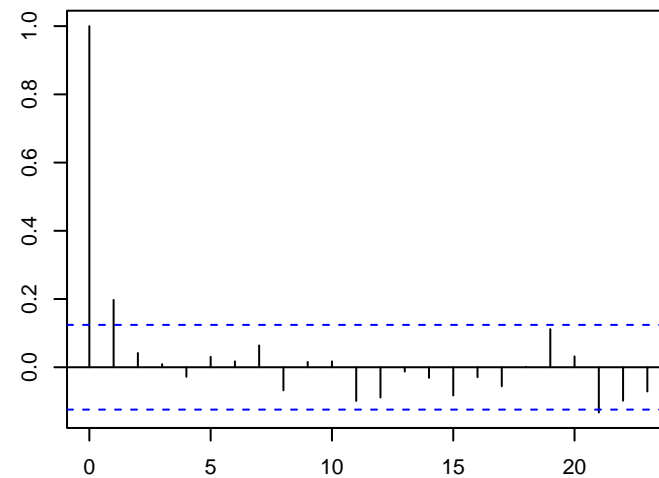
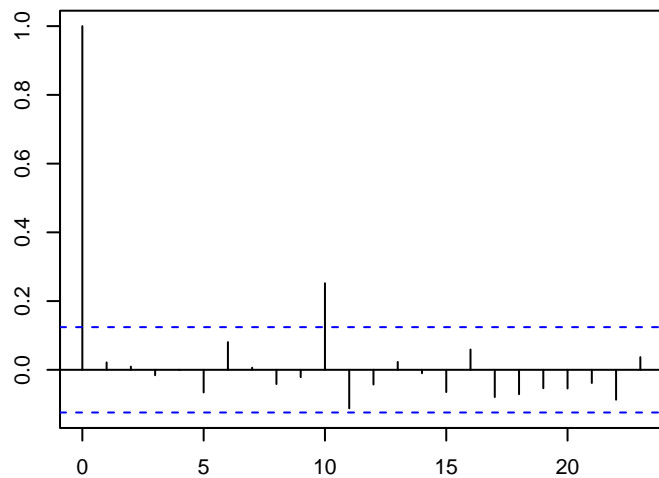
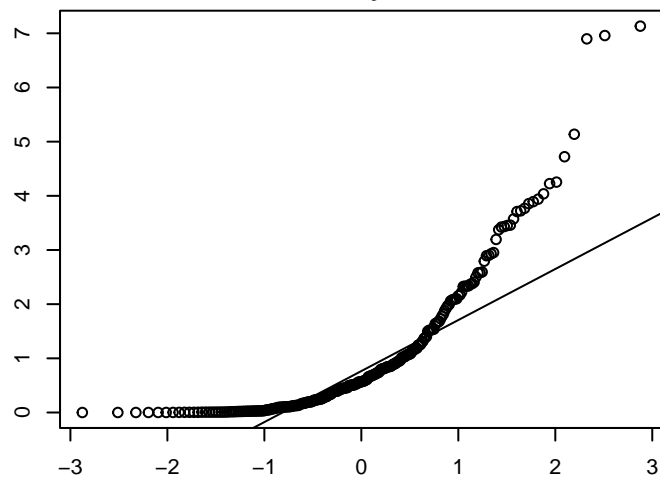
Resid y2



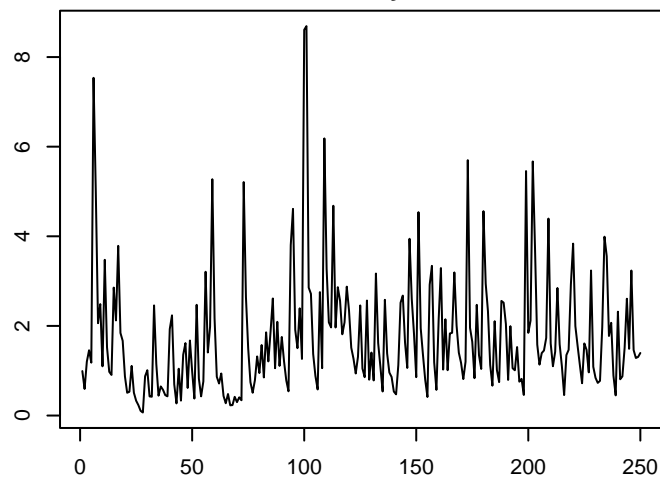
QQ y1



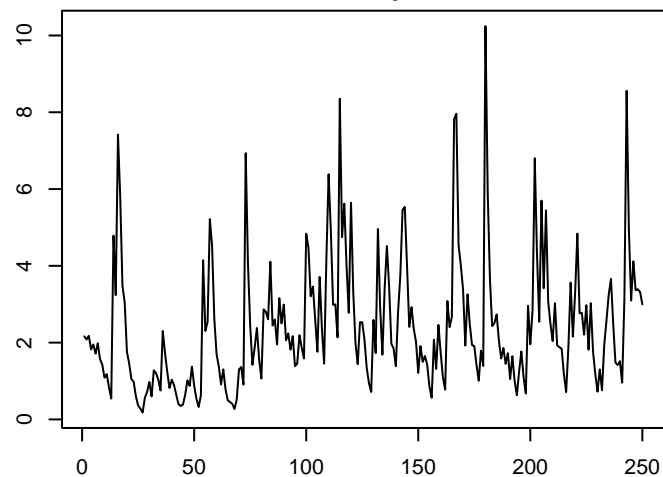
QQ y2



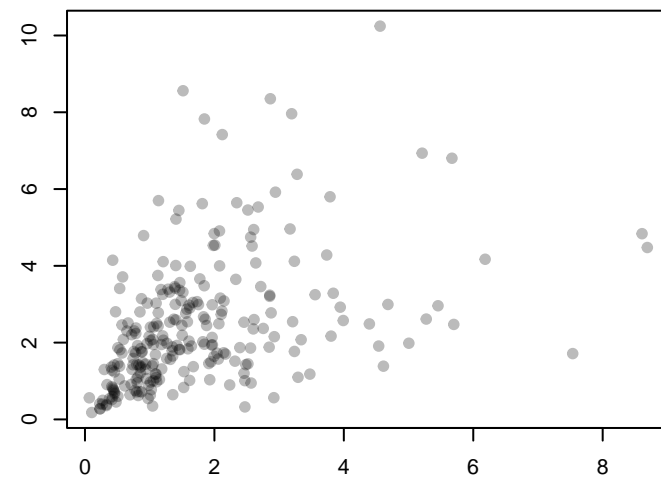
Series y1



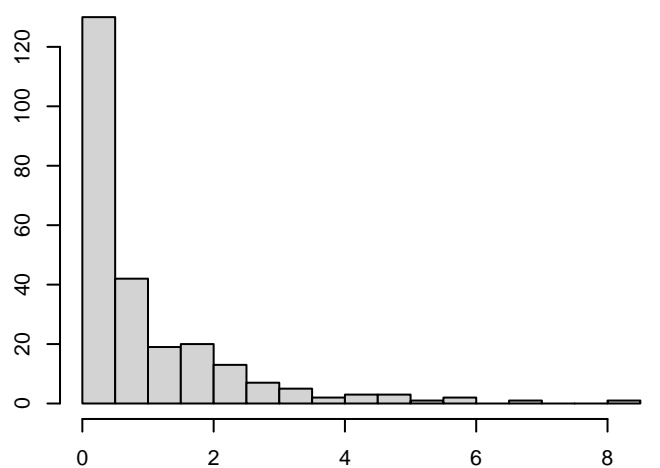
Cond 192 (chisg) | Series y2 | Rep 32



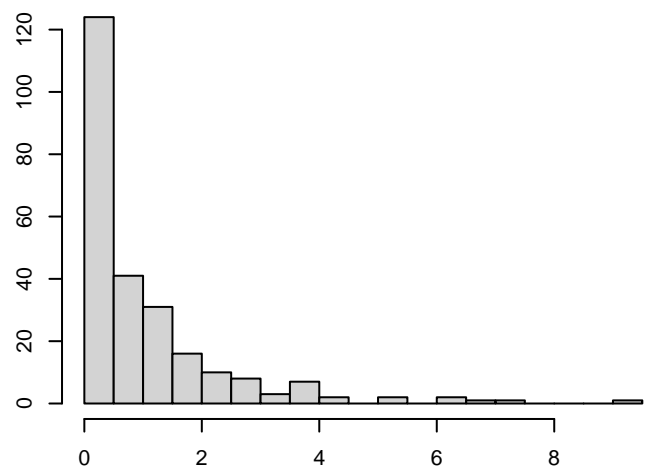
Scatter



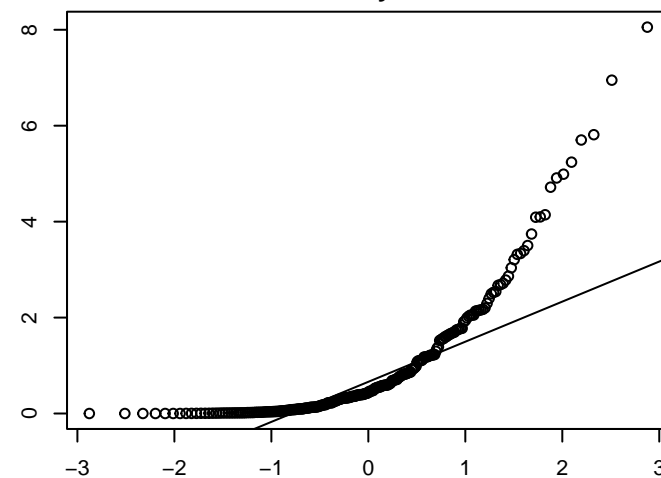
Resid y1



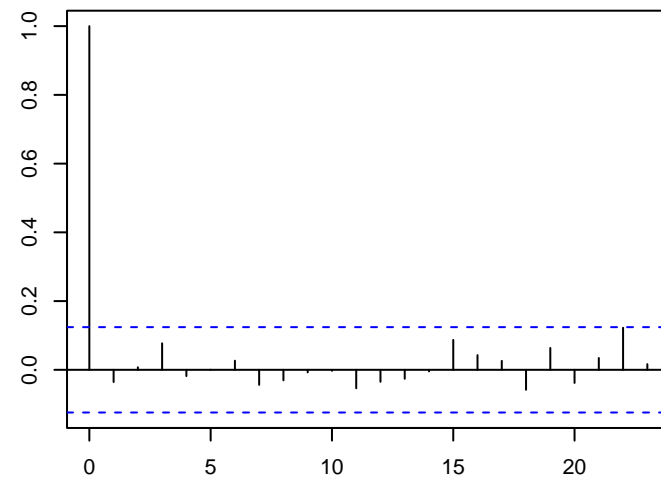
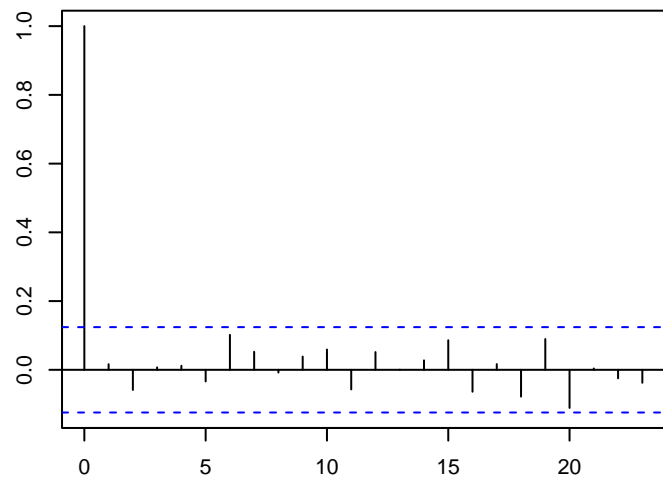
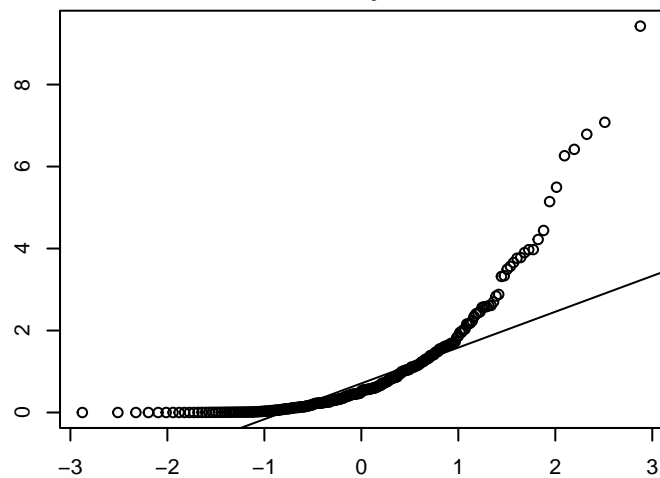
Resid y2



QQ y1

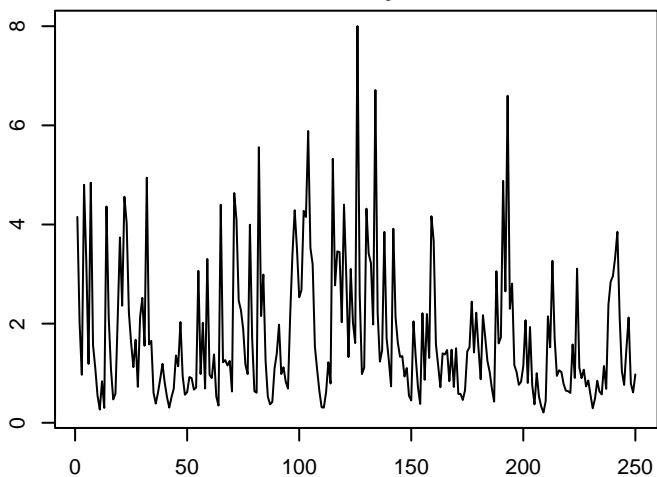


QQ y2

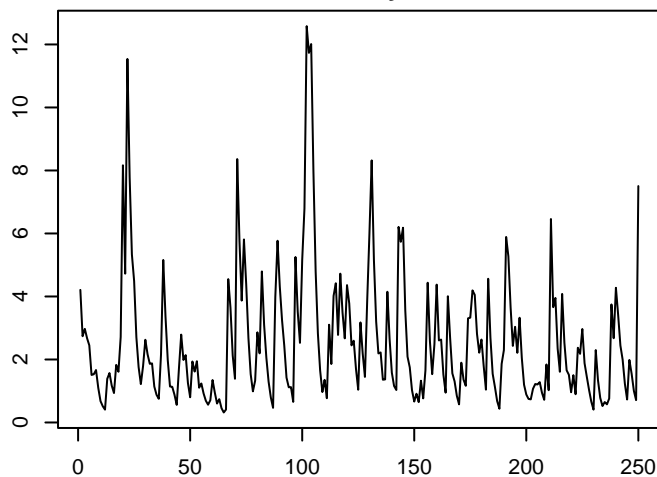


Cond 192 (chisg) | Rep 29

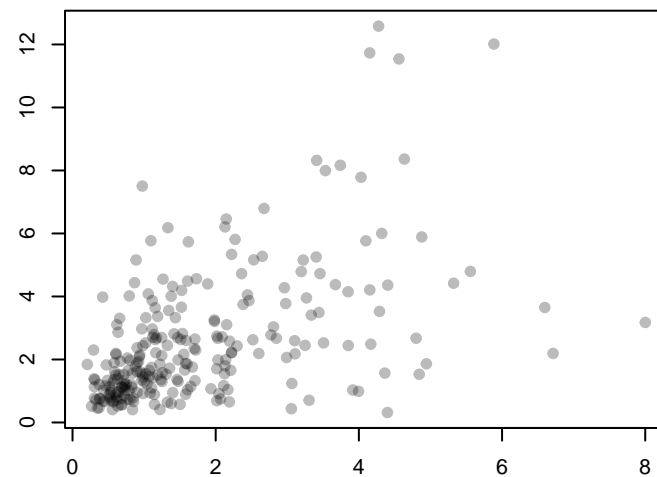
Series y1



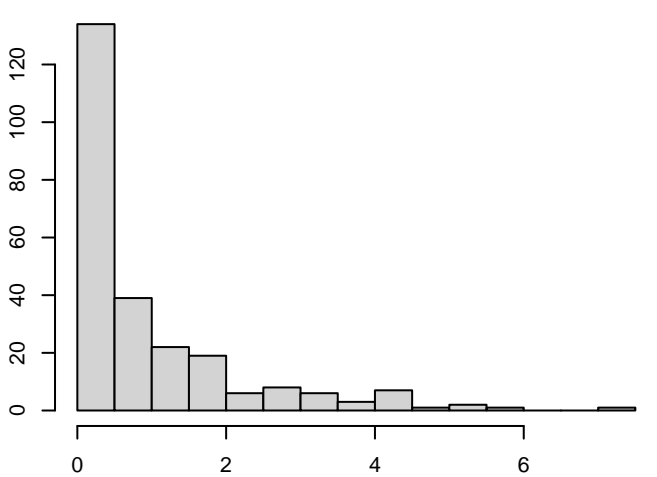
Series y2



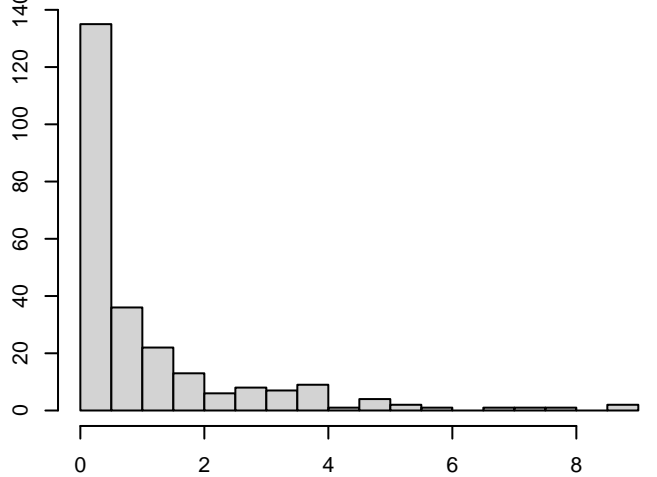
Scatter



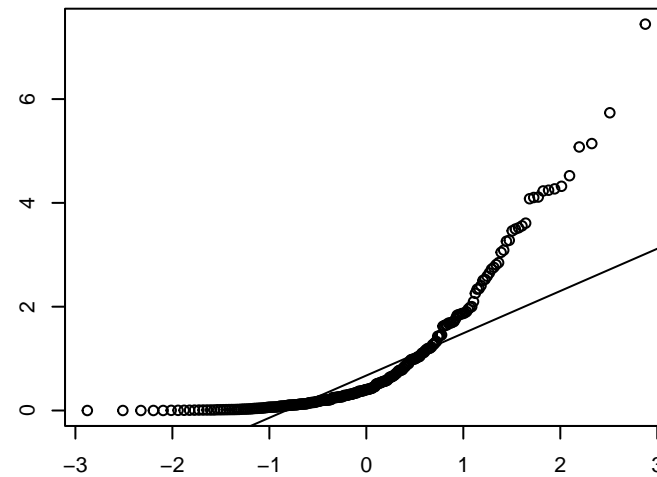
Resid y1



Resid y2



QQ y1



QQ y2

