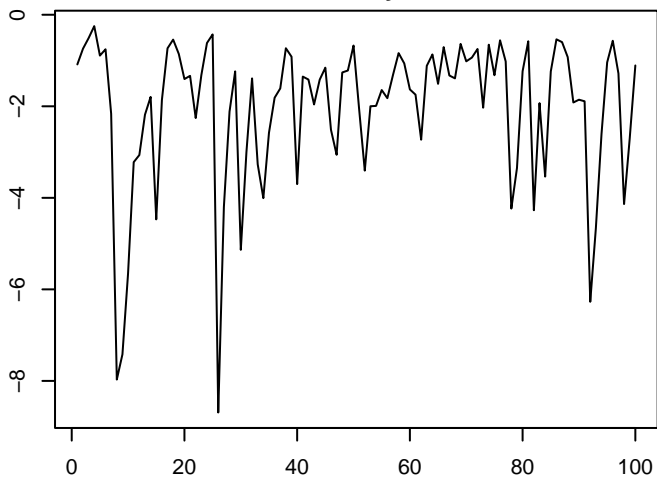
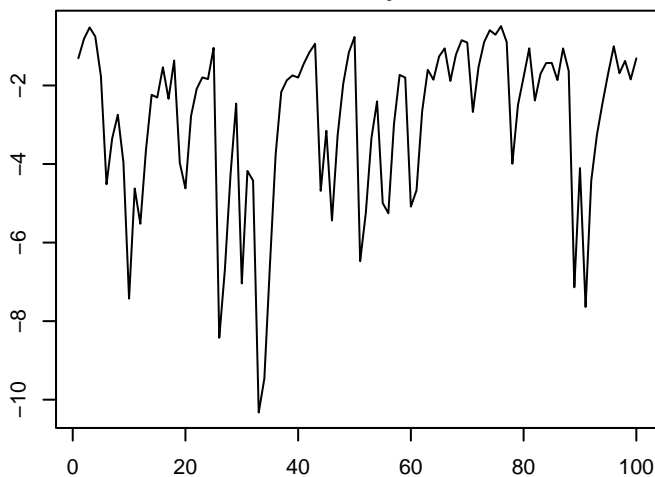


# Cond 174 (chisg) | Rep 19

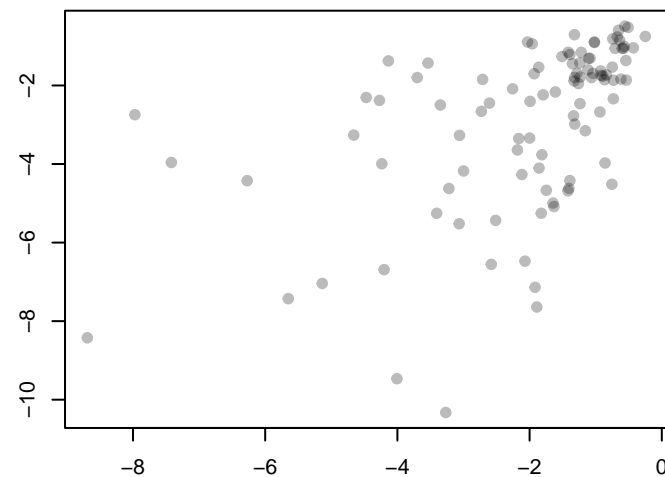
Series y1



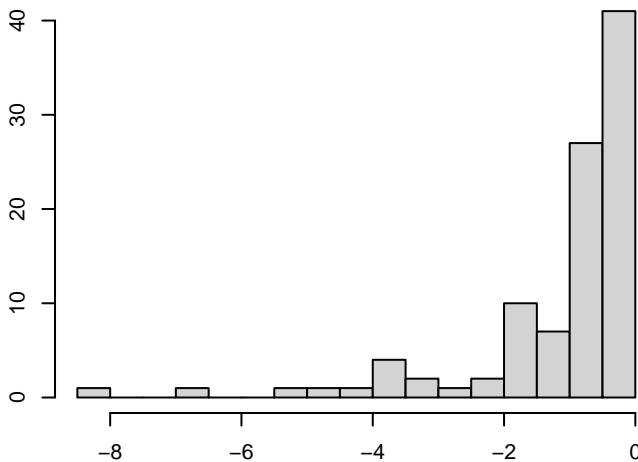
Series y2



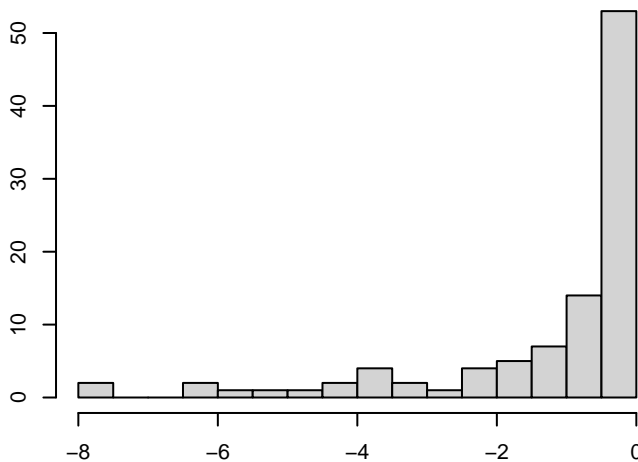
Scatter



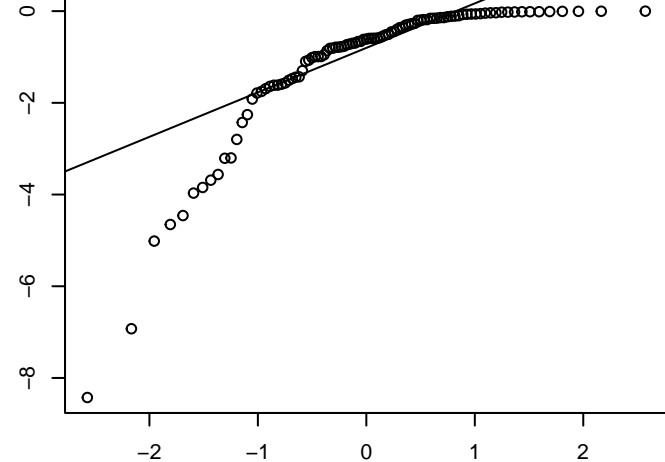
Resid y1



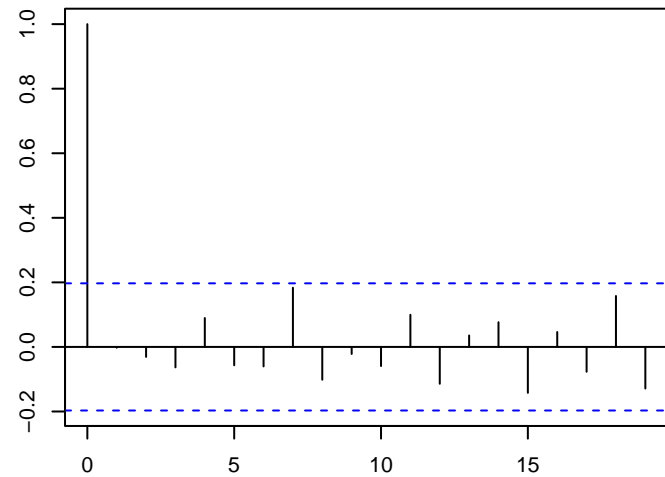
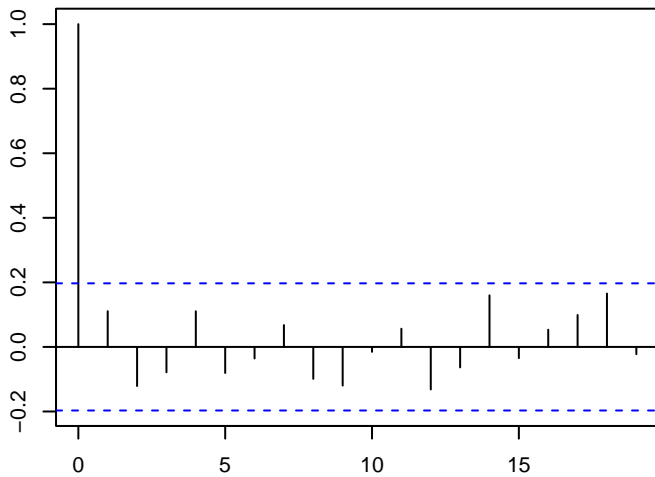
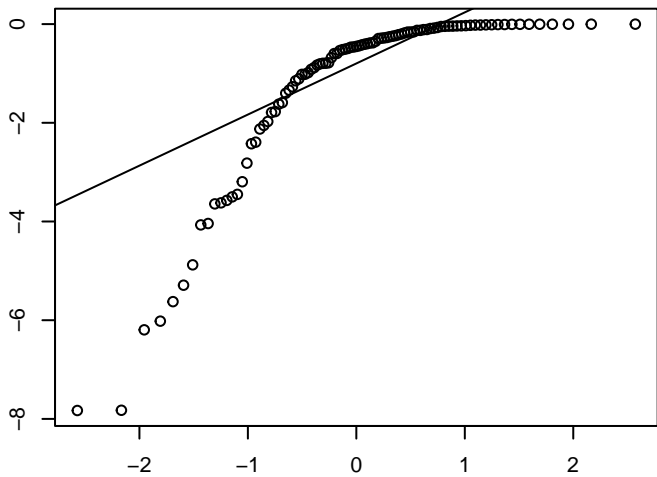
Resid y2



QQ y1

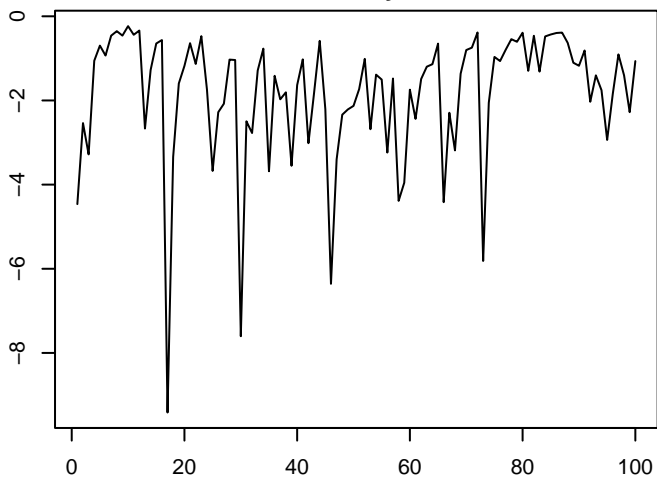


QQ y2

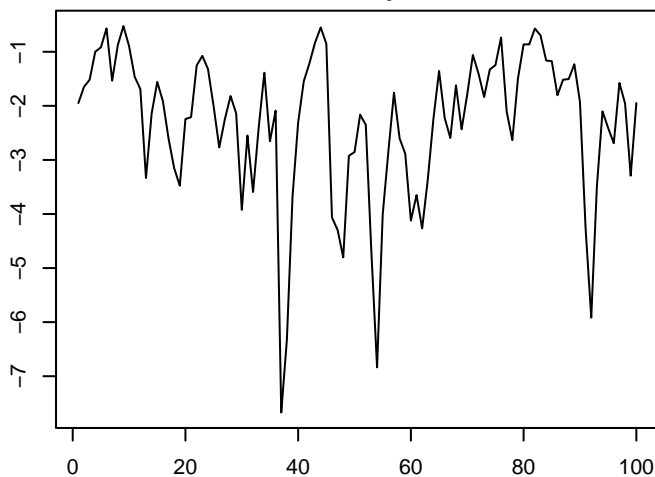


Cond 174 (chisg) | Series y2 | Rep 26

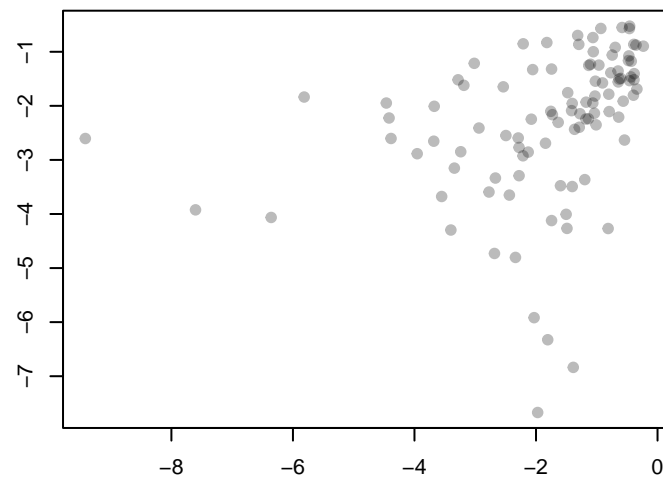
Series y1



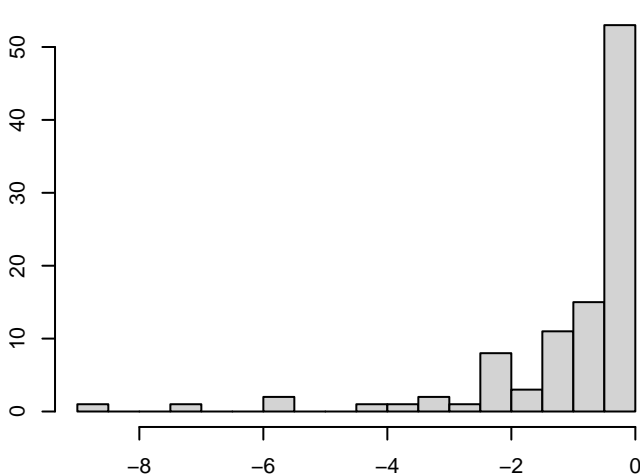
Series y2



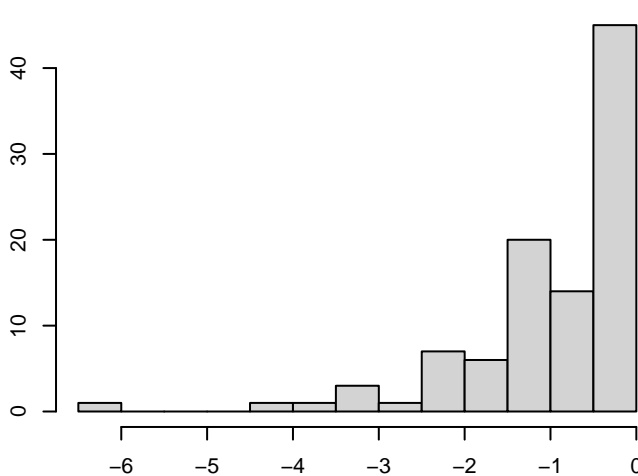
Scatter



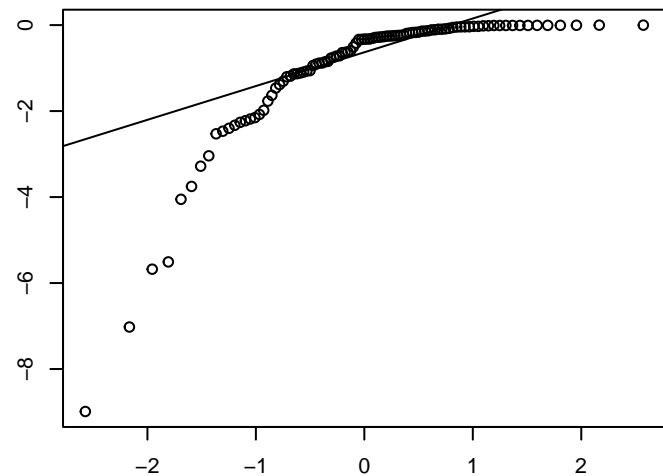
Resid y1



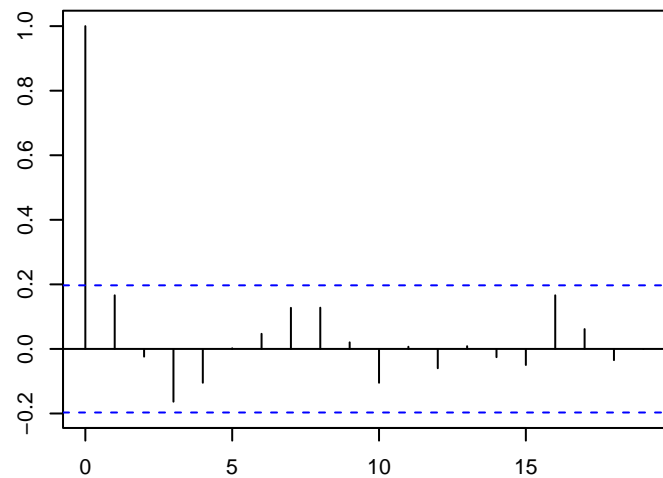
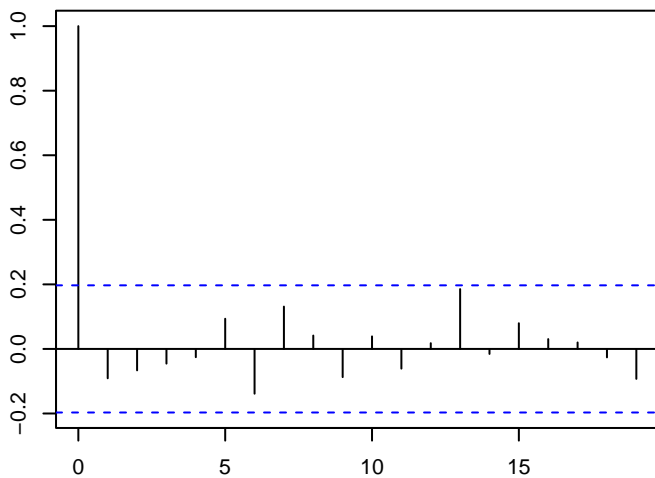
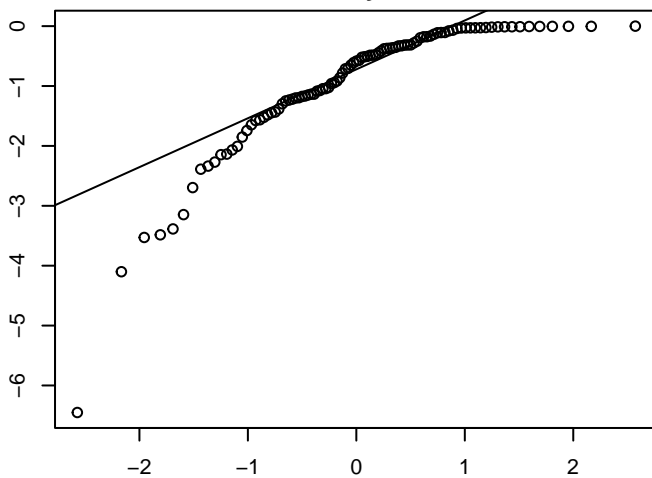
Resid y2



QQ y1

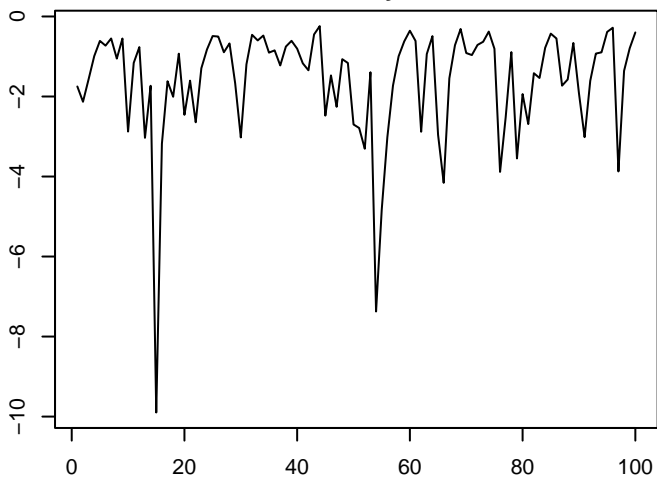


QQ y2

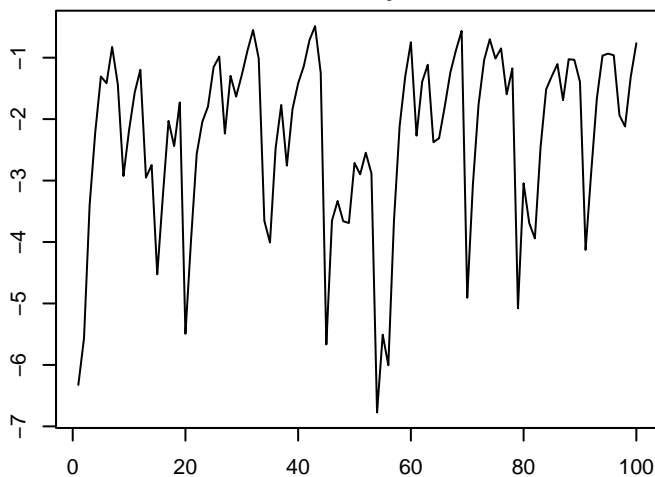


# Cond 174 (chisg)\_1 Rep 23

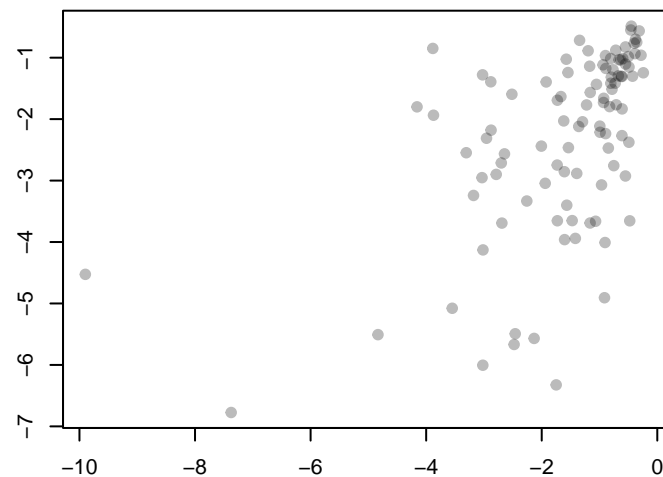
Series y1



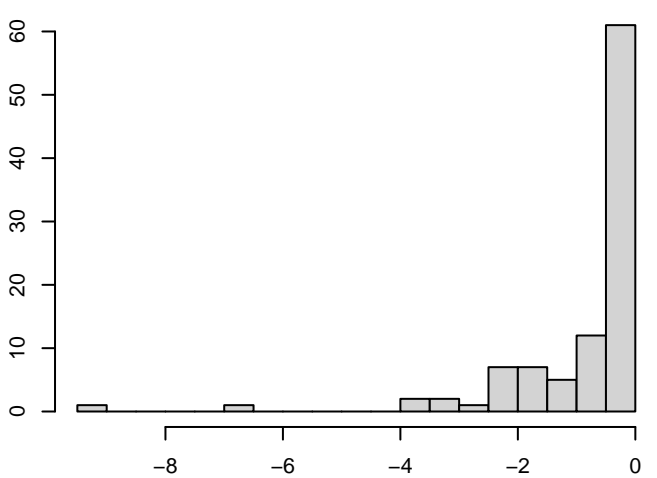
Series y2



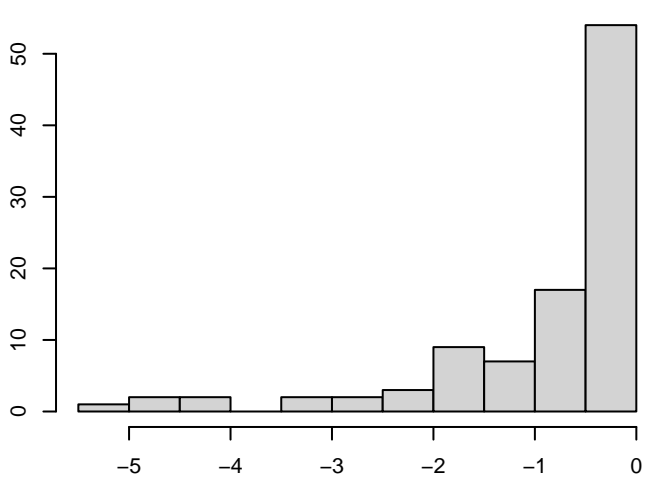
Scatter



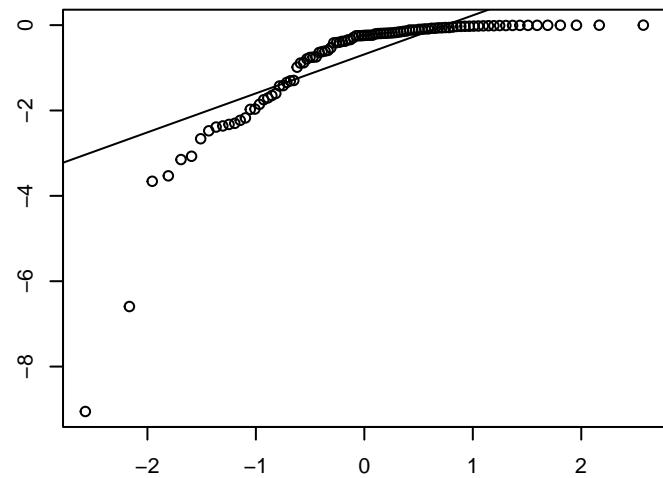
Resid y1



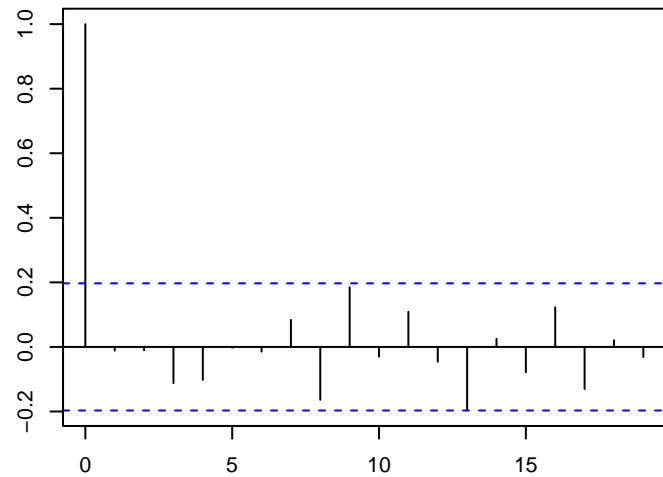
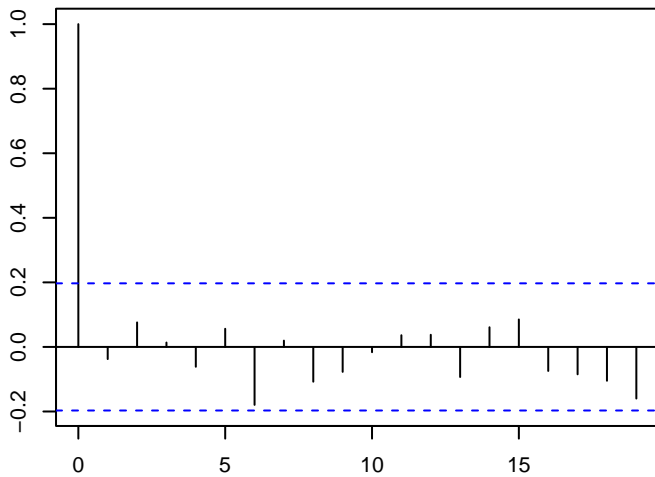
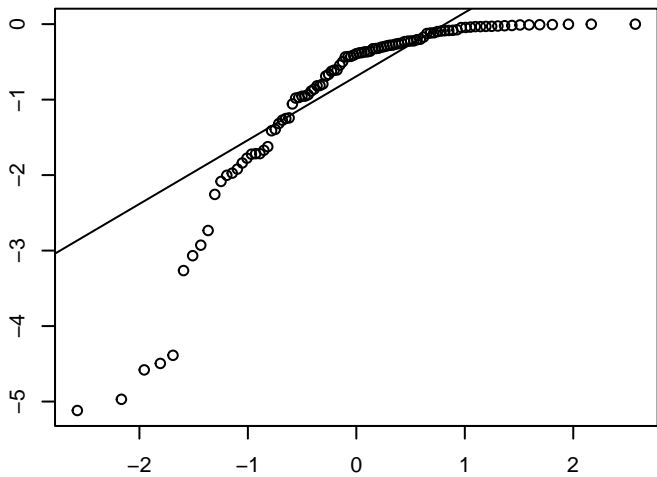
Resid y2



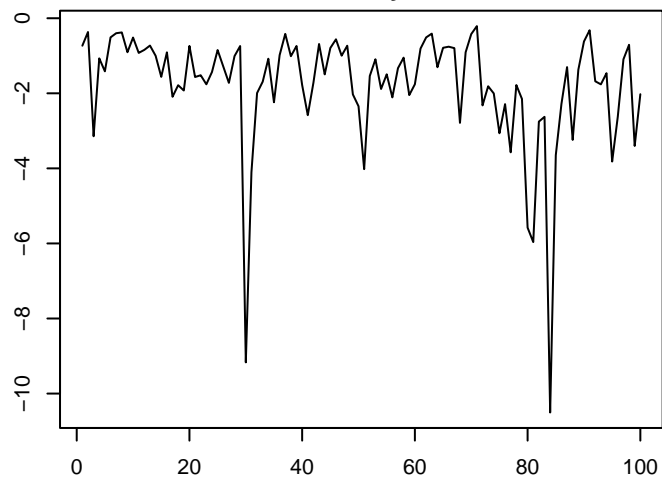
QQ y1



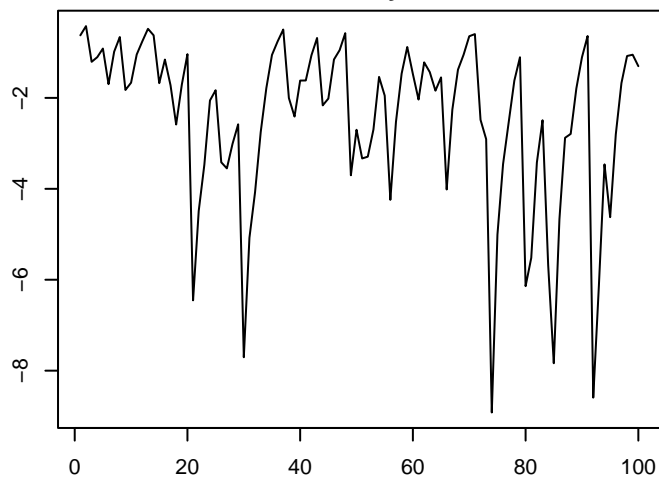
QQ y2



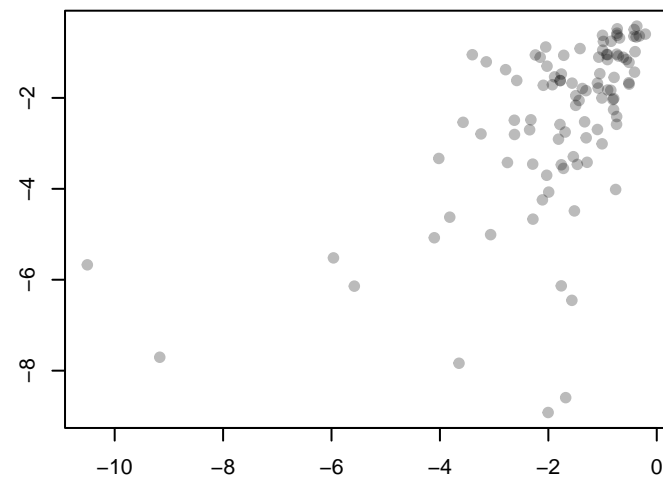
Series y1



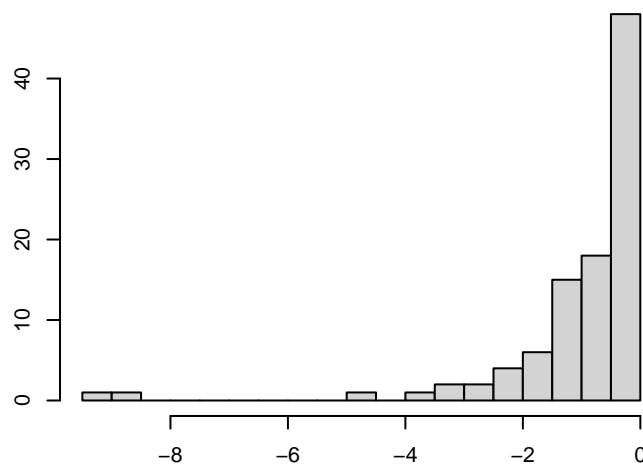
Cond 174 (chisq) | Series y2 | Rep 2



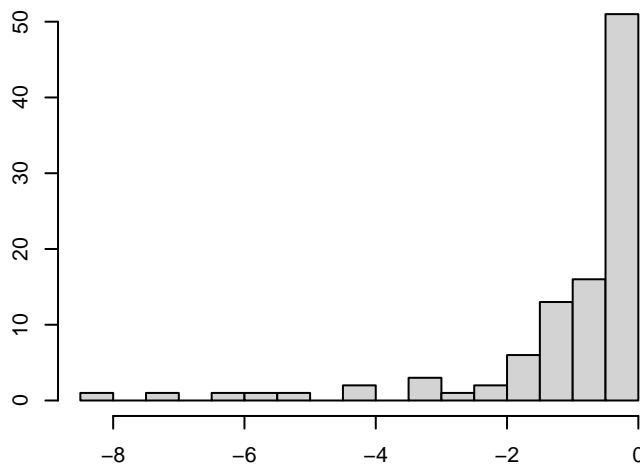
Scatter



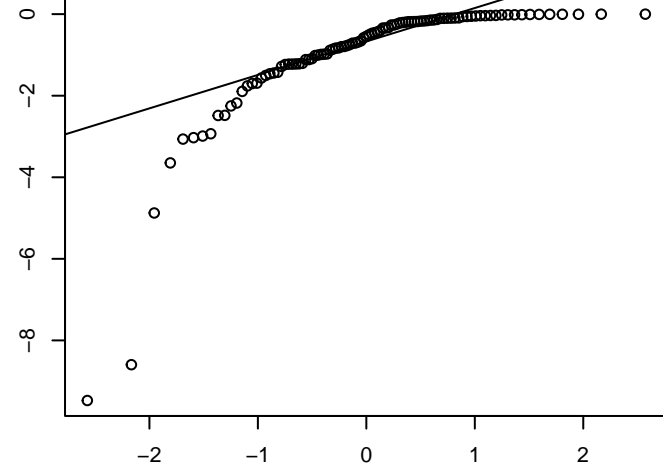
Resid y1



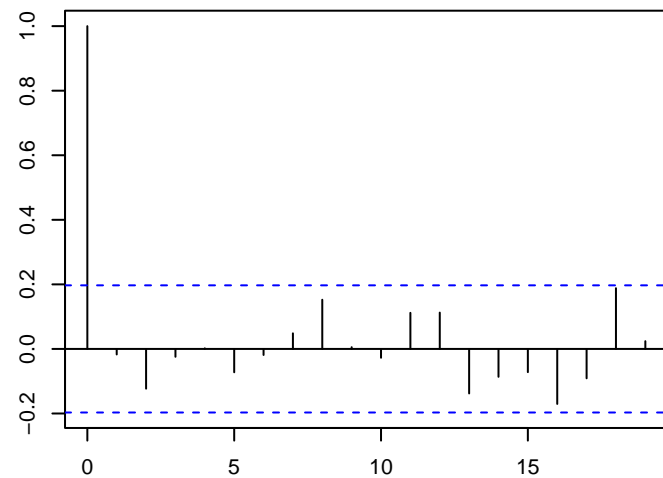
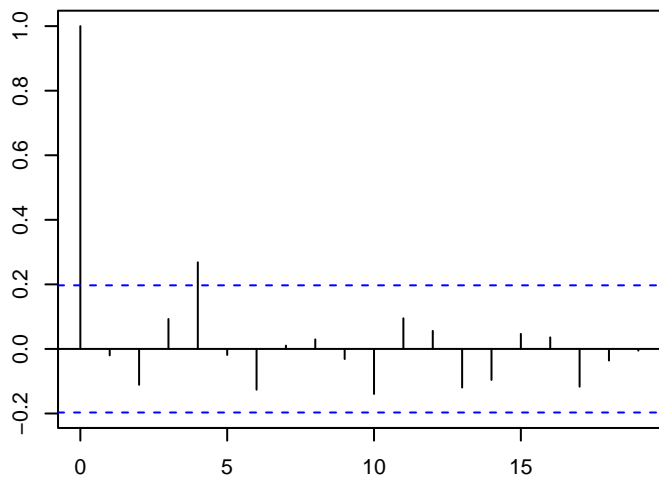
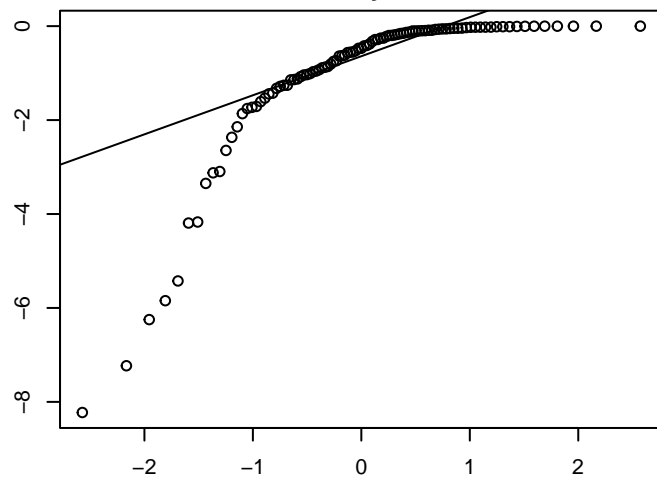
Resid y2



QQ y1

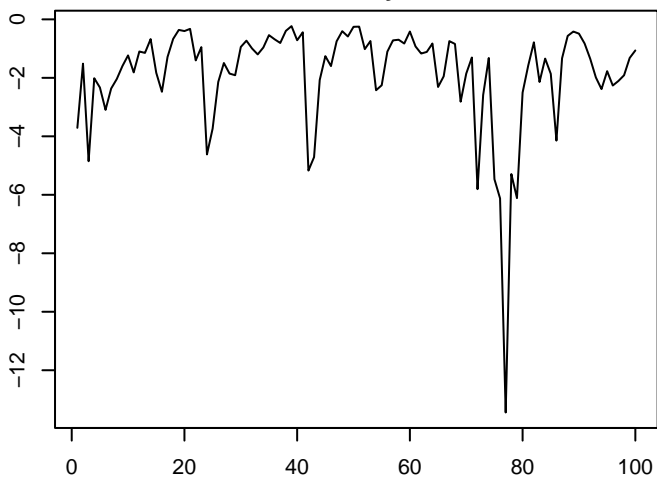


QQ y2

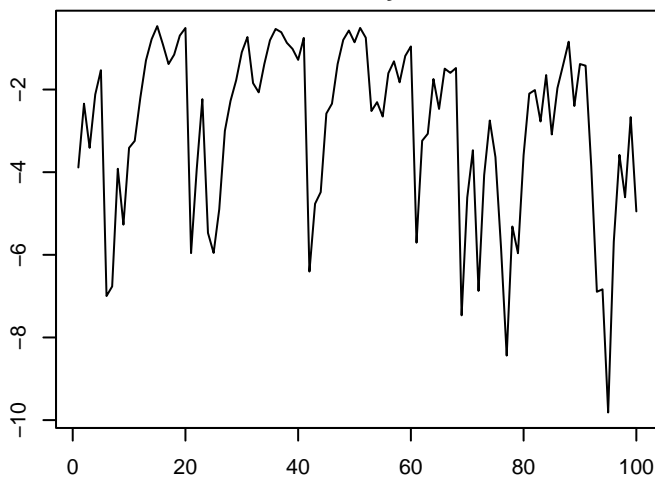


Cond 174 (chisq), Rep 16

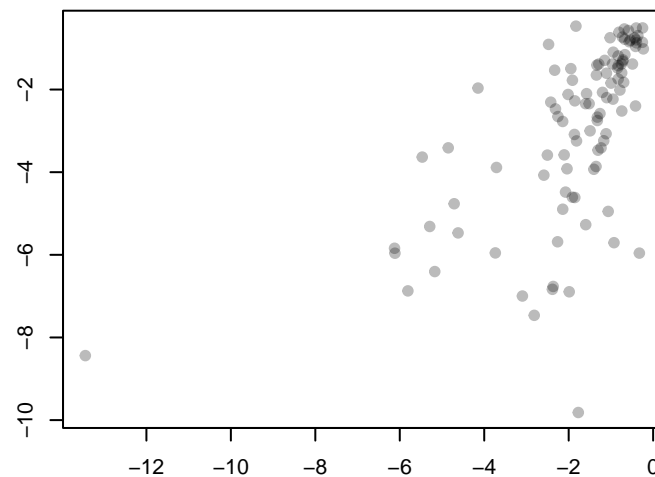
Series y1



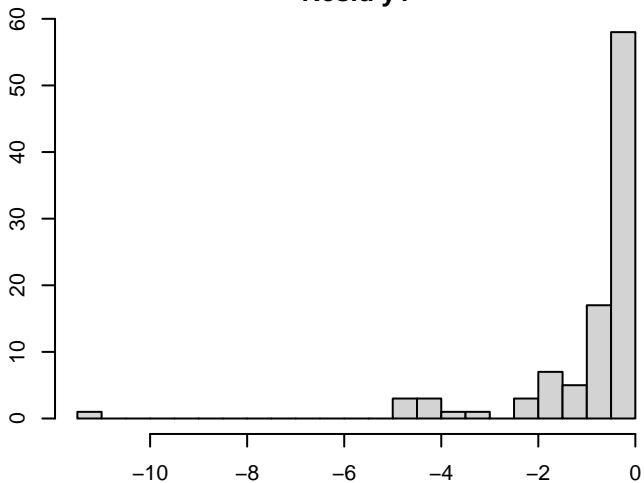
Series y2



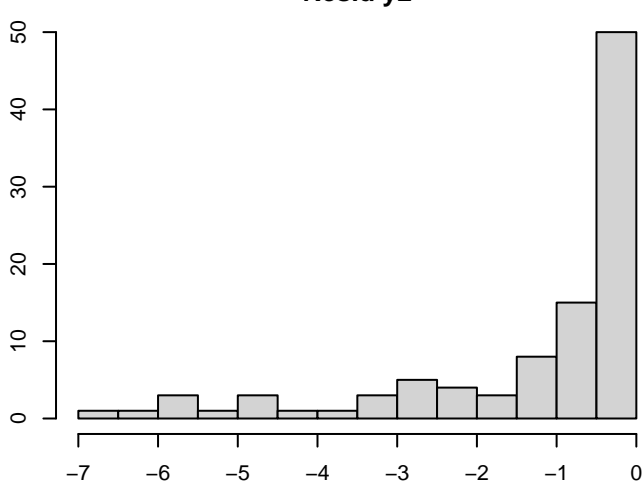
Scatter



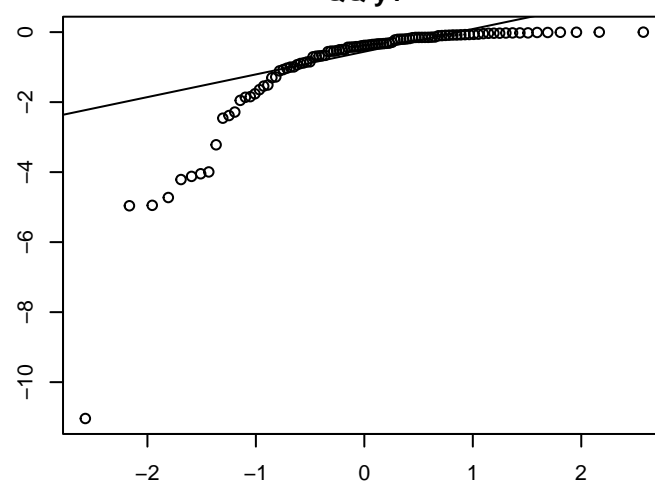
Resid y1



Resid y2



QQ y1



QQ y2

