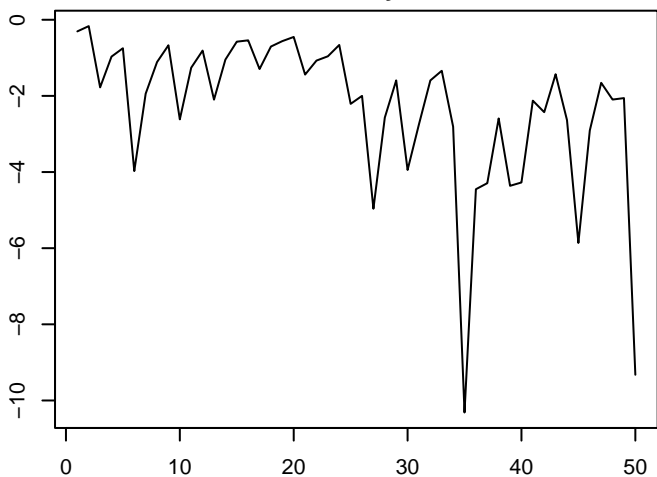
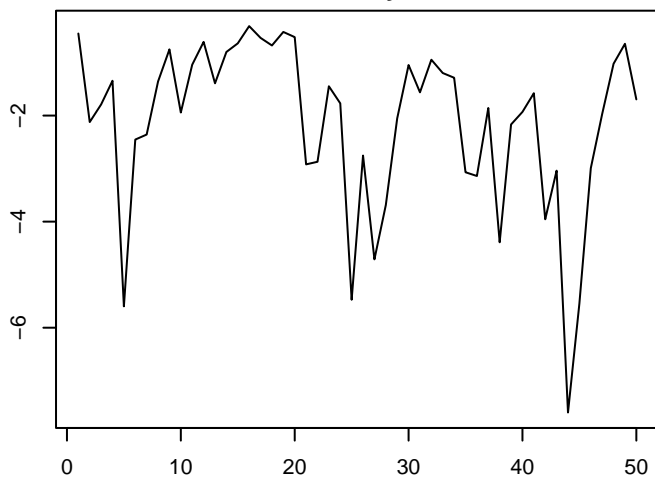


# Cond 024 (chisg) Rep 19

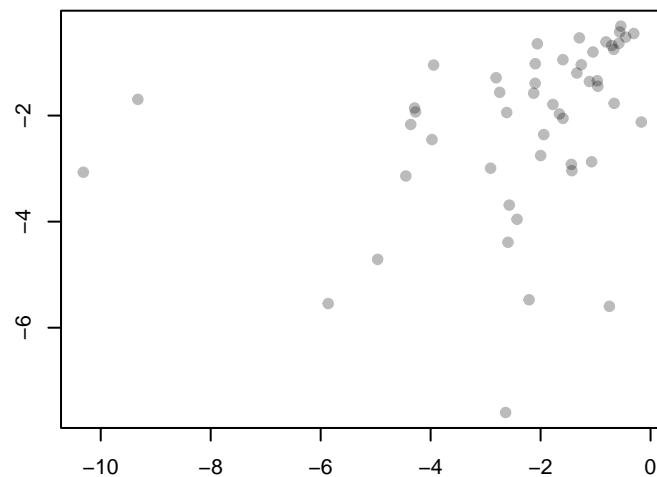
Series y1



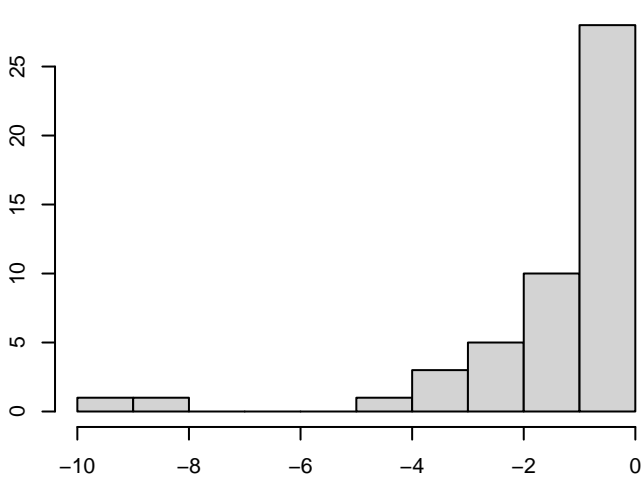
Series y2



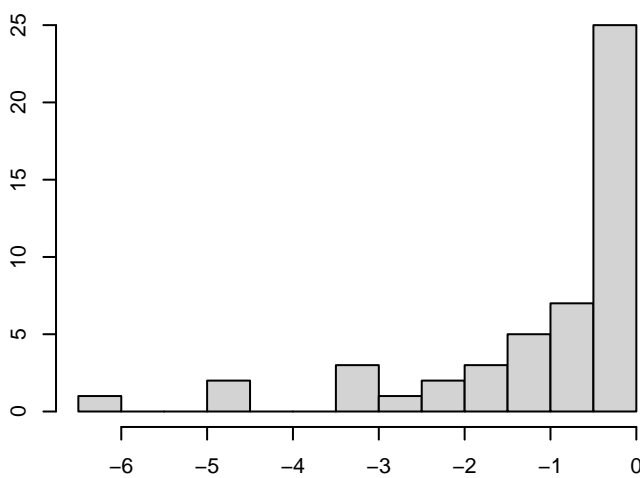
Scatter



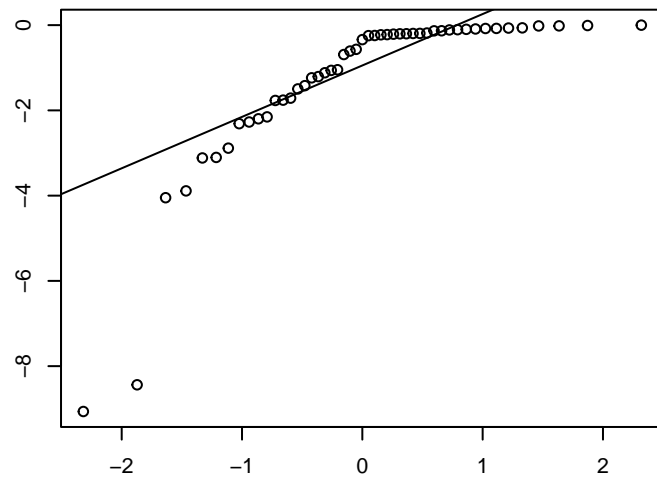
Resid y1



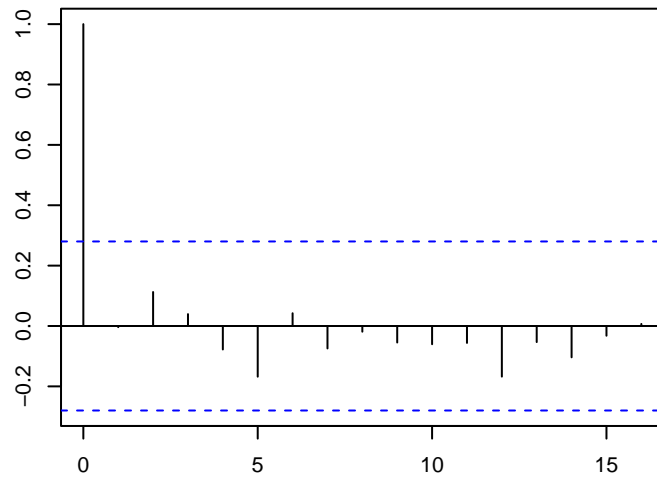
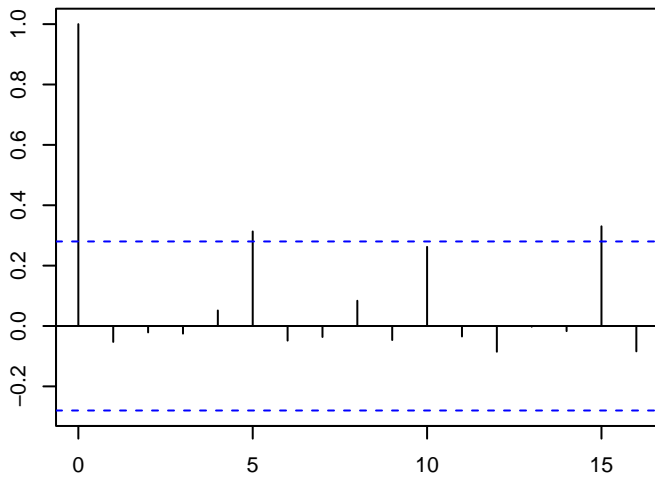
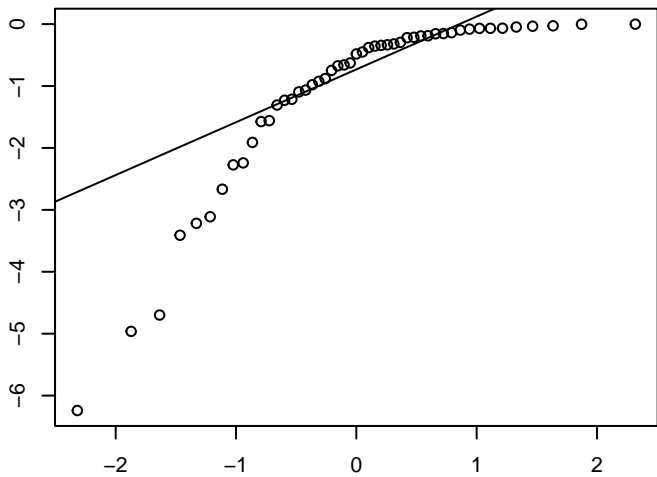
Resid y2



QQ y1

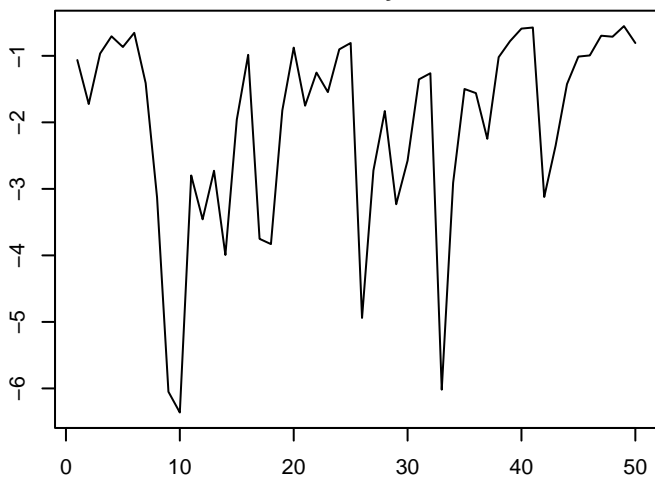


QQ y2

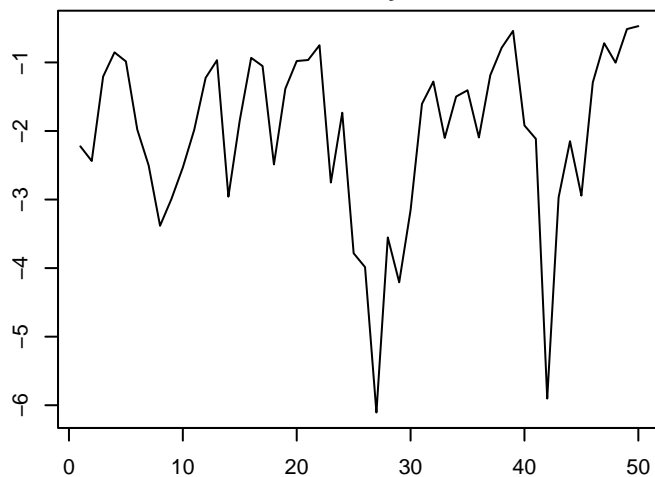


Cond 024 (chisg) | Series y2 | Rep 85

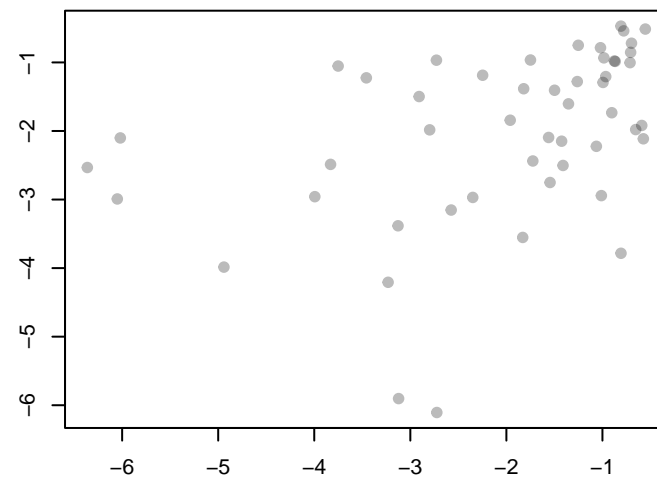
Series y1



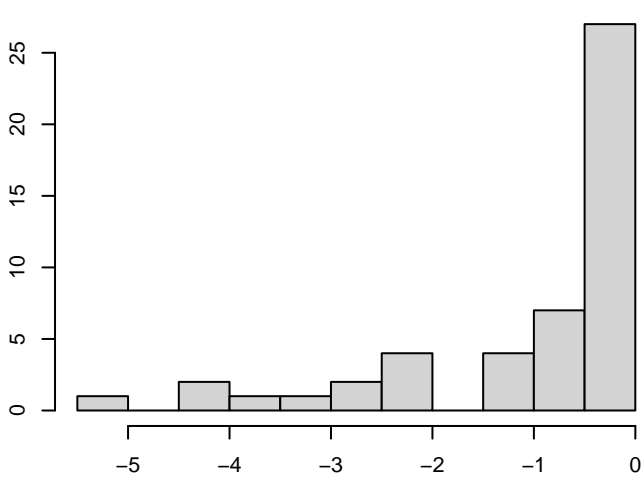
Series y2



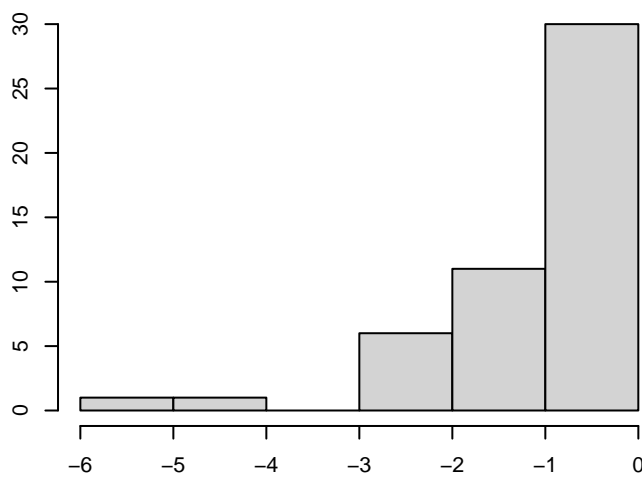
Scatter



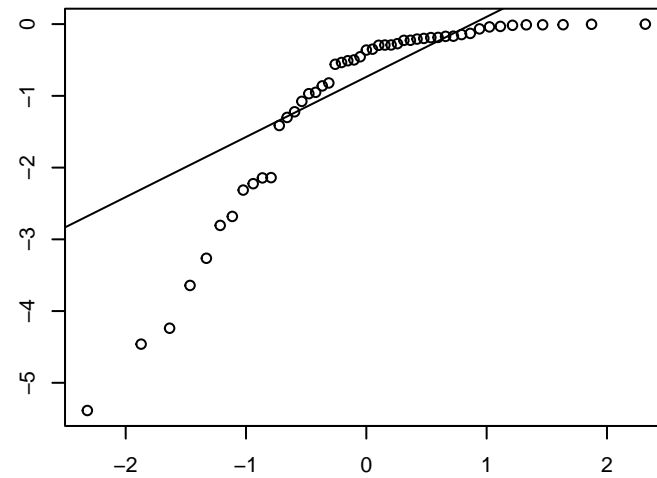
Resid y1



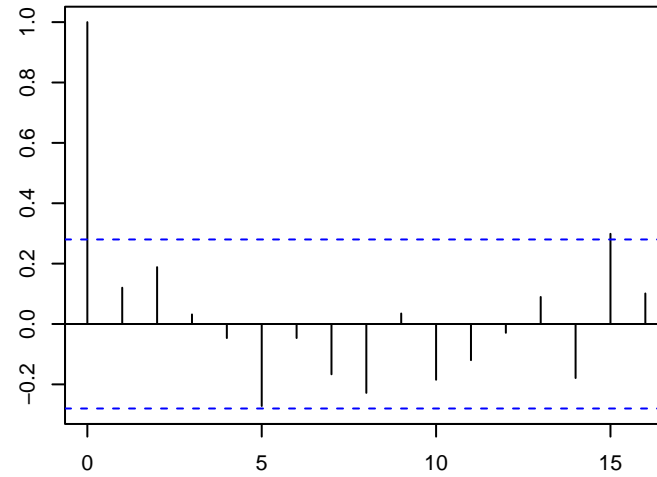
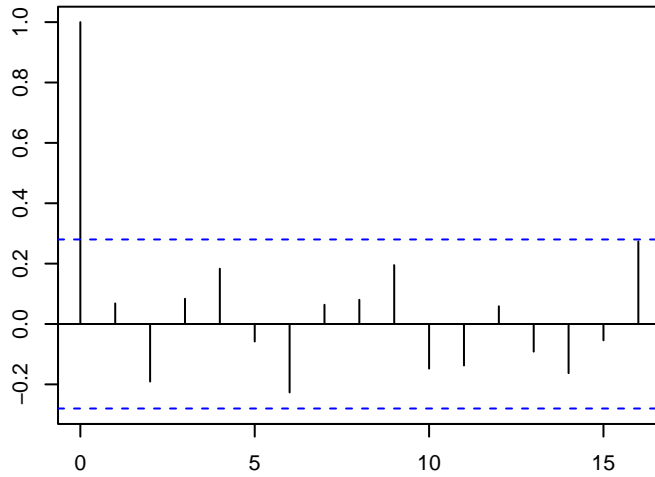
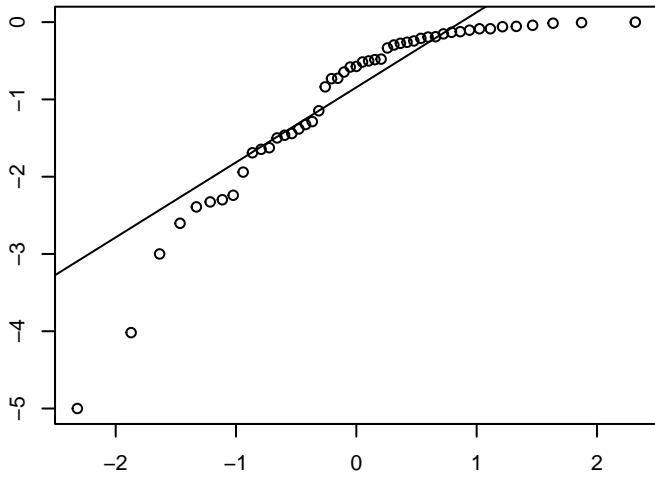
Resid y2



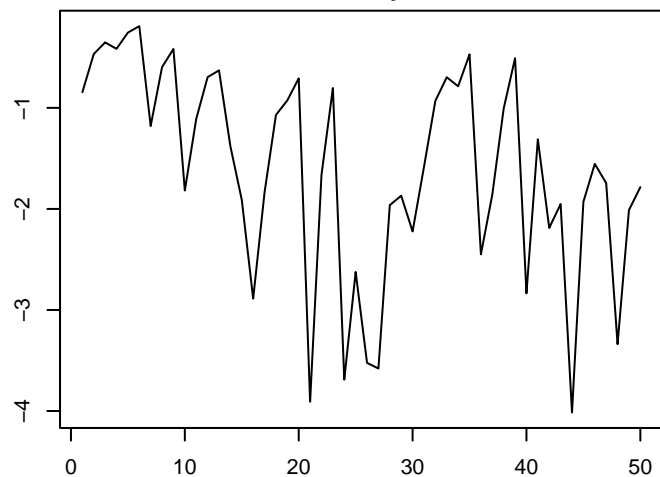
QQ y1



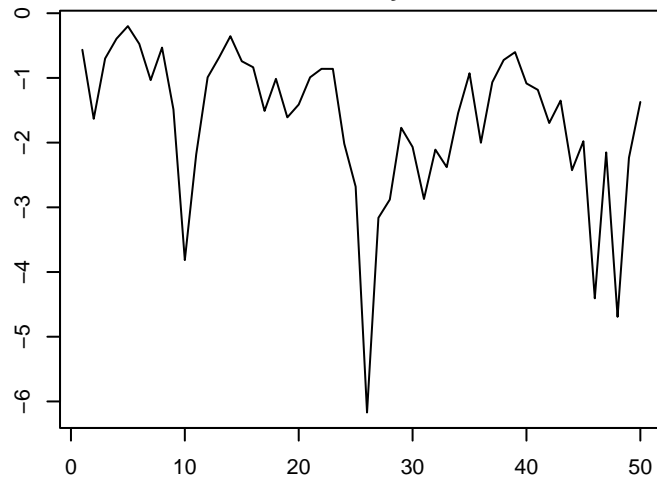
QQ y2



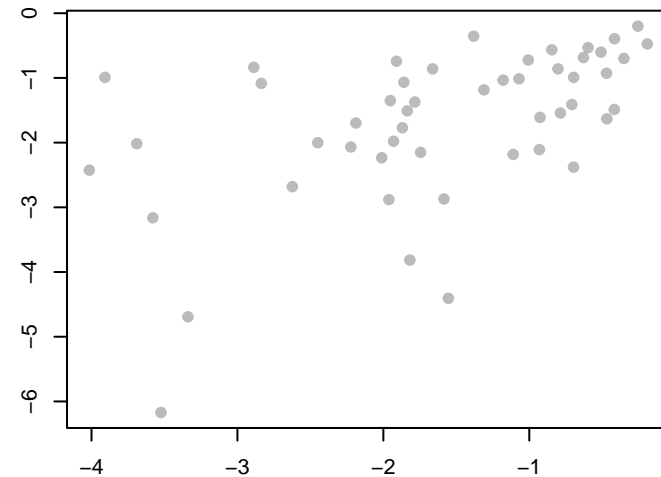
Series y1



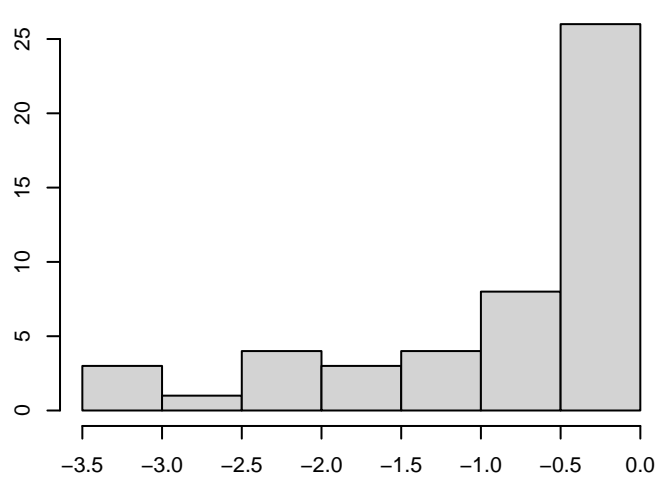
Cond 024 (chisg)\_2 Rep 42



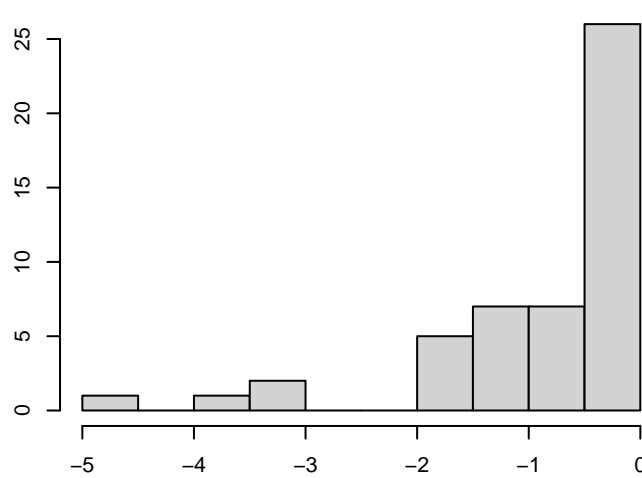
Scatter



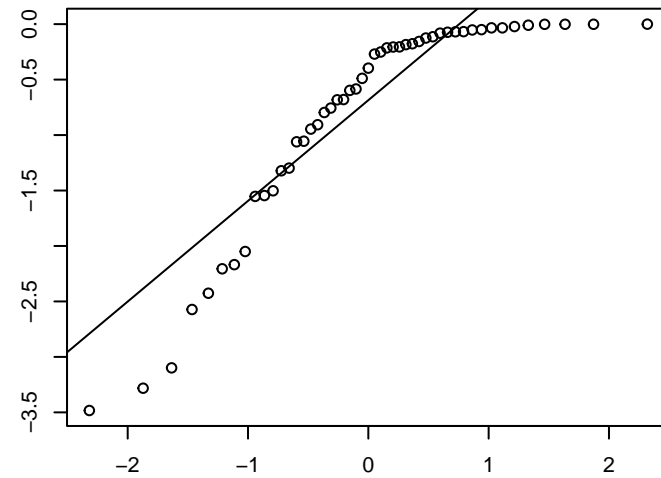
Resid y1



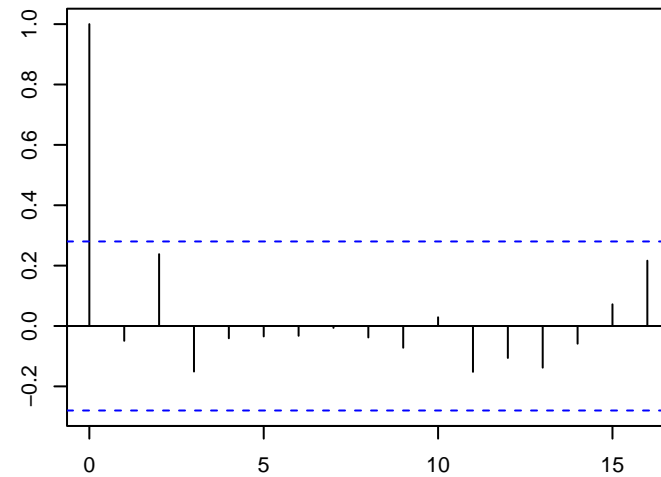
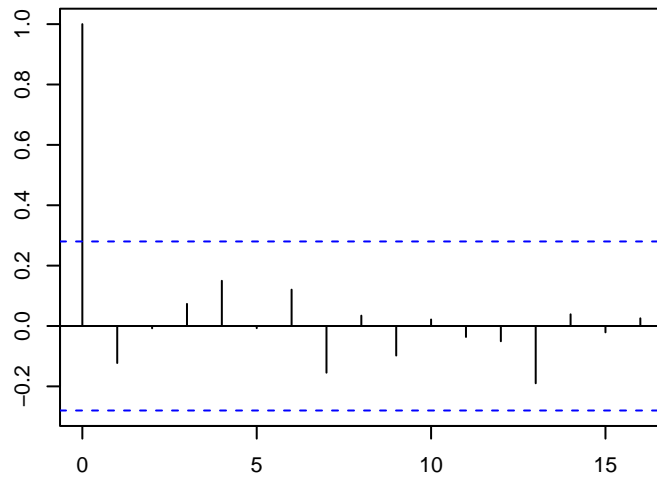
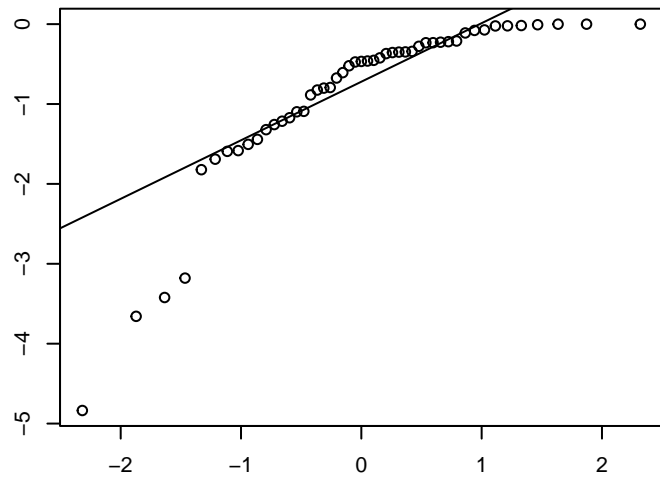
Resid y2



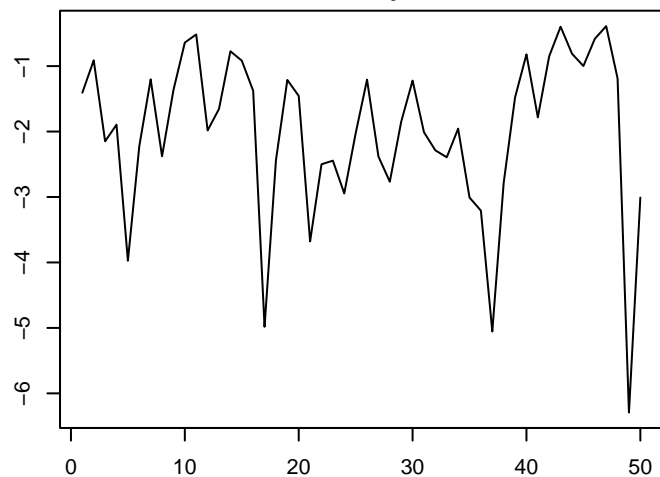
QQ y1



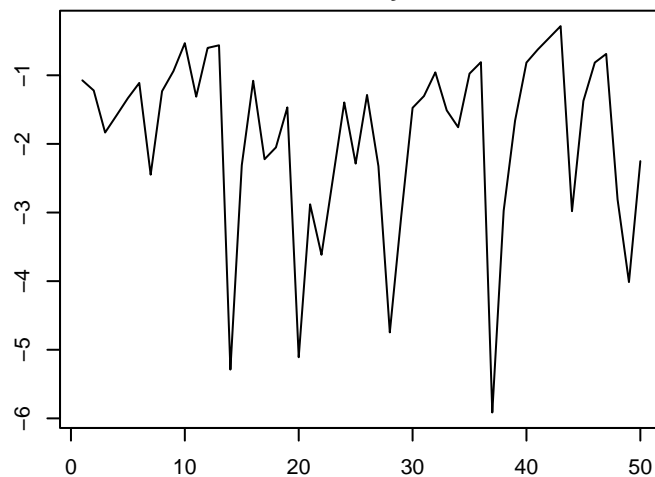
QQ y2



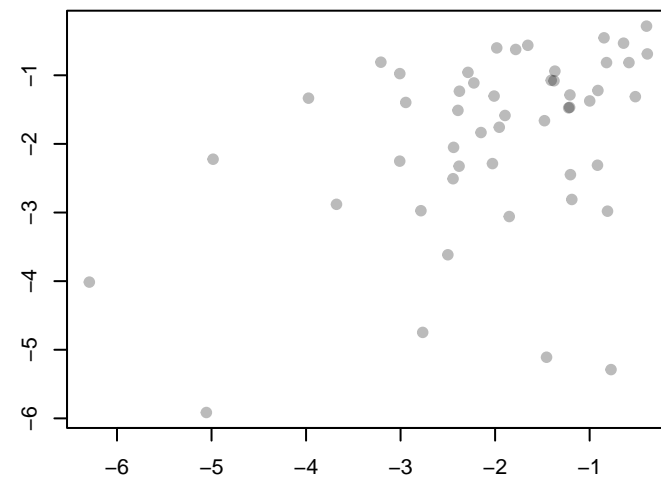
Series y1



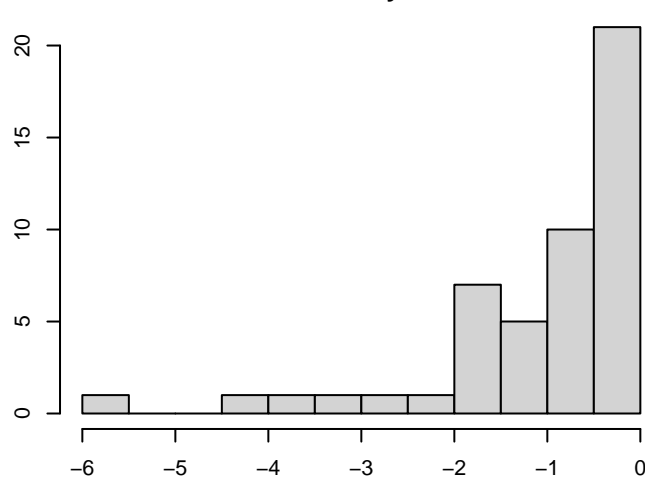
Cond 024 (chisg) y2 Rep 57



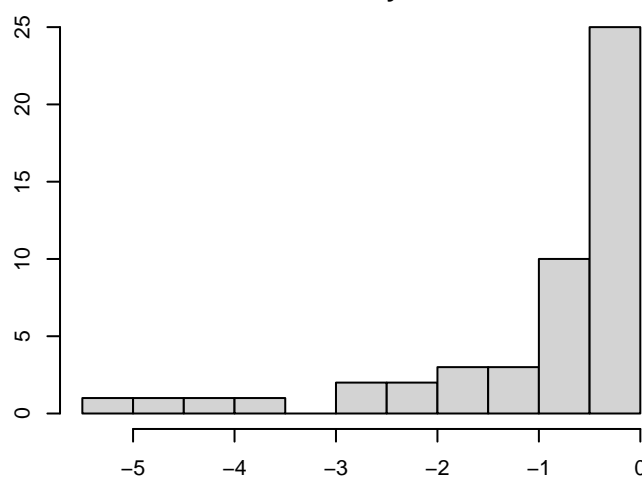
Scatter



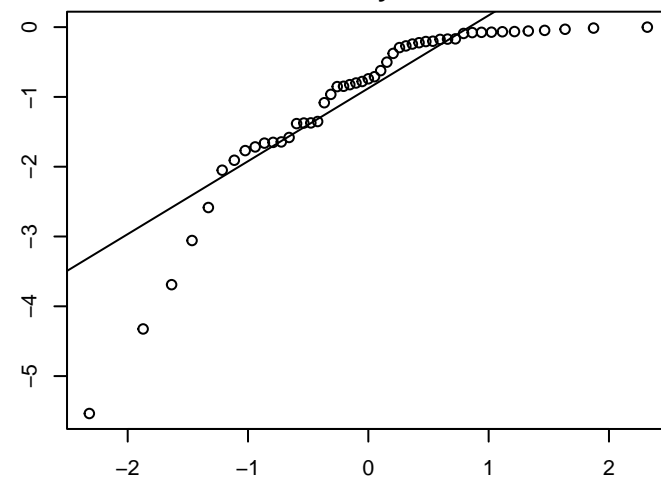
Resid y1



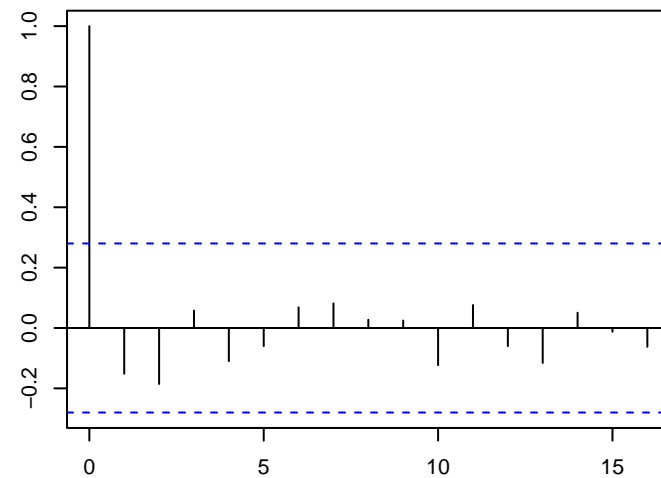
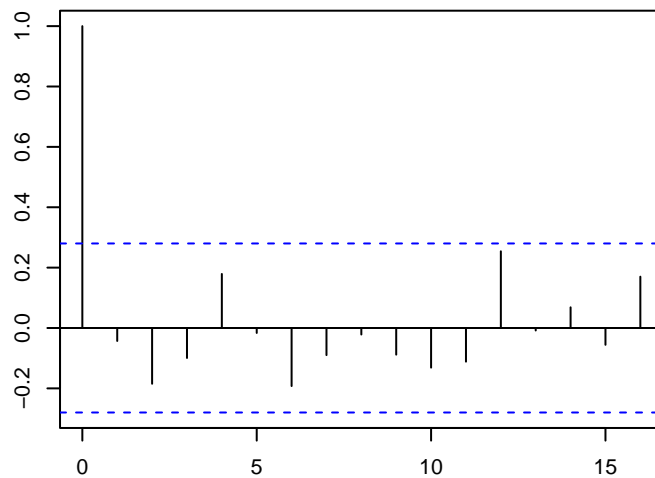
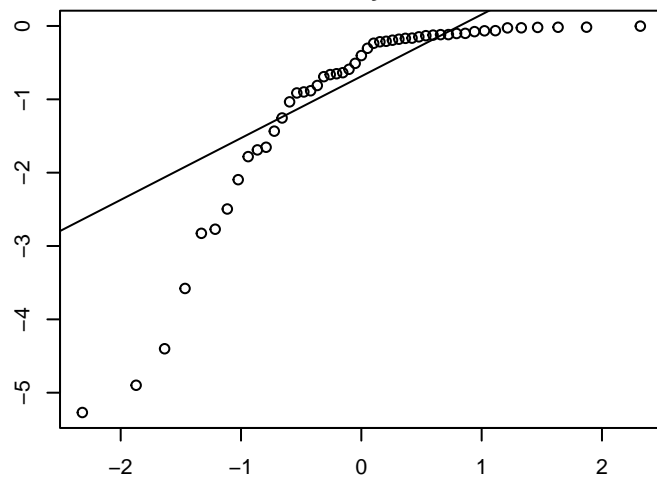
Resid y2



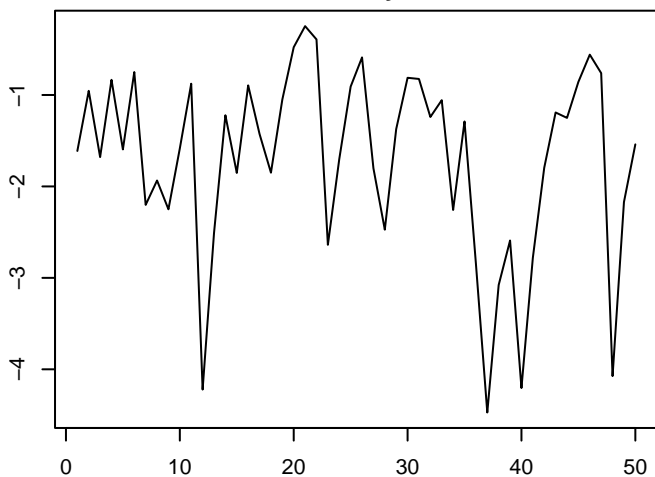
QQ y1



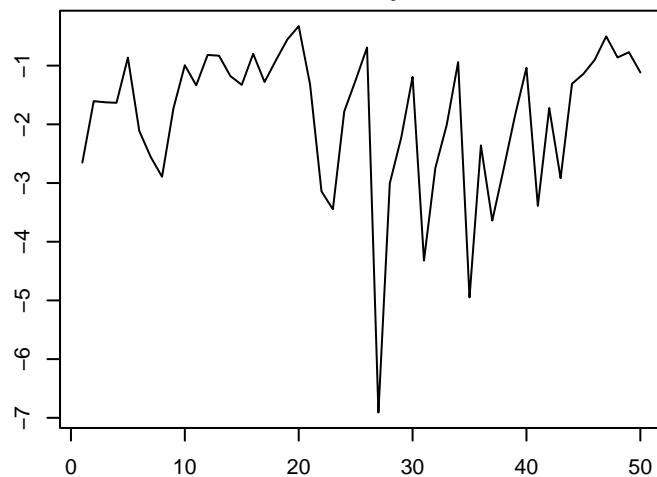
QQ y2



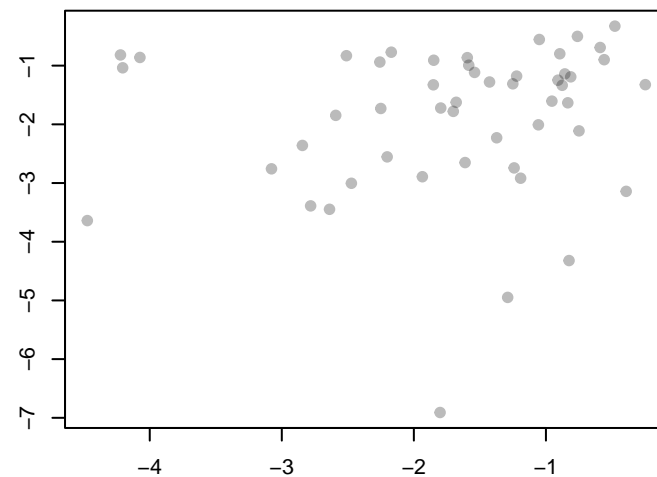
Series y1



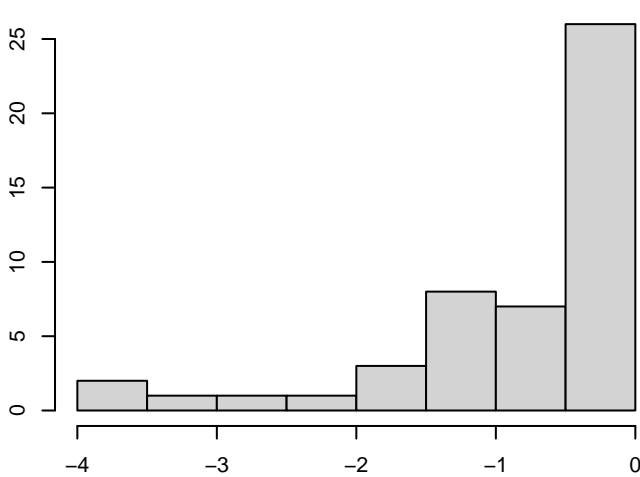
Cond 024 (chisg) | Series y2 | Rep 90



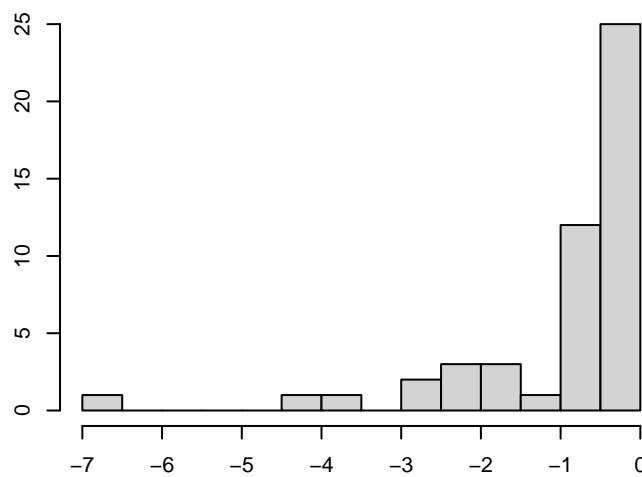
Scatter



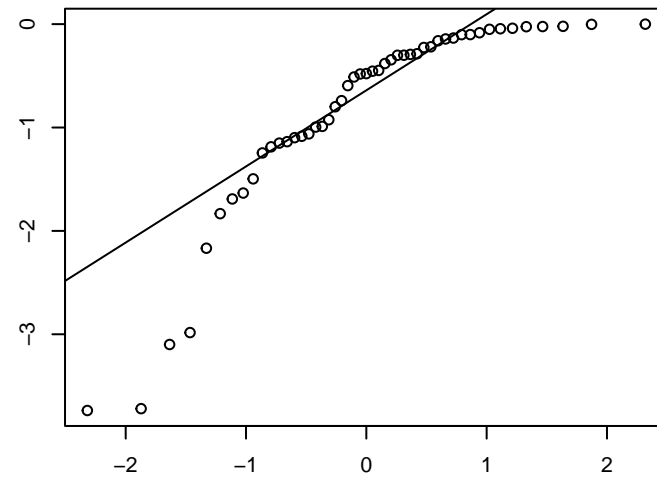
Resid y1



Resid y2



QQ y1



QQ y2

