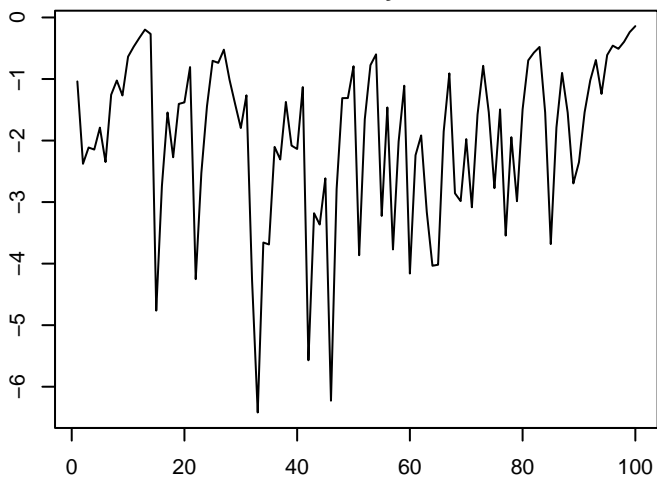
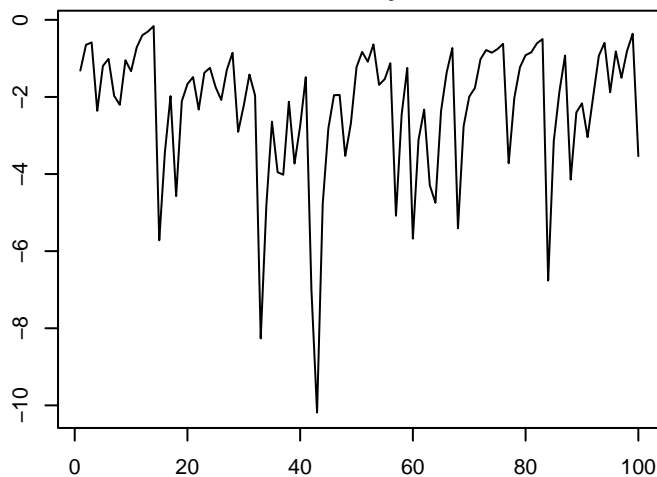


# Cond 033 (chisg)\_1 Rep 53

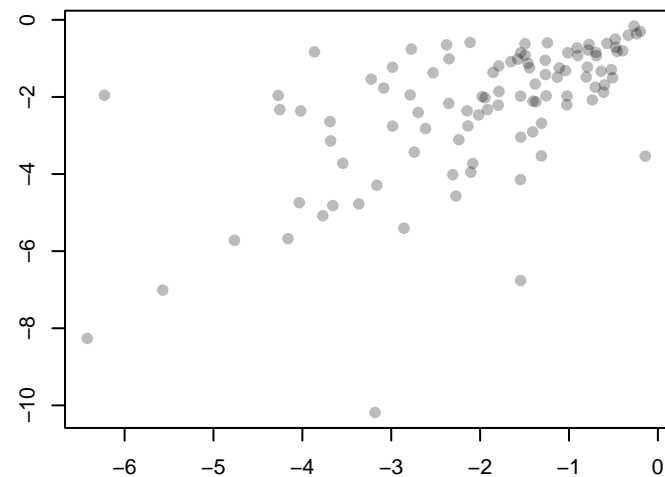
Series y1



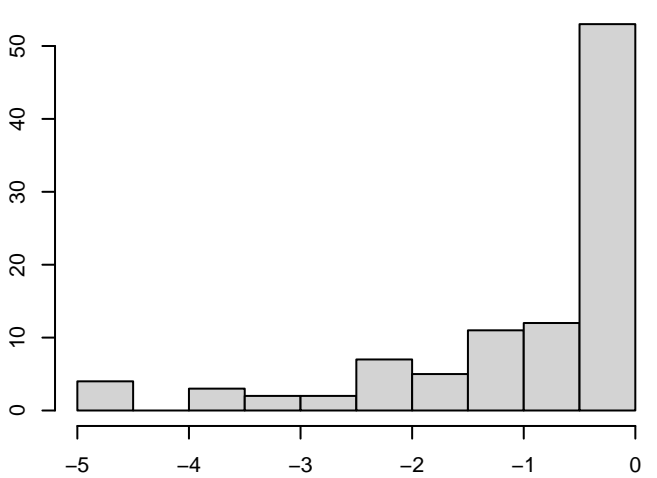
Series y2



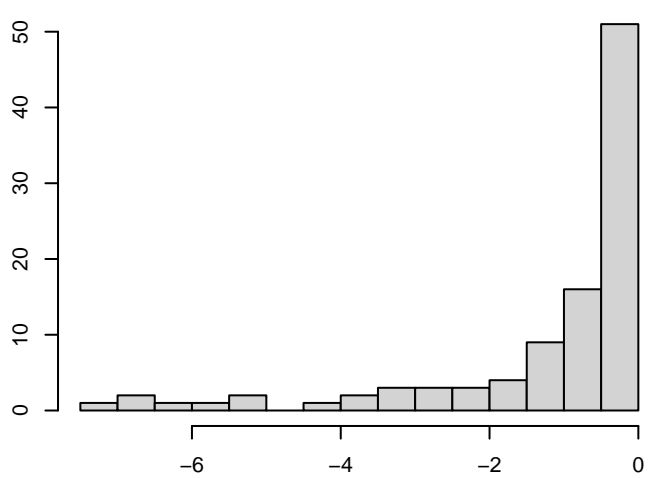
Scatter



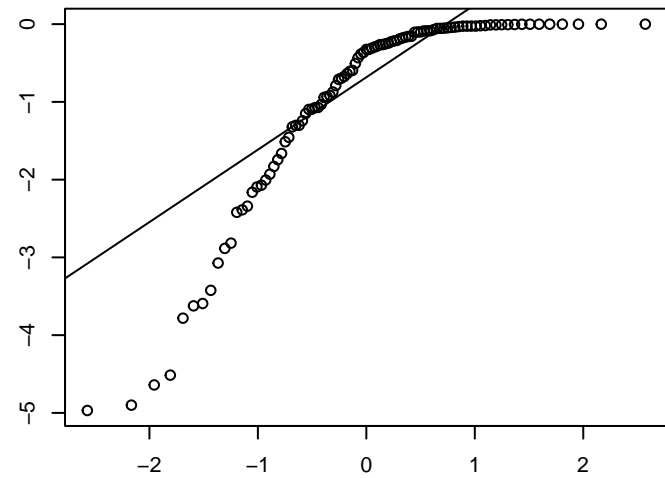
Resid y1



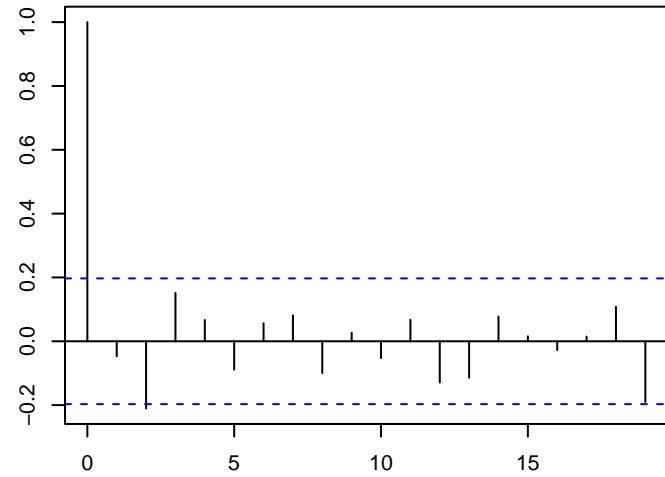
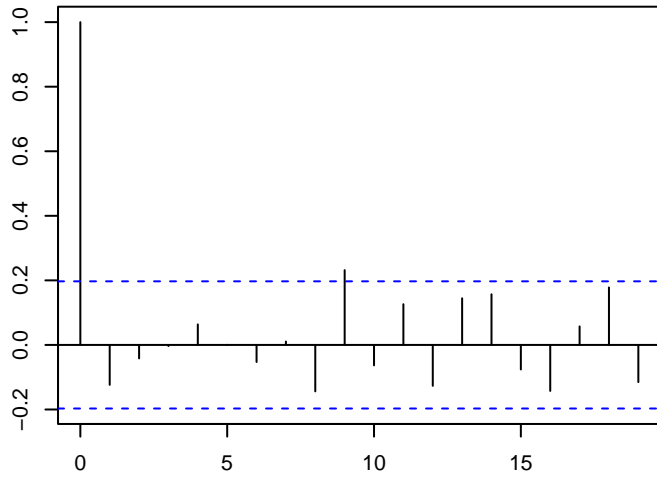
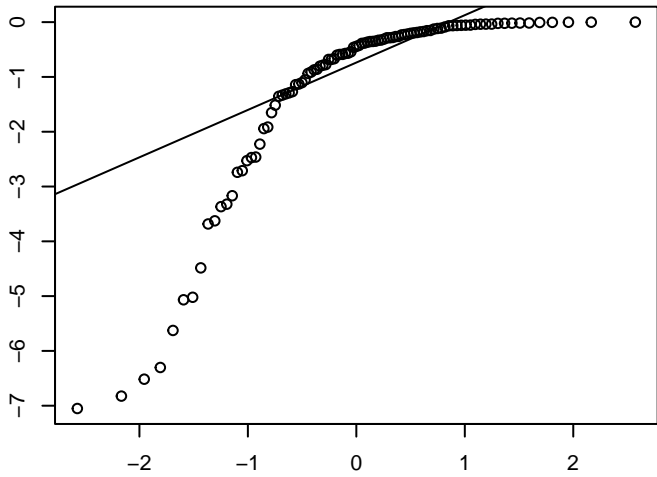
Resid y2



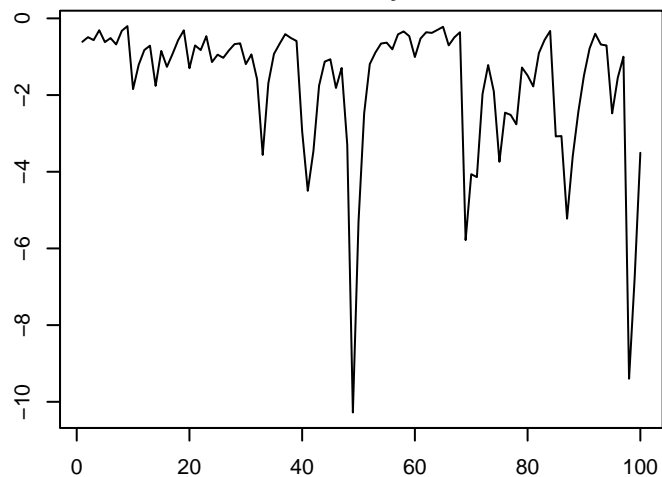
QQ y1



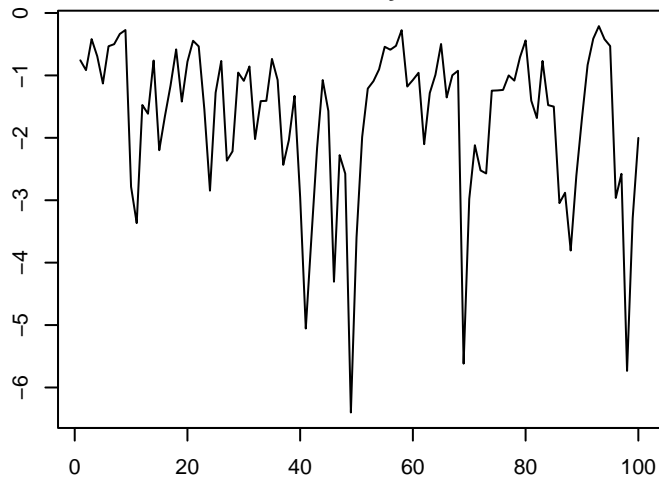
QQ y2



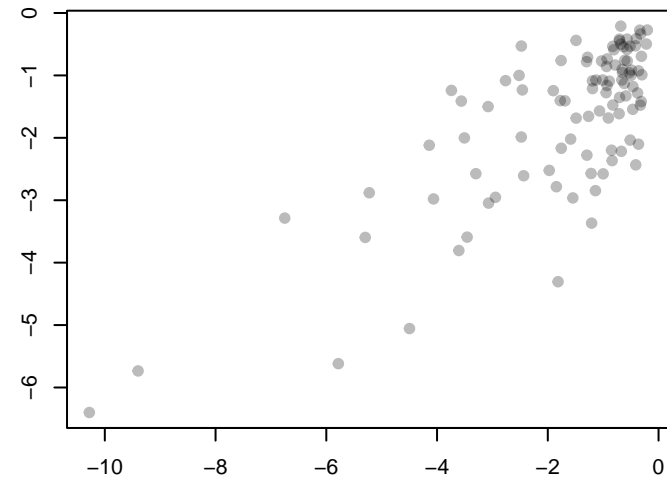
Series y1



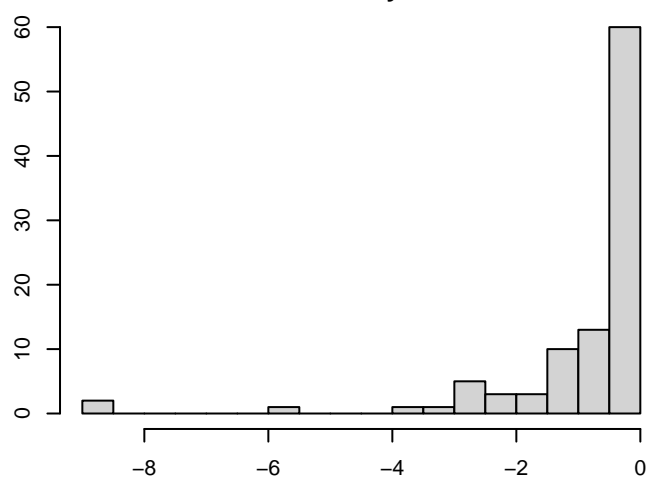
Cond 033 (chisg)\_2



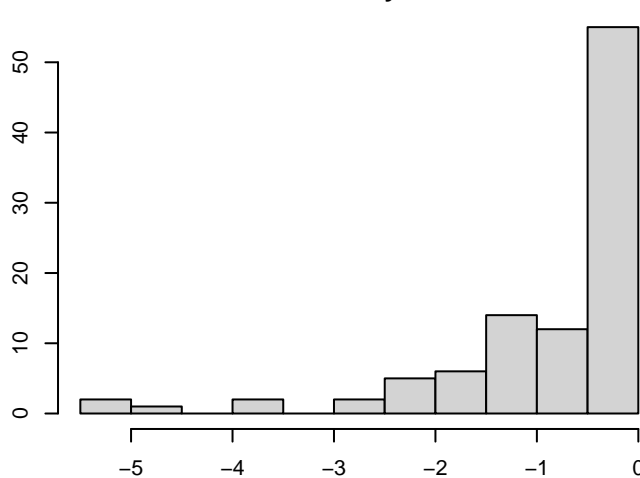
Scatter



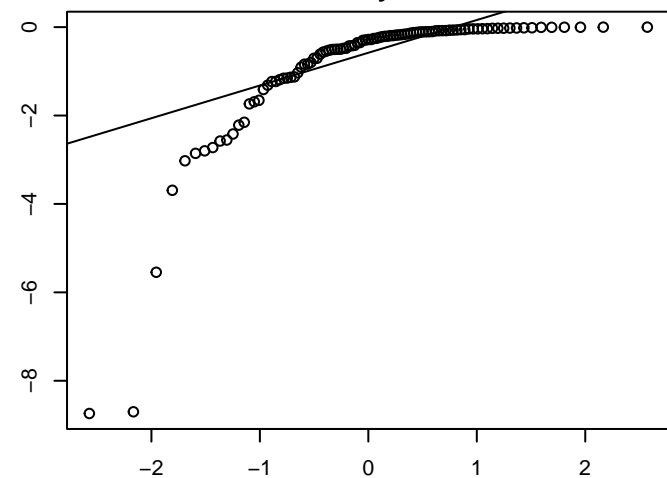
Resid y1



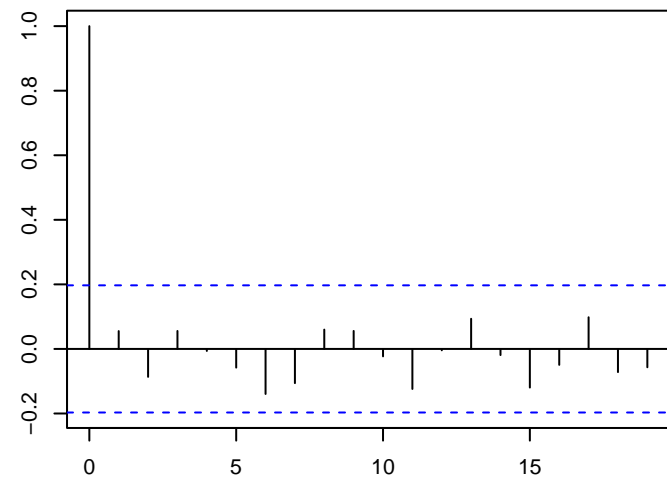
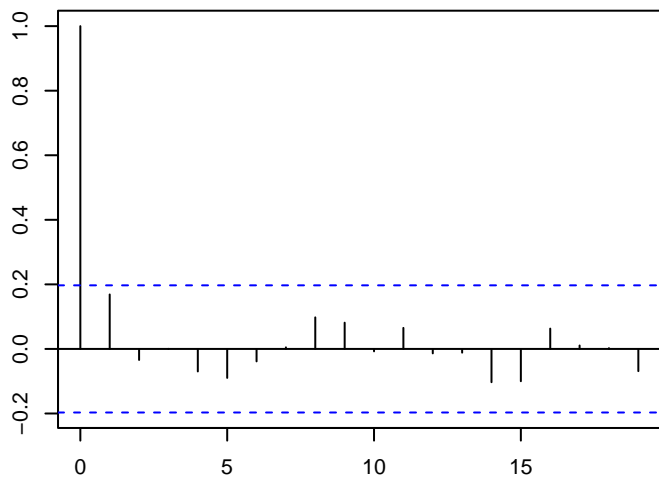
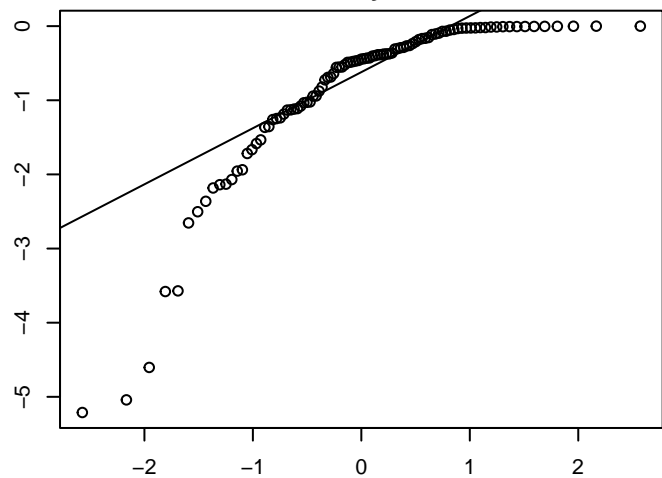
Resid y2



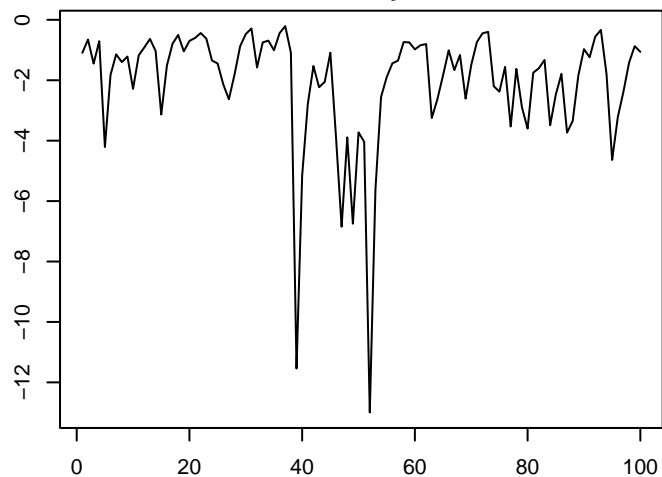
QQ y1



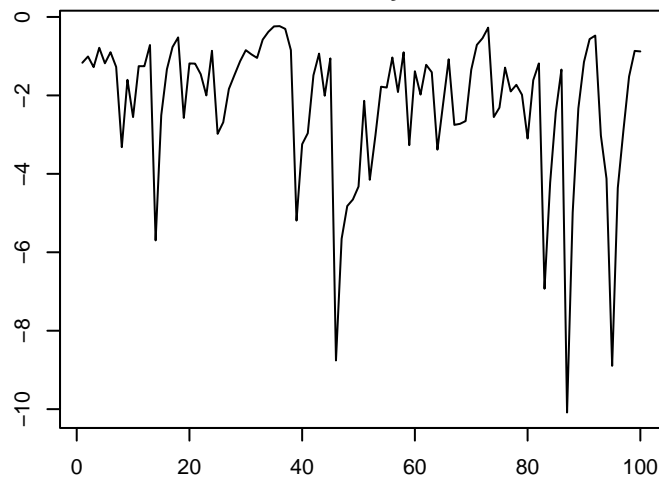
QQ y2



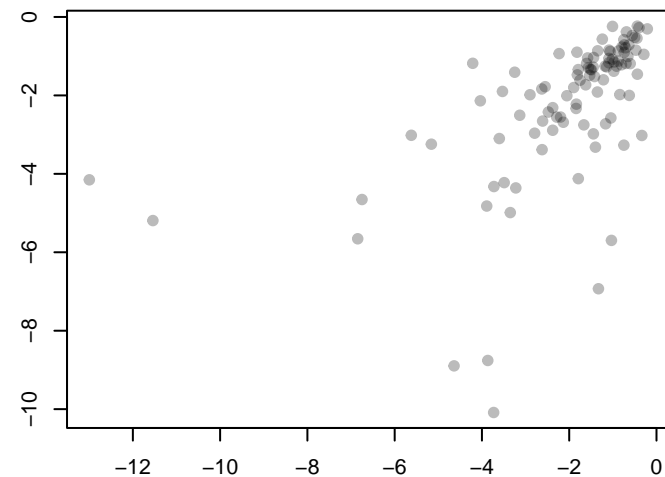
Series y1



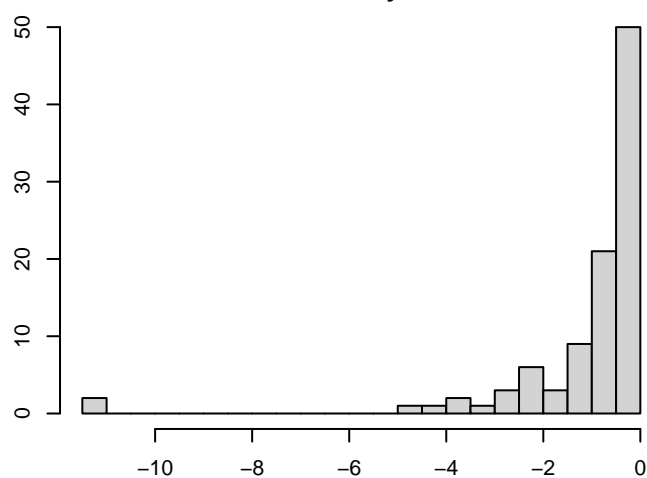
Cond 033 (chisq) | Series y2 | Rep 4



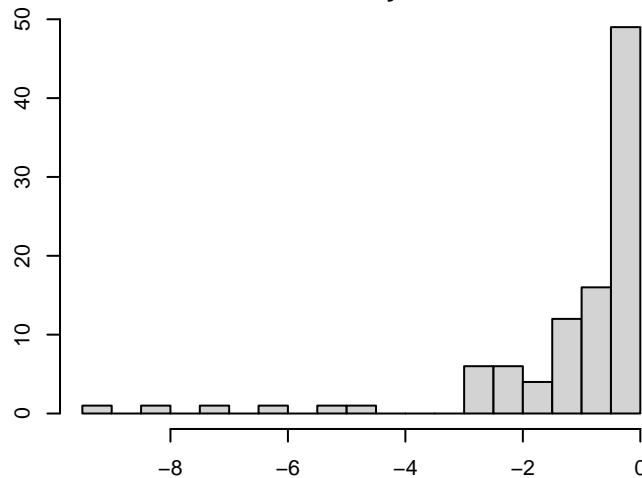
Scatter



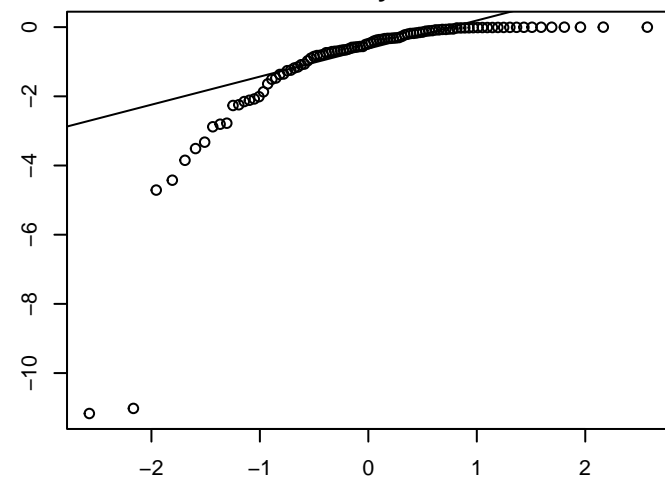
Resid y1



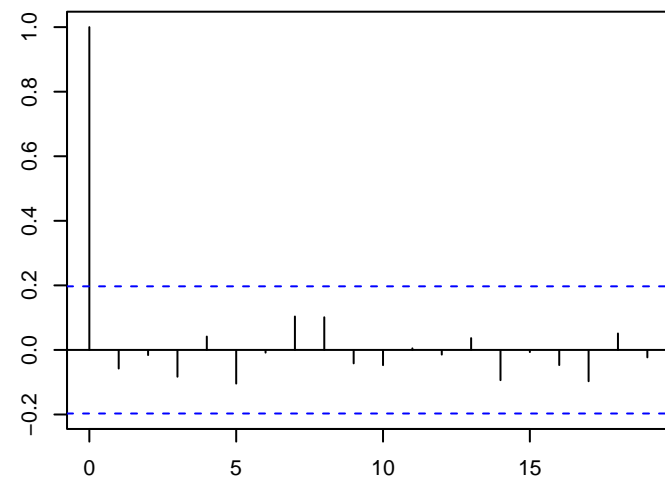
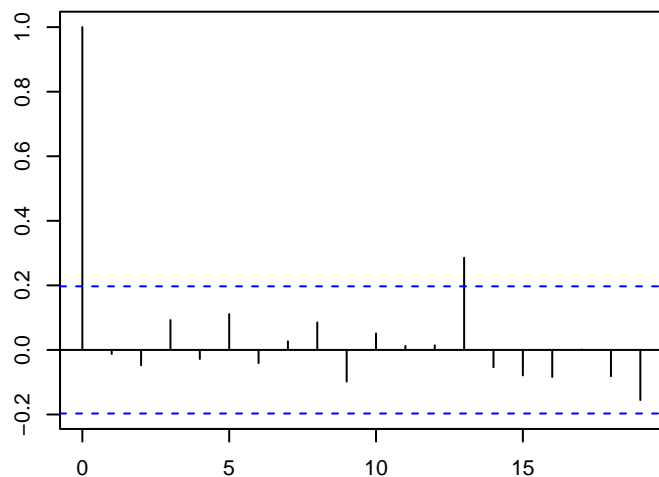
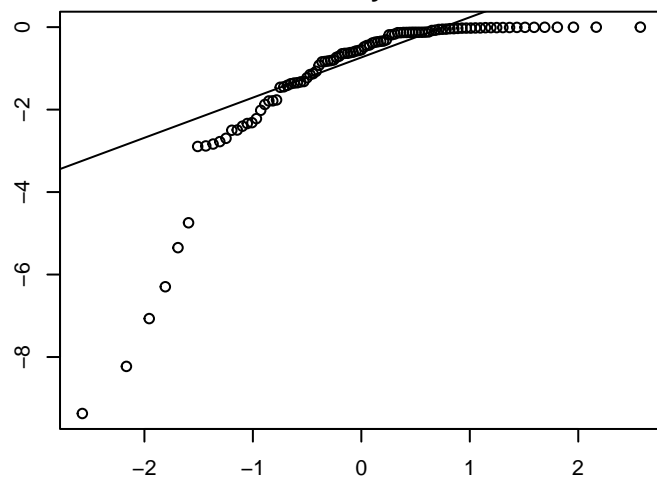
Resid y2



QQ y1

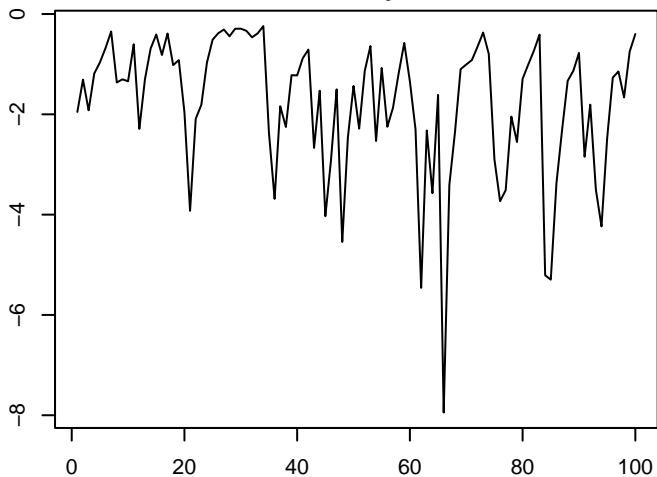


QQ y2

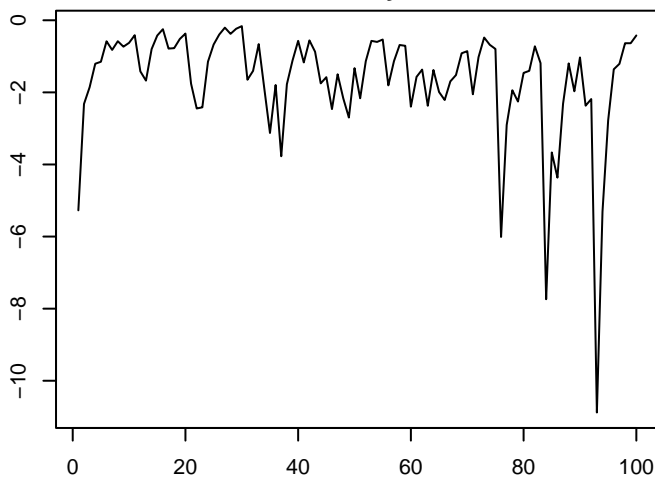


# Cond 033 (chisg)\_2 Rep 63

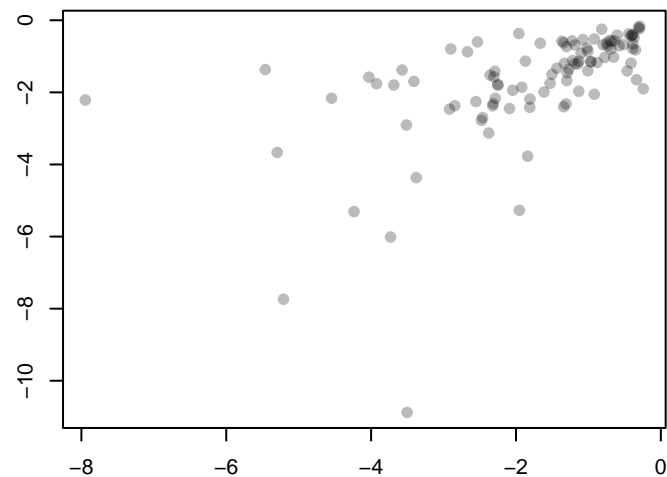
Series y1



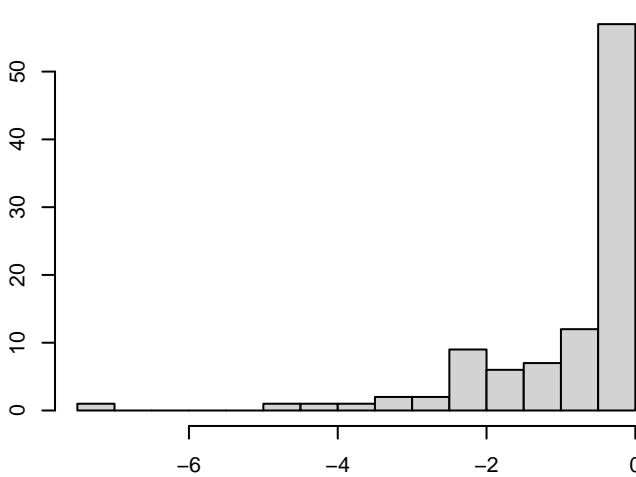
Series y2



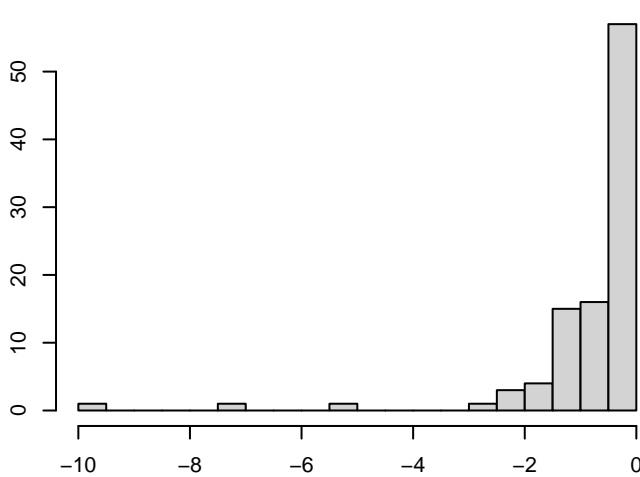
Scatter



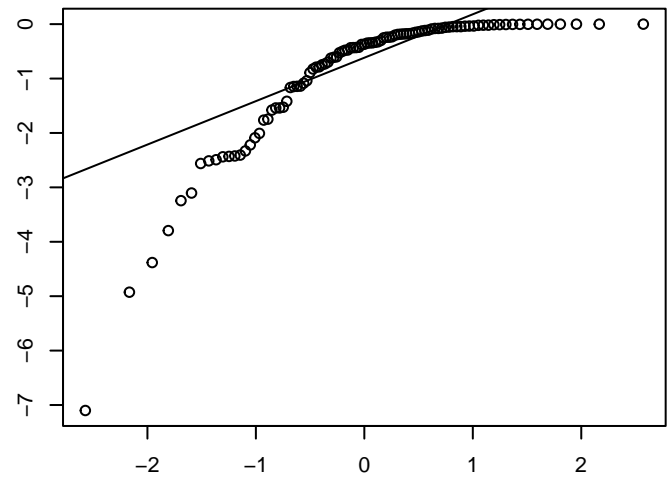
Resid y1



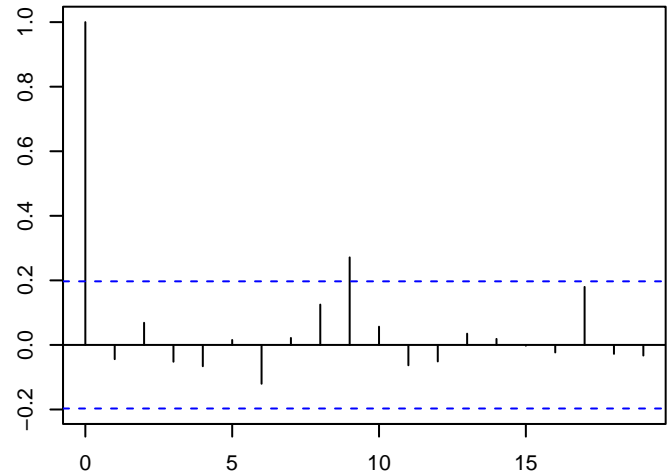
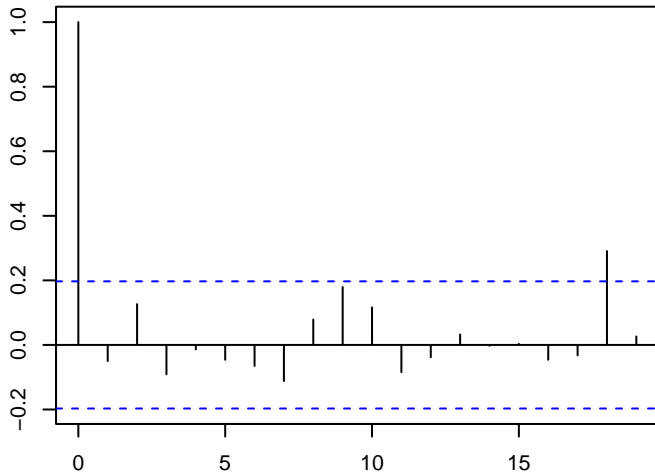
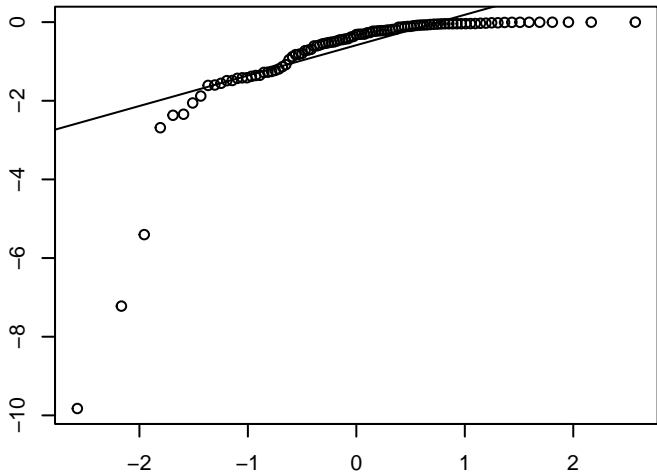
Resid y2



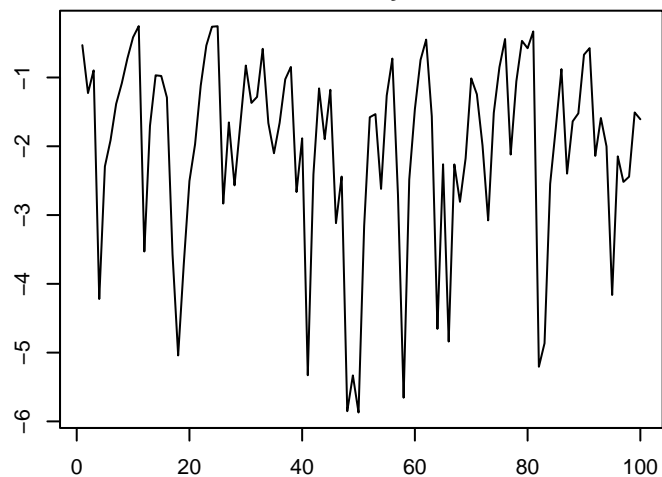
QQ y1



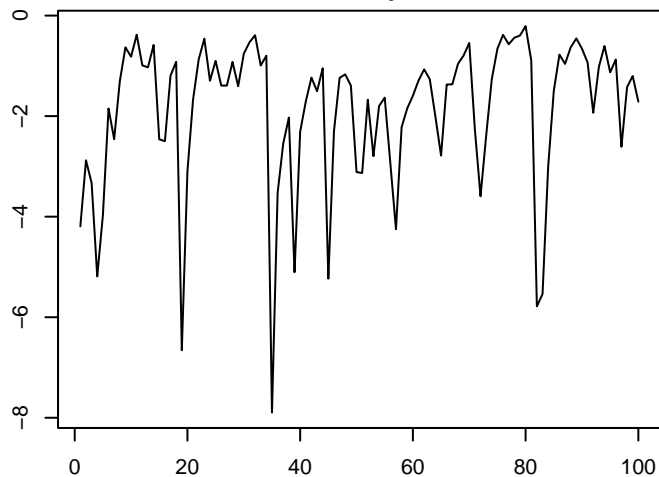
QQ y2



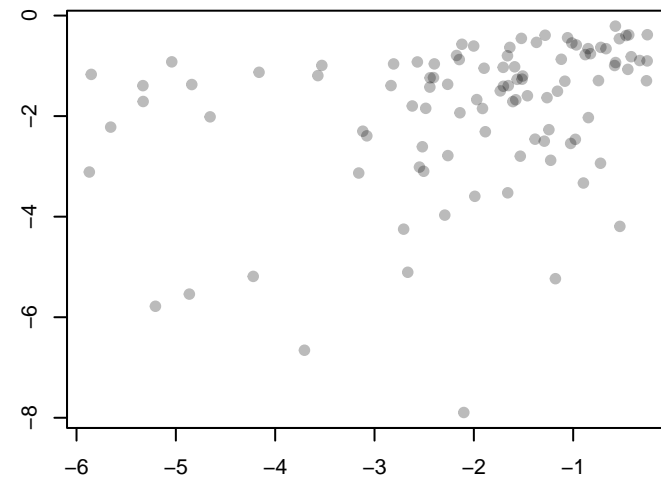
Series y1



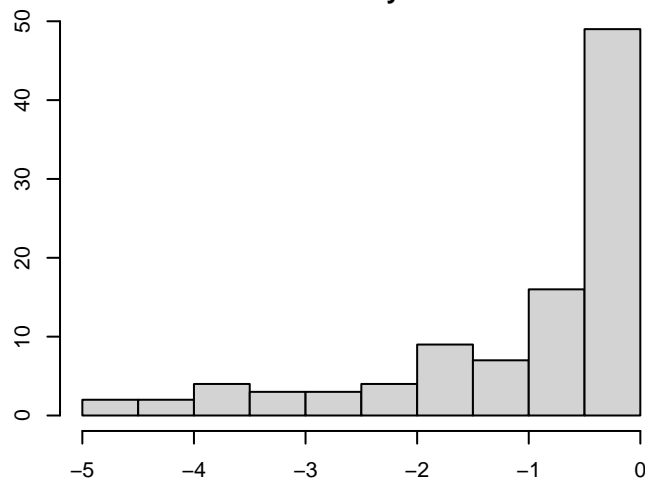
Cond 033 (chisq), Rep 54



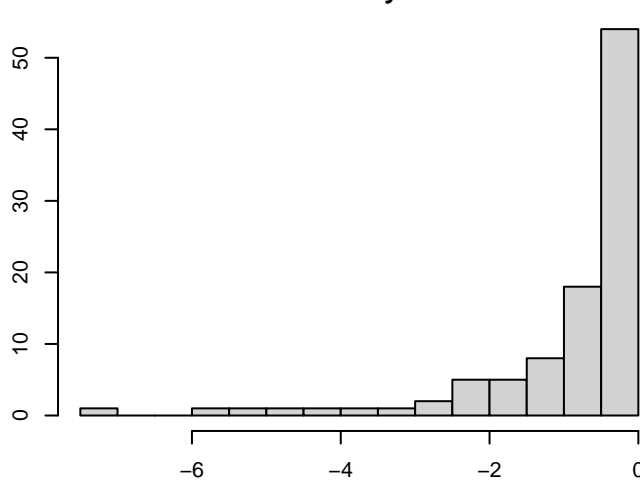
Scatter



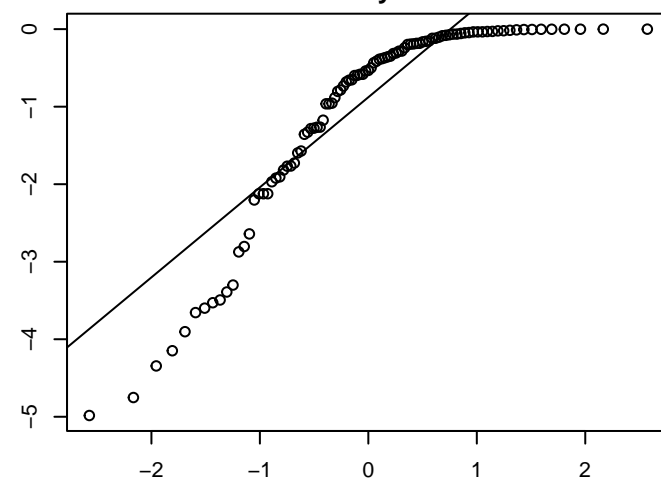
Resid y1



Resid y2



QQ y1



QQ y2

