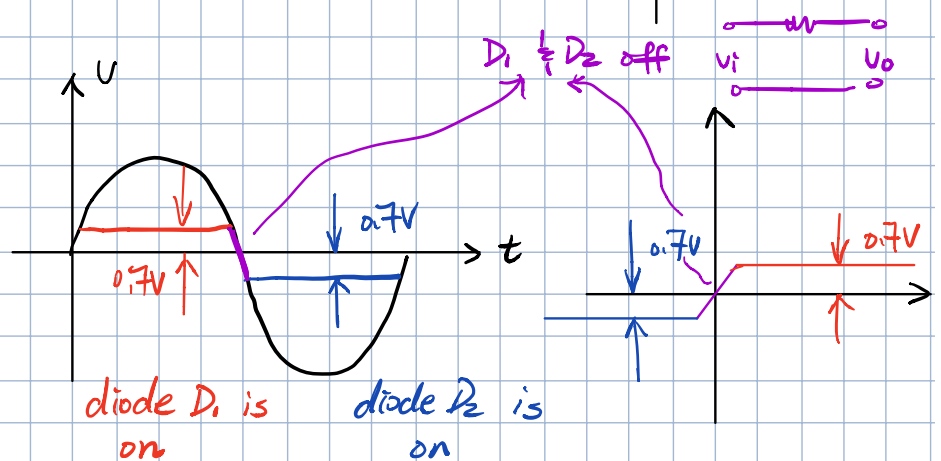
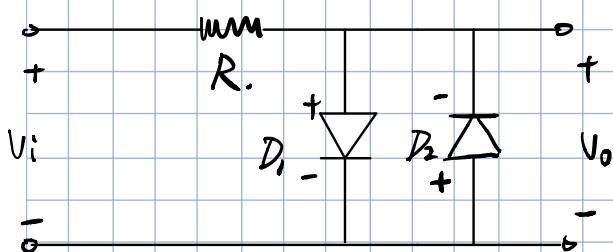
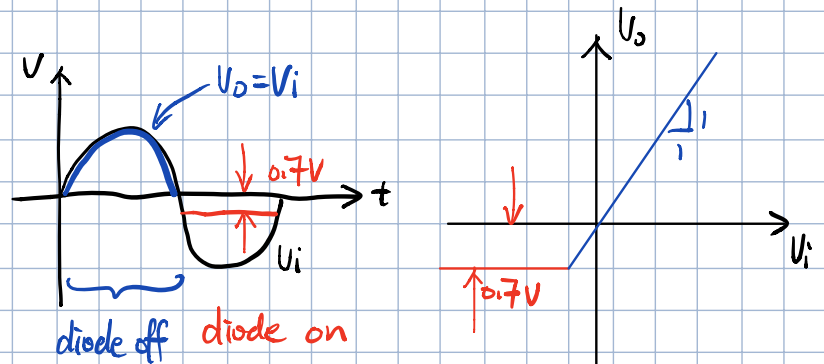
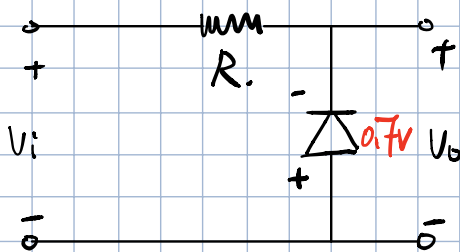
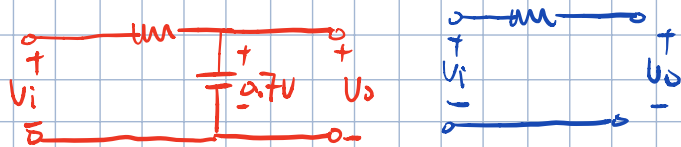
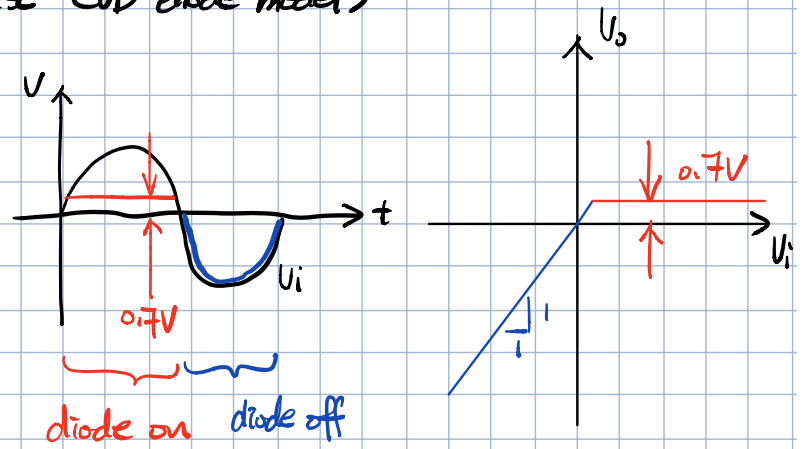
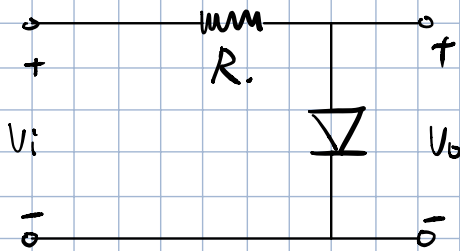
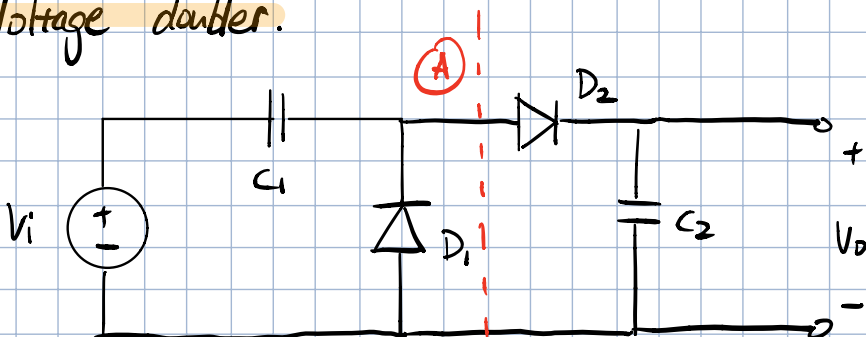


# Limiting & Clamping Circuits

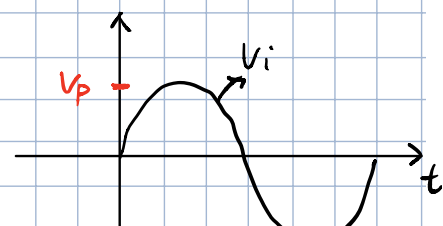
(use CuD diode model)

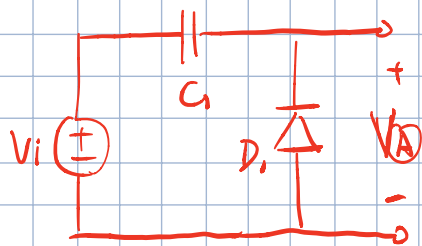


## Voltage doubler.



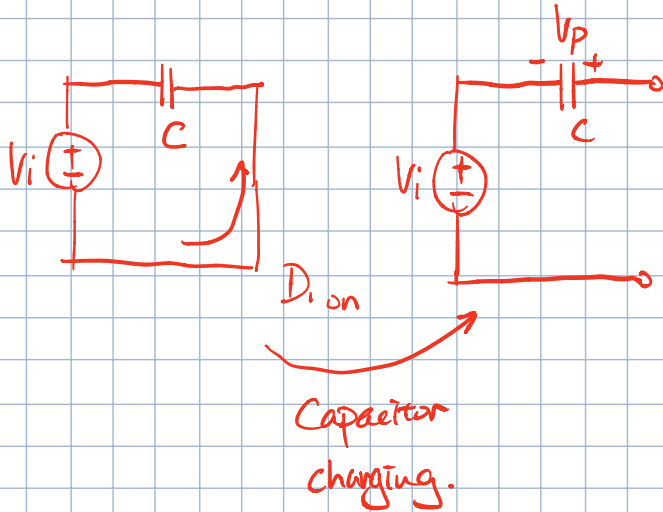
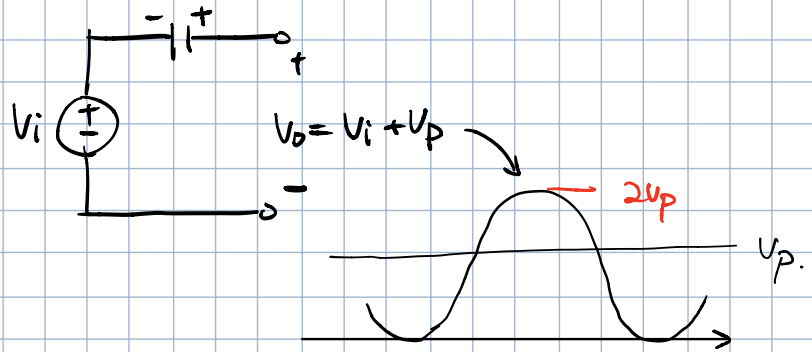
assume ideal diodes.





see bottom.  
(peak detector)

At positive half cycle of  $V_i$



Capacitor  
charging.

This left half circuit is called the  
clamping circuit.

