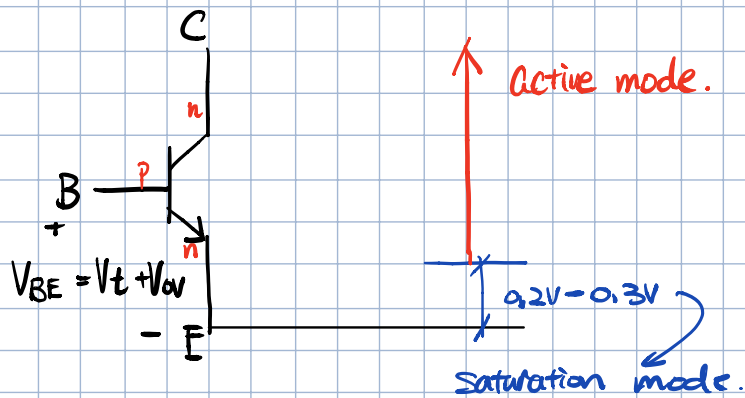
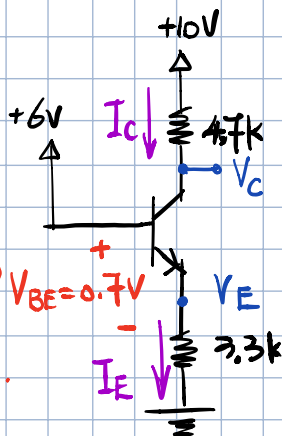


# BJT DC circuits.



Example 1  $\beta \geq 50$  for active mode.



1) Not likely to be in cut-off mode.

2) Assume in active mode.

$$\textcircled{2} V_E = 6 - 0.7 = 5.3V$$

$$\textcircled{3} I_E = \frac{V_E}{3.3k} = 1.6mA$$

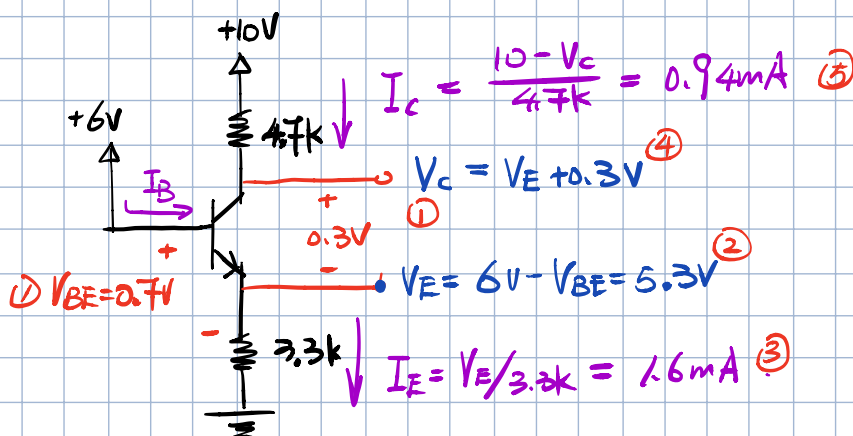
$$\textcircled{4} I_C = \frac{\beta}{\beta+1} I_E \approx 1.6mA$$

$$\textcircled{5} V_C = 10 - (4.7k)I_C = 2.48V$$

$$V_C < V_E$$

$\therefore$  Assumption is wrong.

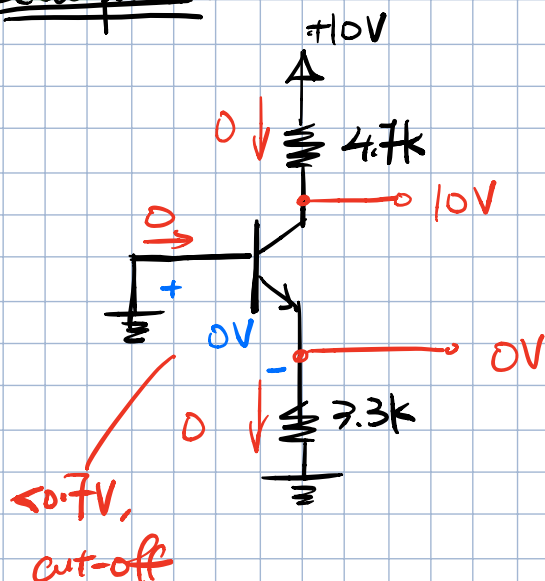
3) It must be in Saturation mode!



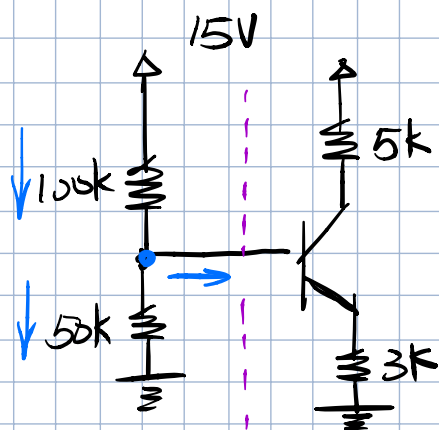
$$I_B = I_E - I_C = 0.66mA$$

$$\beta_{forced} = \frac{I_C}{I_B} = 1.42$$

## example 2.



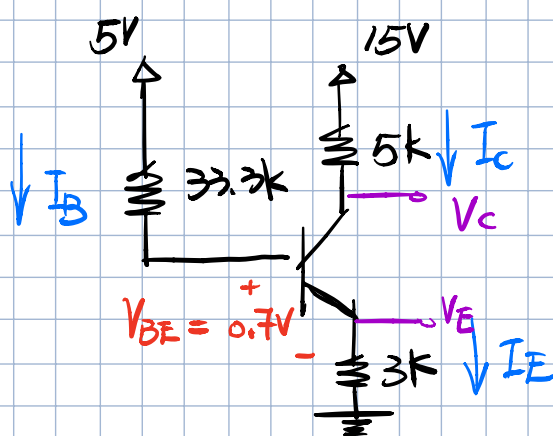
## example 3 $\beta=100$ in active mode



$$5V = \frac{50}{100+50} \times 15V$$

$$100k // 50k = 33.3k$$

Thevenin Equivalent. ckt.



assume. active mode (first)

$$5V = (3k)I_E + 0.7V + (33.3k)I_B$$

$$I_E = (\beta + 1)I_B$$

$$\rightarrow I_B = 0.0128mA \text{ and}$$

$$I_E = (100 + 1) 0.0128mA = 1.29mA$$

$$V_C = 15 - 5k \times I_C = 8.6V$$

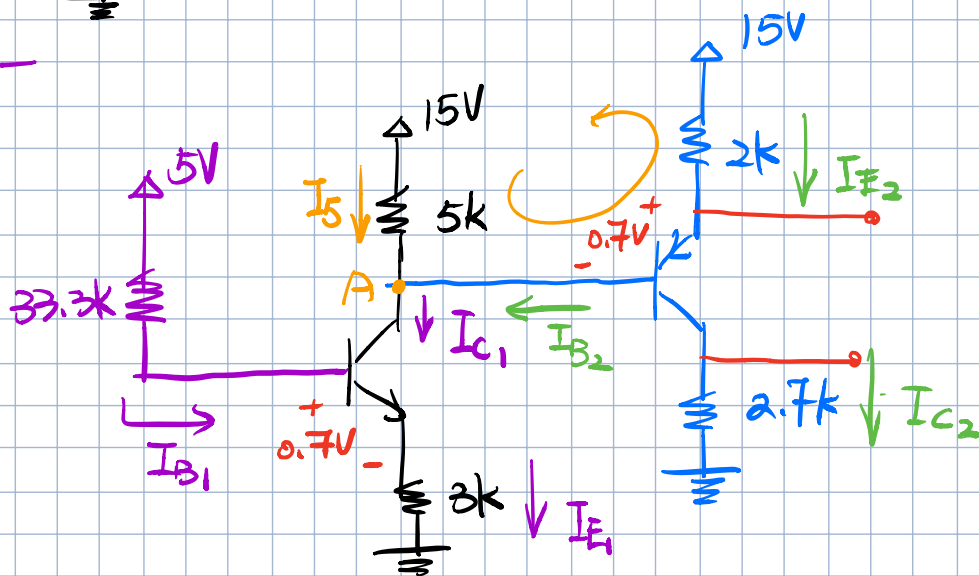
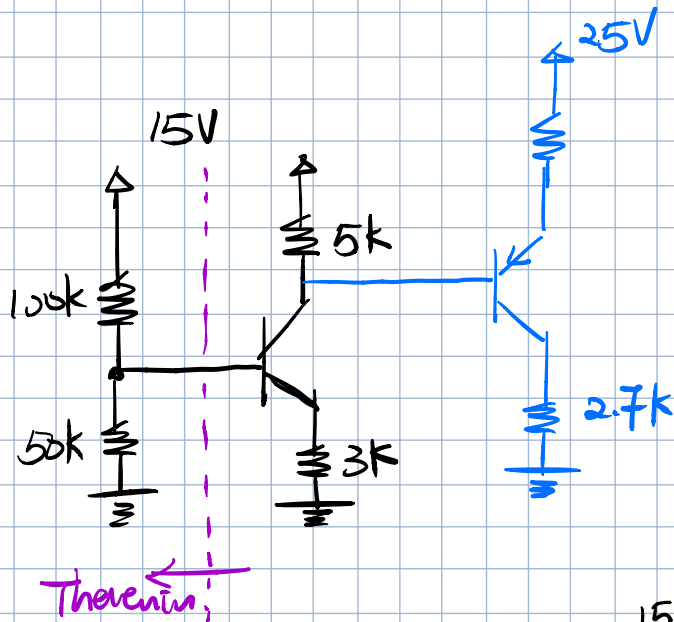
$$V_E = 3k \times I_E = 3.87V$$

$$\left\{ \begin{array}{l} V_C > V_E \text{ good } \ddot{\smile} \\ V_{CE} > 0.3V \end{array} \right\}$$

$$I_C = I_E - I_B = 1.28 \text{ mA}$$

assumption correct.

example 4. ( $\beta = 100$  in active mode)



KVL:

$$3k I_{E1} + 0.7V + 33.3k I_{B1} = 5V, \quad I_{B1} = I_{E1} / \beta + 1$$

$$I_{E1} = 1.29 \text{ mA} \quad I_{C1} = \frac{\beta}{\beta + 1} I_{E1} = 1.28 \text{ mA}$$

$$\text{KCL @ A: } I_5 + I_{B2} = I_{C1} \quad I_5 = I_{C1} - I_{B2}$$

KVL:

Eq.

$$5k(I_5) = 0.7 + 2k(I_{E2}), \quad \text{因为两边都是从 Node A} \rightarrow 15V$$

$$I_{B2} = \frac{I_{E2}}{\beta + 1}$$

$$\Rightarrow 5k(I_{C1} - I_{B2}) = 0.7 + 2k(I_{E2}), \quad I_{E2} = 2.78 \text{ mA}$$

$$I_{C2} = \frac{\beta}{\beta + 1} I_{E2}$$

$$I_{C2} = 2.75 \text{ mA}$$