

Review of 241

Representing Information.

- In computer hardware circuits (logic circuits, memory) all information is represented using only two symbols ϕ (electrical ground), 1 (power supply voltage)

Info. are encoded as combinations of ϕ 's & 1's.

Example.

Numbers: we represent numbers in base 2 (binary), because the values of the digits are only ϕ 's and 1, so, all numbers are composed of powers of 2

$$\text{eg. } (167)_{10} = (?)_2$$

$$167 = 128 + 32 + 4 + 2 + 1$$

$$= 2^7 + 2^5 + 2^2 + 2^1 + 2^0 = (10100111)_2$$

Each binary digit is called a bit. Four bits is a nibble. and 8 bits is byte.

It's very useful to memorize powers of 2.

<u>power</u>	<u>values.</u>	<u>power.</u>	<u>values.</u>
0	1	11	2048 (2K)
1	2	12	4K
2	4	\vdots	\vdots
3	8	20	1024 K (1 M)
4	16	21	2 M
5	32	22	4 M
6	64	\vdots	\vdots
7	128	30	1024 M (1 G)
8	256		
9	512		
10	1024 (1 K)		

Hexadecimal. (Base 16)

we use hex as a shorthand for writing a binary value. Each hex digit is a nibble (4 bits)

<u>Hex.</u>	<u>Binary.</u>	<u>Hex.</u>	<u>Binary.</u>
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0	0000	8	1000
1	0001	9	1001
2	0010	A	1010
3	0011	B	1011
4	0100	C	1100
5	0101	D	1101
6	0110	E	1110
7	0111	F	1111

eg. $(1023)_{10} = (?)_{16}$.

$$= 1024 - 1 = 10000000000 - 1$$

$$= (\underbrace{011}_{3} \underbrace{1111}_{F} \underbrace{1111}_{F})_2 = (3FF)_{16}$$

Note: }

$$3 \times 16^2 + 15 \times 16^1 + 15 \times 16^0 = 768 + 240 + 15 = 1023$$

Addition, Subtraction.

Addition.

Decimal	Binary	hex	Binary
5	0101	3A2	00110100010
+ 6	+ 0110	+ 168	+ 000101101000
11	1011	50A	01010001010

Subtraction.

Decimal	Binary
8	1000
- 5	- 0101
3	0011

* we had to borrow from the 2^3 column, which is 8. Then $8 - 1 = 7 = 111$, which means the result was 11 with carry 11.

- This is awkward, so instead we perform subtraction in computer hardware by

adding the negative.

eg.

8	8
- 5	+ (-5)
3	3

$$\Rightarrow \begin{array}{r} 1000 \\ + 1011 \\ \hline 0011 \end{array}$$

ignore the carry out.