

# Graph- and behaviour-based machine learning models to understand program semantics

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## ABSTRACT

Applying machine learning to detect patterns in programs requires finding a suitable form to input these programs into models. In this work, we look into two recently explored directions which do not require manually feature engineering: using the graph structure of programs and using execution traces. Both representations are proposed for different reasons: graph-based models can capture the syntactical properties of programs well and perform simple reasoning on them, while execution trace models are closer to program semantics. We explain applications of both models and then compare them on a set of tasks. We find that although both models are used for similar applications, different datasets make it hard to compare them. More research is needed to better explain the strengths of each model.

## 1 INTRODUCTION

Many tools that operate on source code require a form of program analysis. This includes compilers needing to understand program semantics for optimization, static analyzers designed to prevent bugs in programs or development environments suggesting code completions and improvements. Traditionally program analysis relies on abstraction, since finding an exact solution to the underlying problems is often computationally infeasible or even impossible.

But with the availability of large open source code repositories such as GitHub<sup>1</sup>, a new form of program analysis based on methods from machine learning is possible. The fundamental principle of this form of program analysis is captured by the following hypothesis, the natural code hypothesis, as stated by Allamanis et al. [2]:

Software is a form of human communication; software corpora have similar statistical properties to natural language corpora; and these properties can be exploited to build better software engineering tools.

For example, identifier names are not chosen randomly, but instead based on the meaning of the variable and therefore convey useful information for analysis. This suggests using machine learning models for code.

A straightforward method then is to take models that have proven successful in natural language processing and apply them to source code. Various applications of this pattern have been described in the literature [1, 7, 18, 24]. The advantage of this method is that models working on sequences are well understood. Additionally, since the order of tokens is preserved, the models can also learn from that information. But the disadvantage is that these models generally are not well suited to deal with long-range dependencies

such as those found when referring to variables and other structures that might be far away in the token representation of source code.

For this reason, new variants of representing programs as input to machine learning models have been explored. Here, we will focus on two variants: graph-based models that rely on the graph structure of source code and behaviour-based models that make use of the executability of source code to find patterns in data gathered from execution traces. First, we give a description of both models and its application to computer programs. Then, we compare them on two tasks (method naming/classification and invariant inference). We finish with a conclusion summarizing the results and listing some interesting future research directions.

## 2 GRAPH-BASED MODEL

Graphs are a natural representation for source code, due to its highly interlinked structure. In this section, we will thus look at models that can learn from the graph structure. A flexible model for this task is the message passing neural network, introduced by Gilmer et al. [17]. Message passing neural networks are a variant of graph neural networks. We will first explain the general concept of message passing neural networks in section 2.1. We then compare different concrete instances of this model applied to source code in section 2.2.

Message passing neural networks are not the only kind of model that uses the graph structure for learning. One alternative is described by Raychev et al. [29] where they use conditional random fields to predict types and names of JavaScript variables. Program elements are represented as nodes in a graph with edges between them capturing dependencies in their properties. The probabilistic weight of the dependency can then be learned from data, and the model be used to predict the unknown properties from known properties, taking into account the dependencies between the predictions. We will however focus on message passing neural networks here since they are the most popular model found in literature and have successfully been applied to several different tasks (see also section 4).

### 2.1 Message passing neural network

We explain the concept of message passing neural networks following Gilmer et al. [17]. Let  $G$  be a directed graph, where edges from  $w$  to  $v$  have a label represented as  $e_{vw}$ . Two nodes can be connected by multiple edges of different kinds. In the application to source code, each edge kind often has an associated backward edge, allowing information to flow in both directions. These can just be treated as an additional edge with a label different from the corresponding forward edge.

Message passing neural networks start by assigning an initial hidden state  $h_v^0$  to each node  $v$  in the graph. The initial state is

<sup>1</sup><https://github.com>

usually computed as an embedding of the features of the node. At every timestep, the network then computes a message for each edge using the *message function*  $M$ . The messages of all edges with the same target are aggregated by an *aggregation function*  $h$ . This is a slight generalization of the original formulation, where  $h$  is always the sum of all incoming messages. The aggregated messages are then used to update the node hidden state, using the *update function*  $U$  for the node. The following two equations describe this process:

$$m_v^{t+1} = h(\{M(h_v^t, h_w^t, e_{vw}) | w \in N(v)\}) \quad (1)$$

$$h_v^{t+1} = U(h_v^t, m_v^{t+1}) \quad (2)$$

where  $N(v)$  is the set of all nodes  $w$  for which there is an edge from  $w$  to  $v$ . The steps are repeated for  $T$  timesteps. Finally, the overall prediction is computed by applying the readout function to the final hidden states  $\{h_v^T\}$  of all nodes:

$$\hat{y} = R(\{h_v^T | v \in G\}) \quad (3)$$

In this computation  $M$ ,  $U$  and  $R$  are all learnable differentiable functions. Because the whole system is differentiable, parameters can be learned with gradient-based optimization.

## 2.2 Applications to source code

Table 1 shows some applications of (variations of) message passing neural networks to source code. We will first look at different representations of source code as a graph. Then, we describe the concrete variant of message passing neural network used in these applications.

**2.2.1 AST representations.** A syntactic way to represent source code as a graph is the form of an abstract syntax tree (AST). This representation was first introduced by Allamanis et al. [4] and many further works are based on variations [3, 11, 16, 19, 20, 22, 30]. Semantic information is added to the AST in the form of additional edge types. An overview of the edge types found in these works is given in table 2. These edges add data flow and control flow information for variables to the graph. Since in practice the message passing network only uses a small number of timesteps, these additional edges are useful to capture dependencies that can potentially be far away in the graph. Allamanis et al. found that these semantic edges greatly increase the performance. Schrouff et al. [30] also use an AST representation, but group their 96 relationship types into only two categories: AST relation like child/parent and reference/traverse which are a kind of semantic edge.

Because each edge type requires its own parameters for the message generation step of the model, restricting the edge types can improve training time. Which edges are useful however depends on the task. In an ablation study, Allamanis et al. [4] found that restricting the edges to semantic edges (removing NEXT\_TOKEN and CHILD edges) affected the performance on the VAR\_MISUSE task much more than for the VAR\_NAMING task.

**2.2.2 CFG representations.** AST representations include information about syntax that may not be relevant for semantics, such as dead code. By basing their representation on a control flow graph

(CFG), CFG representations eliminate some of this “noise”. The tradeoff is that some of the superfluous information might still communicate useful information. For example, the order of statements in a control flow graph will be different to the source order, so this information can no longer be used by the model. Brauckmann et al. [11] compare both representations on the same tasks and find that there is no single best representation. They base their graph on a CFG extracted from the LLVM intermediate representation, so the nodes in the graph are LLVM IR instructions. A similar approach is followed by Cummins et al. [14]. They also construct a control flow graph and enrich it with data flow and call flow. In contrast to the work of Brauckmann et al. they include edge positions which differentiate the first operand/branch target from the second and so on. But nodes do not always need to be IR statements in a CFG representation. Si et al. [31] describe a variant where they first construct a control flow graph over statements over the SSA (static single assignment) form but then use the AST representation of each statement as nodes. Since the SSA form splits a single variable in potentially many differently named ones, they introduce variable linking which connects all instances of a single variable to a node named after the original name.

**2.2.3 Application-specific graphs.** A third possibility to represent programs as graphs is shown by Wei et al. [37]. They construct a type inference graph, which only contains predicates believed to be relevant for the type inference problem, such as subtype and usage edges. Nodes in their graph represent types that are known or need to be assigned. In contrast to the previous representation methods, this requires more thought about which edges to include for a given application and is thus a less general method.

**2.2.4 Propagation models.** Most of the applications listed in table 1 are based on a special kind of message passing neural network named Gated Graph Neural Network (GGNN) [23]. These networks use a simple linear layer on the source node as the message function (with different trainable parameters for each edge kind) and the gated recurrent unit (GRU) [13] as the node update function. For message aggregation, GGNNs sum all messages arriving at a single node.

There are only two models that are not based on the GGNN variant. Wei et al. [37] use a different model where they define specialized learnable message functions for each of their edge types. Since their graph is a hypergraph, they specify how to generate messages for each argument of a hyperedge. The specialized message functions are mostly based on multilayer feedforward networks. For aggregation and updating, they use a variant of the attention based aggregation operator proposed in graph attention networks [33].

Si et al. [31] also use a linear function to compute messages for each edges. Messages are aggregated separately for each edge type by summing and applying a nonlinear activation function. The update is performed by transforming each aggregated result by a linear function followed by a nonlinear activation function. Interesting to note here is that the update function is not based on the hidden state of the current node. This means that the model cannot directly propagate information from a node to itself in one step.

The remaining models either use the GGNN model directly [4, 11, 16, 19, 20, 22, 30] or apply slight changes to the message or

Reference	Graph	Task
Allamanis et al. [4]	enriched AST	VARNAMING, VARMISUSE
Allamanis et al. [3]	enriched AST	predict python type annotations
Brauckmann et al. [11]	enriched AST, CDFG+calls+mem	OpenCL device mapping, OpenCL thread corsening
Cummins et al. [14]	IR graph + control-, data- and call-flow	graph algorithms, device mapping, algorithm classification
Fernandes et al. [16]	enriched AST	METHODNAMING, METHODDOC
Hellendoorn et al. [19]	enriched AST	checking if extracted invariants are valid
Hellendoorn et al. [20]	enriched AST	VARMISUSE
Li et al. [22]	enriched AST	predict log level for log statements
Schrouff et al. [30]	AST + reference/traverse	predict javascript type annotations
Si et al. [31]	SSA-CFG with AST + variable-linking	embed semantics for loop invariant prediction
Wei et al. [37]	type variables with relationships	predict python type annotations

Table 1: Different applications of message passing neural networks to source code tasks

Edge	Edge between ...
NEXTTOKEN	two consecutive token nodes or sub-token nodes
INTOKEN	sub-tokens and the AST token node
NEXTLEXICALUSE	lexical uses of the same variable (independent of data flow)
CHILD	AST syntax nodes and their children
NEXTMAYUSE	variable tokens and next potential uses of that variable
ASSIGNEDFROM	left hand side of an assignment and right hand side
COMPUTEDFROM	variable appearing in the LHS of an assignment and all variables appearing in the RHS
OCCURRENCEOF	tokens referencing a symbol and nodes that bind this symbol
FORMALARGNAME	arguments in method calls and formal parameters in the signature
LASTWRITE	syntax node at which a variable was written to and variable token
LASTREAD	syntax node at which a variable was read from and variable token
RETURNSTO	return token and the method declaration
GUARDEDBy	variable to enclosing guard expression that must be true for this statement to be executed and uses this variable
GUARDEDByNEGATION	variable to enclosing guard expression that must be false for this statement to be executed and uses this variable

Table 2: Edge types used to enrich AST graphs

aggregation function. All models use the GRU unit as update function. For type prediction, Allamanis et al. [3] use the elementwise maximum instead of sum for aggregation, as it lead to better results. Intuitively, the elementwise maximum corresponds to a meet-like operation on a lattice defined over  $\mathbb{R}^N$ . Cummins et al. [14] change the message function to add a sinusoidal positional encoding of the edge position to the hidden state of the source node before applying a linear function. All other models in table 1 use GGNN unmodified.

**2.2.5 Initial node embedding and readout.** Before message propagation, an initial node embedding must be computed for all nodes in the graph. These initial node embeddings are commonly computed from a set of features of the nodes. For AST representations, these features include the ast node type (corresponding to the grammar symbol represented by this node), node properties (a BinaryExpression ast node might have a node property specifying the operator) and node value (for literal and terminal nodes) [4, 11, 30]. In statically typed languages, it is also possible to include types of variables as features [4]. All features are then converted

to vectors using an embedding layer. For the terminal AST nodes which correspond the the source code tokens, an alternative is to use embeddings generated by a sequence neural network over the original token sequence. This approach is followed by Fernandes et al. [16] and Hellendoorn et al. [20]. For graphs containing IR instructions, the space of possible node values is large due to the possible operand values. Cummins et al. [14] therefore construct the embedding by applying an embedding layer to the normalized instruction using using `inst2vec` [8].

After message propagation, the resulting graph needs to be interpreted in the context of the concrete application. Predictions about properties of single nodes can directly be computed from the hidden state of that node [3, 22]. To compute a graph-level vector, a weighted average of a projection of each hidden state can be computed [11, 16]. Both the weight (attention) and the projection are learnable functions that take the hidden state and in some variants [14] also the initial embedding of the node as an input. Fernandes et al. [16] combine this graph vector with the sequence embedding by another linear layer. Instead of computing the average over all nodes of the graph, Hellendoorn et al. [19]

only average the nodes that appear in the invariant. A similar approach is followed by Allamanis et al. [4], where they average over all nodes corresponding the slots of the variable whose name they want to predict. Si et al. [31] keep all embeddings as external memory which can be queried by another model through an attention-based mechanism.

### 3 BEHAVIOUR-BASED MODEL

Behaviour-based models make inferences about properties of source code by learning from data gathered from its runtime behaviour. They provide an interesting alternative to the graph-based models in applications that focus on how the code behaves (semantics) instead of how it was written (syntax). These models should therefore be more resistant to syntactic modifications. Some applications of execution-based program features are shown in table 3.

A behaviour-based model of a program works by learning features from the executions of programs. The first question hence is how to represent these executions. The most common form here is to consider executions as a sequence of program states, where each program state is represented by a set of variable assignments [25–27, 34, 38]. Since such trace data contains a lot of unique states, Henkel et al. [21] suggest abstracting the trace to only include a subset of relevant information represented as symbolic events. Events include for example function calls, function returns, equivalence of return values and function parameters. Symbolic traces are also used in the model of Wang and Su [36] in combination to the concrete traces. This is an improvement to prior work by the same authors [34] which only used concrete traces. Instead of simple vectors of program variables, we can also represent the program state as a graph. It is useful if the underlying programming language supports references or pointers, where variables or memory locations can naturally “point” to other locations in the program state. This representation is used by Brockschmidt et al. [12] and Li et al. [23] in a system that learns a heuristic to generate predicates in separation logic that describe invariants of data structures on the program heap (a problem named shape analysis). Whereas the former work uses simple manually extracted features from the graph, Li et al. [23] use the gated graph neural networks described in the previous section to automatically learn features for this task.

There are two fundamentally different approaches to apply machine learning to the data extracted from program executions. The first variant is to train a single embedding or model on executions of different programs. This is then later applied to new unseen programs. Almost all the listed works in table 3 follow this approach, except two. Yao et al. [38] train a model to capture the behaviour of a single program, and then extract a loop invariant from the parameters of this model. A combination between both approaches (learning a shared model for all possible executions and representing a single program by an optimized model) is described by Piech et al. [27]. They learn an encoder from the program state into a new space where the training programs can approximately be described as linear functions. Using these linear functions as features of programs, they then train a classifier that can predict whether feedback given to one program can be propagated to a program by a different student. Both these applications make use of the fact

that machine learning models are often good generic function approximators. The approach has similarities to the field of program synthesis, especially the task of inferring a program or formula that satisfies certain input output examples.

Compared to graph-based models, there appear to be less works that use executions as a feature for learning over programs, especially ones using deep learning methods. Reasons for this may be that it is harder to gather execution data since it requires running the program which is impossible in some situations (such as incomplete programs during development, although this can be mitigated slightly by symbolic techniques which can execute single functions) and must be implemented separately for each supported programming language. That makes this approach more complex to implement compared to the graph-based one. Main applications include the use in tutoring systems to give feedback to students [25, 27] and inferring invariants [12, 26, 38]. Intelligent tutoring systems often contain a very controlled programming environment, sidestepping some of the issues associated with gathering execution traces.

## 4 EVALUATION

We have seen two different approaches to capture the meaning of source code with machine learning. In this section, we attempt to look at how well these two approaches perform. Unfortunately, comparison between the different architectures is difficult, since they are often applied to different tasks. Even when the tasks are similar (for example, both models applied to infer types), sometimes the results are not comparable because they are evaluated on different datasets. These factors prevent a clear comparison. This issue has been noted before by Bourgeois [10, p. 71]. Therefore, we instead will look at the results for some specific tasks where we find some overlap between different papers and note interesting observations and research questions for each task where applicable.

### 4.1 Method naming/classification

In this category, we look at models solving the task of assigning a set of labels or names to methods. We consider two slightly different tasks: METHODNAMING and program classification. METHODNAMING requires the model to predict the name of a method from its body. This task is often used to evaluate the accuracy of embeddings, since gathering data for it is easy (take a dataset with methods and remove the names) and the name of a method provides a succinct description of its meaning. Program classification is a related task that instead requires classifying short programs according to the implemented algorithm or solved task. The implementations for both tasks considered in this section are shown in table 4. Only results relevant to the comparison between graph and behavioural approaches are shown.

A baseline for METHODNAMING is given by a non-graph based model by Alon et al. [6]. They train a model that learns to embed and aggregate paths through the AST of the method automatically. In contrast to their previous code2vec [7] model, code2seq uses a sequence based decoder based on this embedding. The model is evaluated on a dataset of 11 large java projects (java-small [5])



Reference	Description
Yao et al. [38]	extract non-linear loop invariants from learned model of loop behaviour
Henkel et al. [21]	generate function vectors from usage contexts
Wang [34]	learn representation of program semantics from execution trace
Padhi et al. [26]	learn preconditions from program traces
Li et al. [23]	learn separation logic formulas for heap objects
Brockschmidt et al. [12]	learning shape analysis
Piech et al. [27]	learn program embedding to propagate student feedback
Paaßen et al. [25]	identify algorithm, errors and error location for student feedback

Table 3: Application using runtime features of program executions

and two new datasets consisting of top-starred java projects from GitHub (java-medium, java-large). Fernandes et al. [16] improve on this result by using graph neural networks for the encoding step. Their best result is a combination of a gated graph neural network combined with sequence neural networks for encoding, combined with a new decoder (an LSTM network with a pointer network-style copying mechanism). An interesting result for this comparison is that they find networks augmented with graph neural networks to outperform baseline methods which do not use any graph networks. There is also one implementation of a technique based on concrete and symbolic execution traces for the method naming tasks by Wang and Su [36]. However, the authors evaluate their model on a different dataset, so comparison to state of the art is difficult. The motivation they give for using their own dataset is that it is more complex by using algorithmically focused methods from company interviews, in contrast to the java dataset which contains a lot of simple functions such as getters and setters. They retrain code2seq for this dataset, reporting lower results, which suggests the hypothesis that the dataset is more complex. It would be interesting to see how the model by Fernandes et al. [16] performs in comparison on this dataset, and how well their model performs on the java-small and java-large datasets.

The LiGER model is an extension that adds support for symbolic traces to an earlier model called DyPro by the same authors which was also behaviour-based. Both of these models have also been tested for the challenge of program classification, which is a more natural task for behaviour based models due to being inherently tied to the semantics of the program. The evaluations for this model are based on a custom dataset known as CoSET [35] which consists of hand-labelled programs from an online coding competition. The results show that the behaviour-based methods (DyPro, LiGER) significantly outperform the gated graph neural network variant. However, the exact AST graph representation used for the GGNN variant was left unspecified in this evaluation. That the graph representation matters is shown by Cummins et al. [14]. Their graph representation is flow, position and value sensitive, and in the evaluation they show that this graph representation leads to improved classification rates using a GGNN on the POJ-104 dataset. The POJ-104 dataset contains implementations of 104 different algorithms submitted to a judge system. They compare their result to prior work by Ben-Nun et al. [8] which didn't use graph neural networks and also used a graph representation that did not preserve the node positional order. This shows that

the learning from structure that graph neural networks provide is useful. The question that remains is how well the LiGER approach would perform on this dataset.

In summary, we can observe that graph-based representations provide useful signal for both tasks, improving over prior sequence based implementations. There are some indications that behavioural approaches might have an advantage when the dataset contains many algorithmic-heavy functions but a direct comparison is not possible due to different implementations and datasets.

## 4.2 Invariant inference

Invariant inference is a task from program verification. An invariant is a property that is preserved by a certain code sequence whenever it is executed. One use case for invariants is proving statements about loops. Because the invariant is preserved no matter how many times to loop body is executed, it allows reasoning about the state after the loop without explicitly considering the number of iterations. Automatic loop invariant inference is thus an important problem for program verification.

Because the task requires inferring correct invariants, a natural choice is to apply a formal checker to the candidate invariants predicted by the model. The checker can also return additional counter examples to improve the prediction. This iterative process is known as counterexample guided inductive synthesis (CeGIS [32]). This approach is followed by Si et al. [31], where they use graph based model to convert the program to a structural external memory. It then uses this external memory in a reinforcement-based learning process interacting with the formal checker. The model can solve 106 of 133 benchmark programs compared to 100 solved by the best state of the art algorithm, but only if given a larger timeout than in the original competition. We can see from this result that graph-based embeddings are useful to predict loop invariants. They also perform an ablation study with an LSTM instead of the graph neural network, showing that the LSTM-variant can solve 13 fewer instances. This is supported by work of Helledoorn et al. [19], where they use a gated graph neural network to reduce the false positive rate of invariants extracted by the dynamic analysis tool Daikon [15].

Behavioural based models should be well suited to this problem, since loop invariants essentially require fitting formulas that hold over all possible runtime traces. And indeed, as demonstrated by Yao et al. [38], fitting a model (gated continuous logic networks) to

Reference	Task (Metric)	Dataset	Results ( <i>model score</i> )
code2seq [6]	METHODNAMING (F1)	java-small	<i>code2vec</i> 18.62 <i>code2seq</i> 43.02 <i>TreeLSTM</i> 35.46
code2seq [6]	METHODNAMING (F1)	java-large	<i>code2vec</i> 42.73 <i>code2seq</i> 59.19 <i>TreeLSTM</i> 53.63
structured neural summarization [16]	METHODNAMING (F1)	java-small	<i>BiLSTM+GNN</i> $\rightarrow$ <i>LSTM+POINTER</i> 51.4 <i>code2seq</i> 43.0
LiGER [36]	METHODNAMING (F1)	named methods from algorithmic focused company interviews	<i>code2seq</i> 0.39 <i>LiGER</i> 0.57
LiGER [36]	program classification (F1)	CoSET [35]	<i>code2vec</i> 0.68 <i>GGNN</i> 0.70 <i>DyPRO</i> 0.81 <i>LiGER</i> 0.85
DyPRO [34]	program classification (F1)	handpicked problems from popular online coding platforms	<i>TreeLSTM</i> 0.61 <i>GGNN</i> 0.66 <i>DyPRO</i> 0.78
NCC [8]	program classification (accuracy)	POJ-104	NCC 94.83 <i>TBCNN</i> 94.0
ProGRAML	program classification (accuracy)	POJ-104	NCC 94.83 <i>XFG-GGNN</i> 95.71 <i>GGNN-s</i> 96.13 <i>GGNN</i> 96.22 <i>Transformer</i> 96.67

Table 4: Implementations for method summarization/classification

the traces and then extracting the formula from that model can perform well, solving all but 9 of the 133 problems in the benchmark set of Si et al. [31]. The remaining 9 problems are claimed to be theoretically unsolvable.

The gated continuous logic network by Yao et al. [38] is trained for each single program separately, learning to represent only the behaviour of that program. In contrast, Wang [34] show a different approach where they train a single generic model to embed program traces, and apply this model to the task of verifying whether an invariant for a loop is valid or not. They evaluate their approach on a dataset generated by formally checking invariants produced from Daikon to obtain positive examples and add negative examples based on mutations of those. In both accuracy and F1-score, the model beats a similar model trained using a gated graph neural network and one using a TreeLSTM. They do however simply use the hidden state of the root node of the AST to extract a single embedding from the GGNN, and leave the concrete edges they used in their graph representation of code unspecified. It would be interesting to see the results for the GGNN for on an enriched AST, where the embedding is extracted by averaging the hidden state of the nodes which are present in the invariant, as in Hellendoorn et al. [19].

In general, there are still not enough comparable works on predicting loop invariants with different models to reach a definite answer on which model is better. While both architectures capture enough information to be useful predictors for invariants, they are evaluated in different settings (verifying invariants vs generating) and on different datasets. A possible future research question could be to evaluate them in a common setting, to obtain insights into how well the different program representations perform.

## 5 CONCLUSION

We have seen that both graph- and behaviour-based models have their use for learning tasks over source code. Although they are applied for similar tasks, a direct comparison is often not possible due to different datasets and slightly different applications. Some future research directions could be:

*Can we design a generic benchmark dataset to compare these models on?* As seen in the evaluation, models are often evaluated on ad-hoc tasks and benchmark sets. It would be good to have a standardised, commonly accepted set of tasks along with datasets to compare different approaches on. This could also be helpful in determining if there are generic, pre-trainable models that work well for a wide variety of tasks as is the case for NLP.

*What are the weaknesses and strengths of each model?* Graph based models can exploit syntactical similarities taking hints from the programmer, which behaviour based models cannot. There are indications that this makes behaviour based models better for complex code that is algorithmic in nature. To explore this, a comparison of state of the art models for both tasks on the same dataset is needed. An interesting avenue in this direction is also work on adversarial examples, which provide insight into what properties of the input data these model bases their predictions on [9, 28, 39, 40].

*Can both models be useful in combination?* Related to the previous question, if these models have different strengths, a variant that capitalizes on both models' strength by combining could be explored.

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