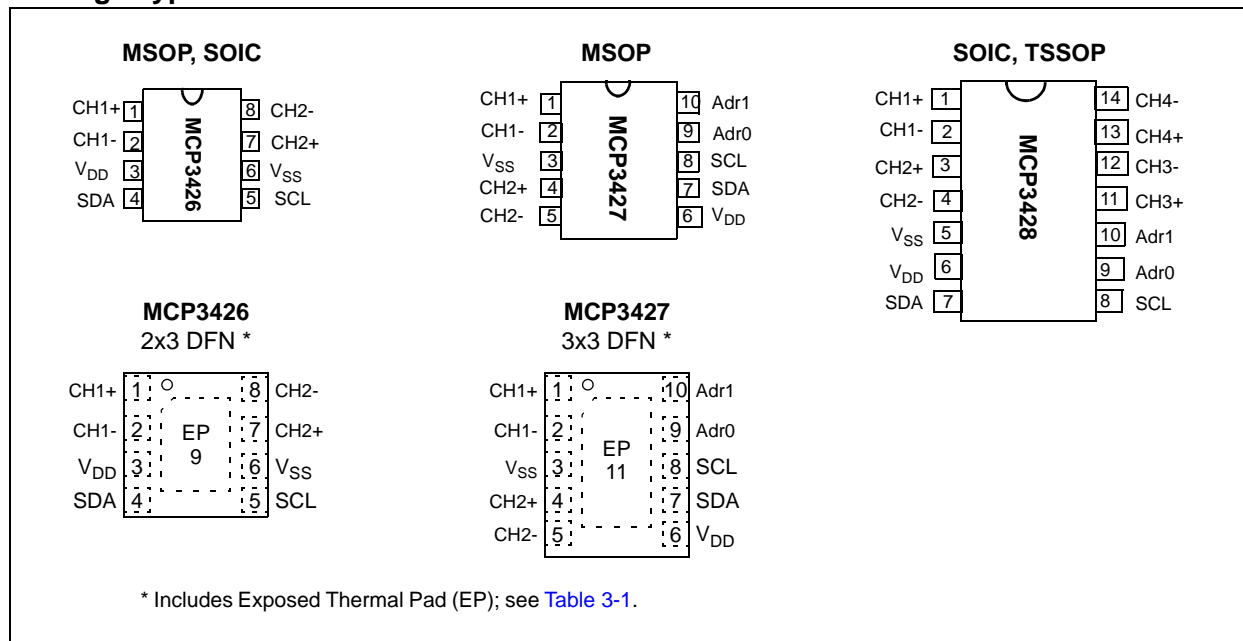


MCP3426/7/8

The I²C address bits for the MCP3427 and MCP3428 are selected by using two external I²C address selection pins (Adr0 and Adr1). The user can configure the device to one of eight available addresses by connecting these two address selection pins to V_{DD}, V_{SS} or float. The I²C address bits of the MCP3426 are programmed at the factory during production.

The MCP3426 is available in 8-pin SOIC, DFN, and MSOP packages. The MCP3427 is available in 10-pin DFN, and MSOP packages. The MCP3428 is available in 14-pin SOIC and TSSOP packages.

Package Types



MCP3426 Functional Block Diagram

