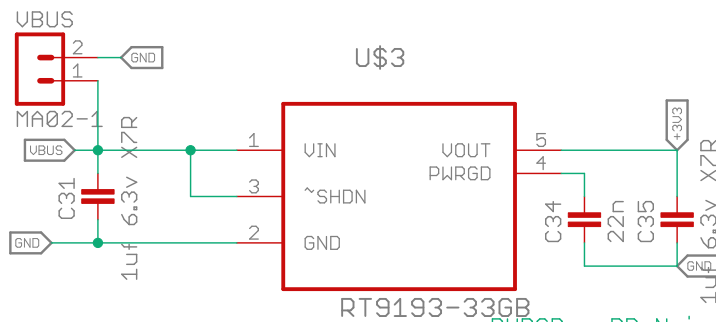
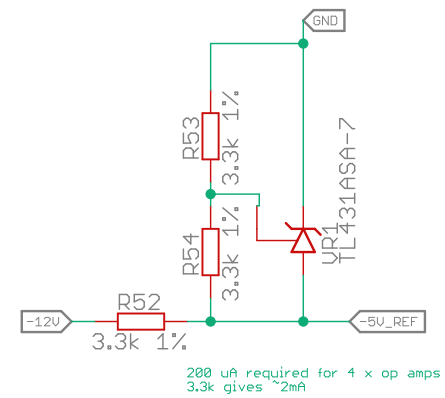


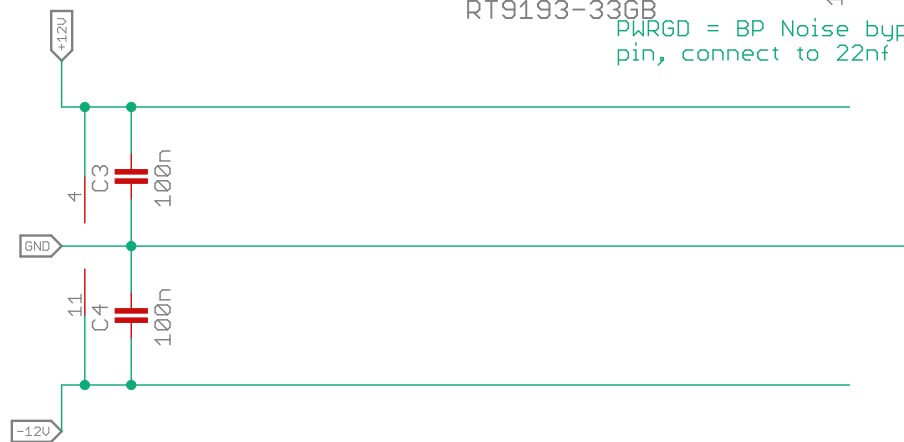
1117 pins:
1 - GND
2 - OUT
3 - IN



U\$3

RT9193 pins
1 - IN
2 - GND
3 - ENABLE
4 - NOISE BYPASS
5 - OUT

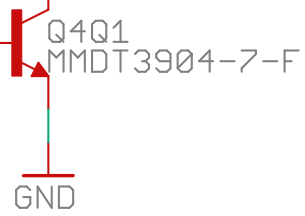
PWRGD = BP Noise bypass
pin, connect to 22nf cap



PULSE_IN_1

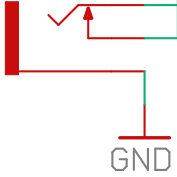


100k
R22

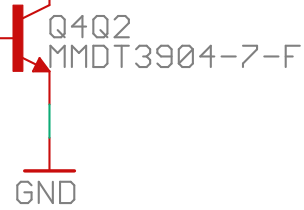


PULSE_1_INPUT

PULSE_IN_2



100k
R16



PULSE_2_INPUT

0v to 12v input,
above 0.4v = pulse
from Branches
Uses pullup on pin to drive transistor

PULSE_1_RAW_OUT

10k
R36



10k
R37
+12V



PULSE_OUT_1

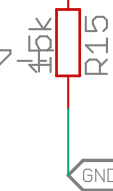
6.5v pulses

PULSE_2_RAW_OUT

10k
R8



10k
R14
+12V



PULSE_OUT_2

6.5v pulses