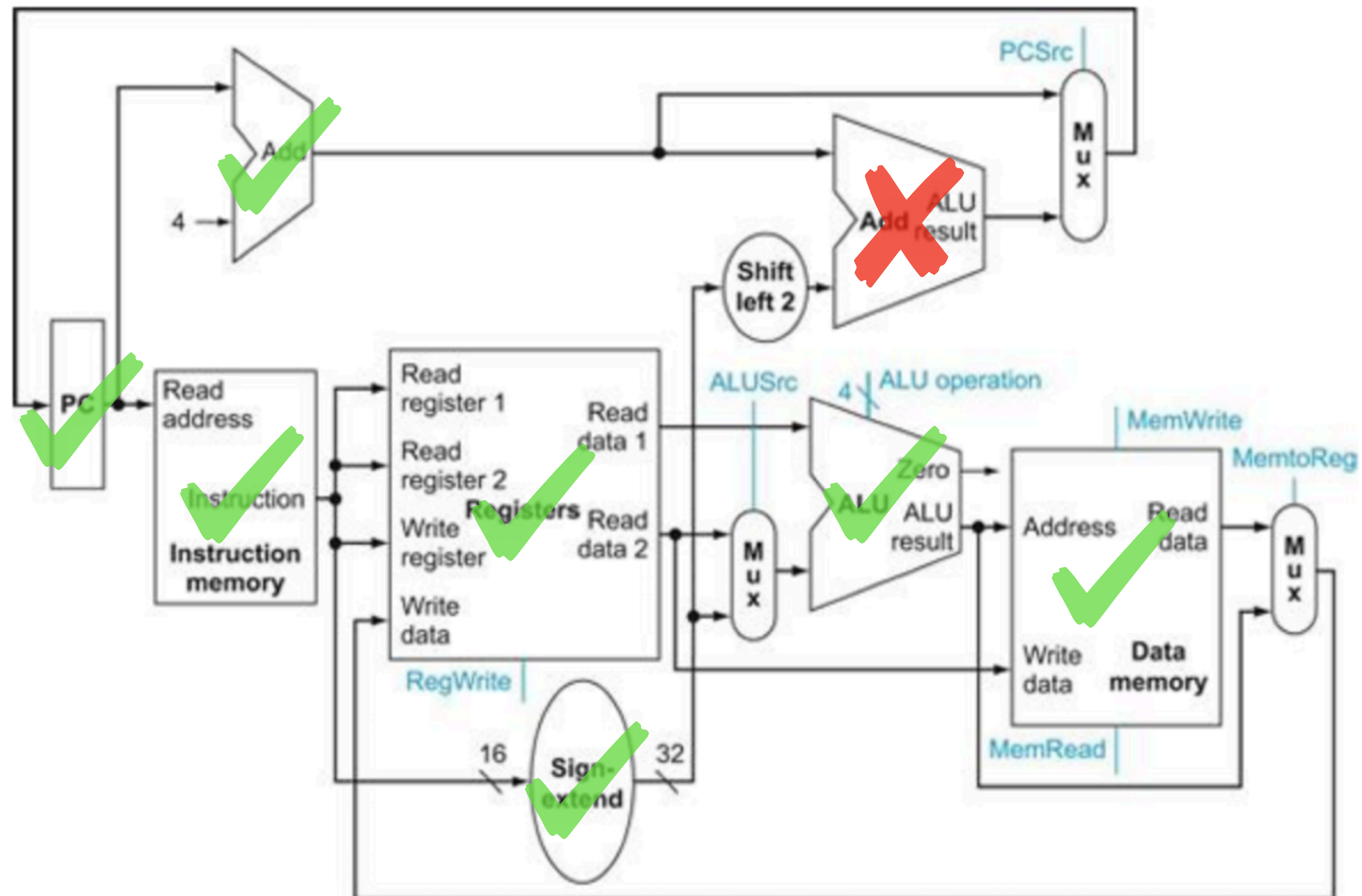


# CPU instruction execution

Instruction: **sw \$s0, 0(\$t0)**



## **6. Assembly language (MIPS)**