

6. Assembly language (MIPS)

MIPS assembly language

- MIPS: stands for "Microprocessor without Interlocked Pipeline Stages" (not relevant, basically just means that CPU uses simple execution model)
- Type of RISC (Reduced Instruction Set Computer) ISA (Instruction Set Architecture) - popular example of a RISC ISA is RISC V, open standard that began development in 2010 at UC Berkeley
- Will go through some sample MIPS programs to get an idea of how assembly language works