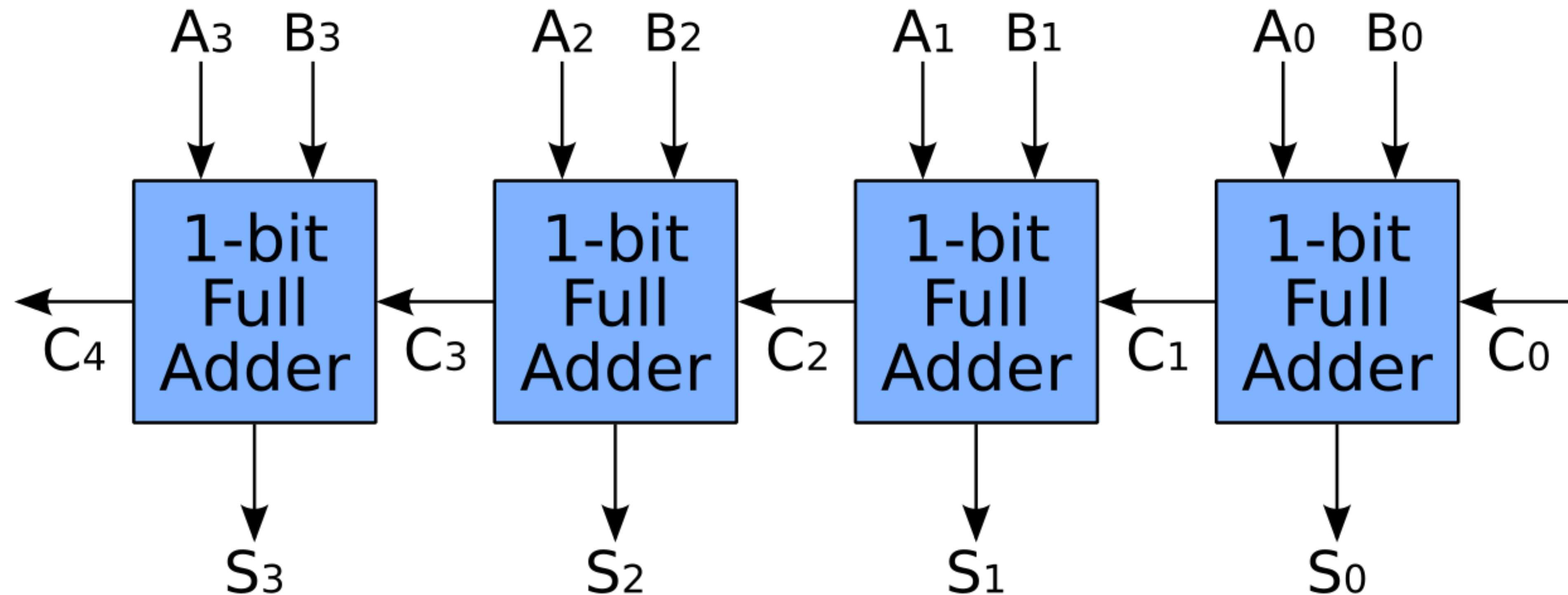


# Adding with logic gates: 4 bits at a time

Can add two 4-bit numbers one bit pair at a time



# Adding or subtracting 4 bits

Can select which operation to use. Inverting bits means negating

