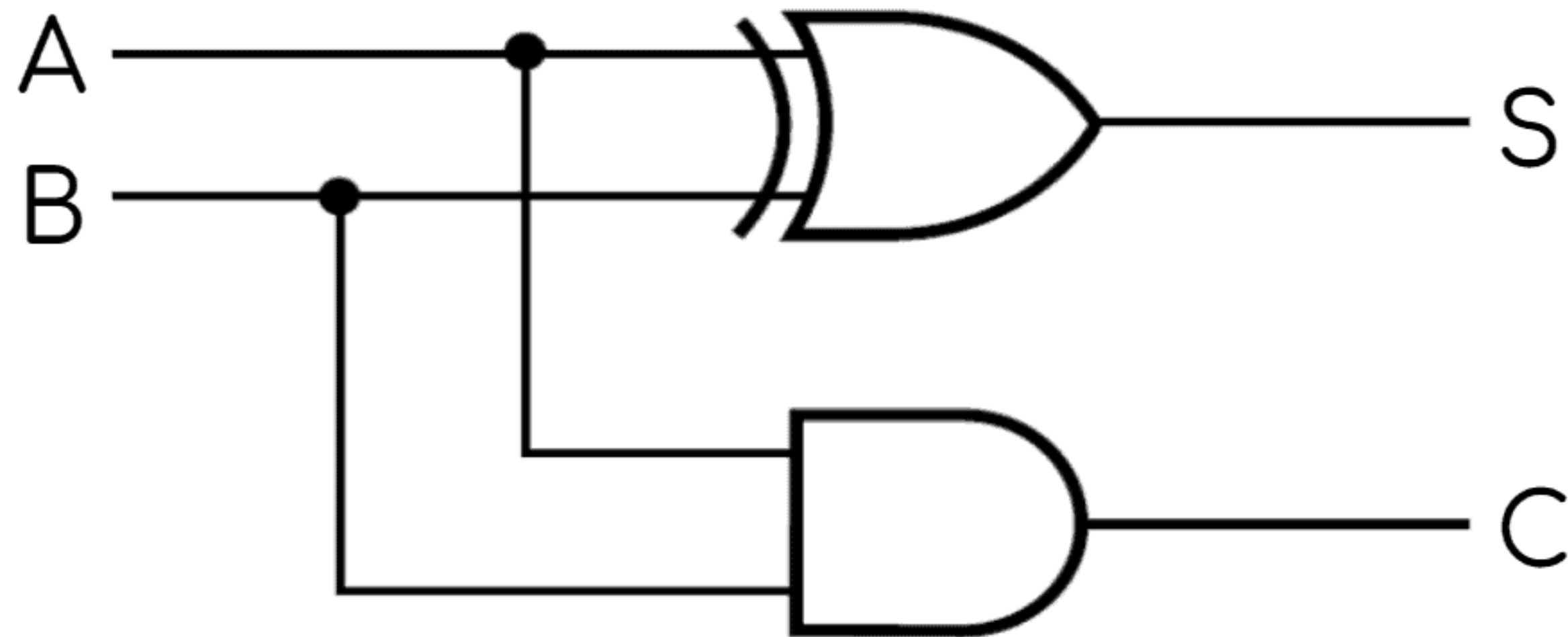# Adding with logic gates: Half adder

Outputs: Sum and Carry

Adding with logic gates: Full adder
Take a Carry bit as an input, in addition to the previous two inputs