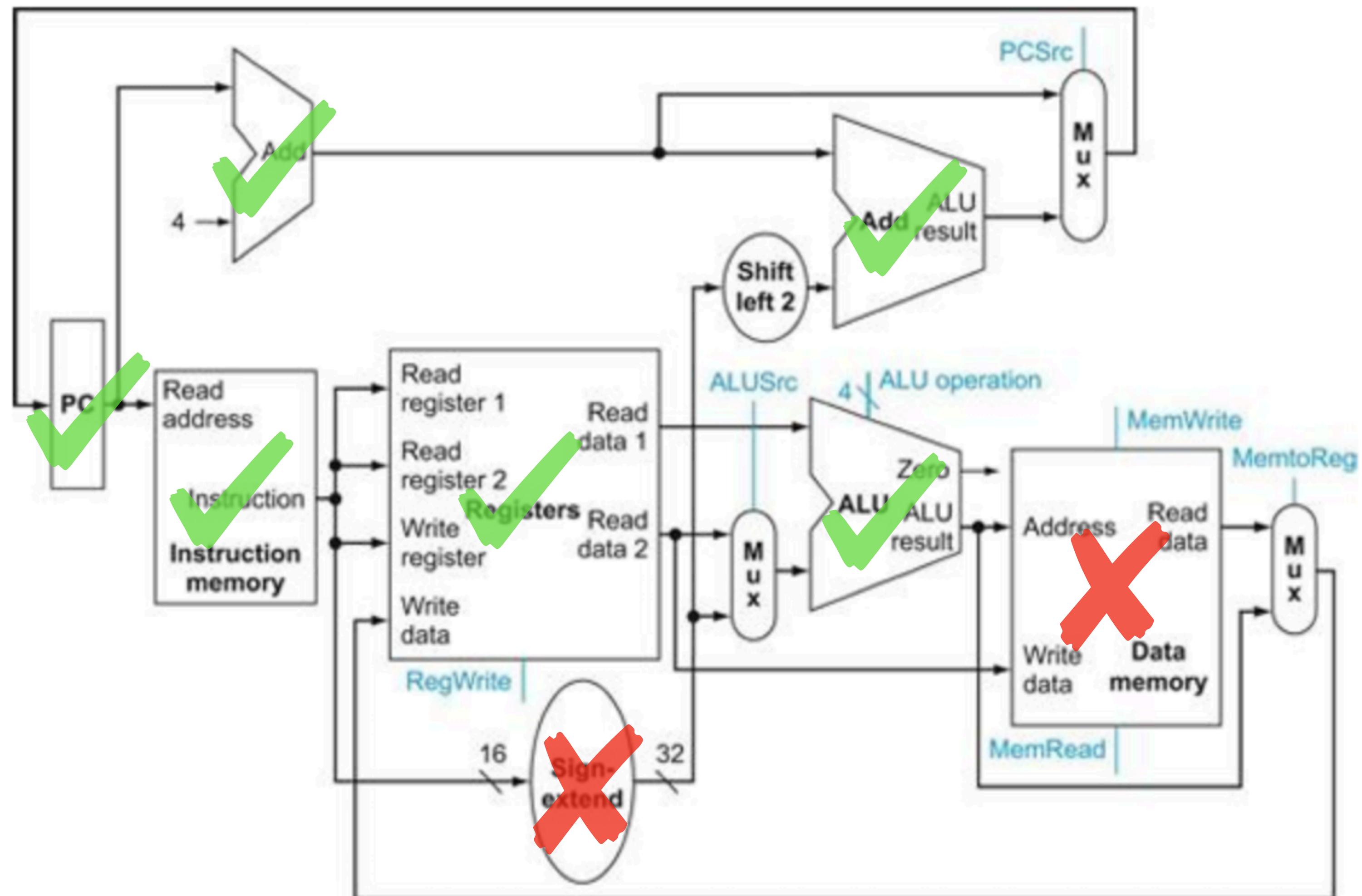# CPU instruction execution

Instruction: **beq $s1, $s2, LABEL**

CPU instruction execution

Instruction: **sw $s0, 0($t0)**