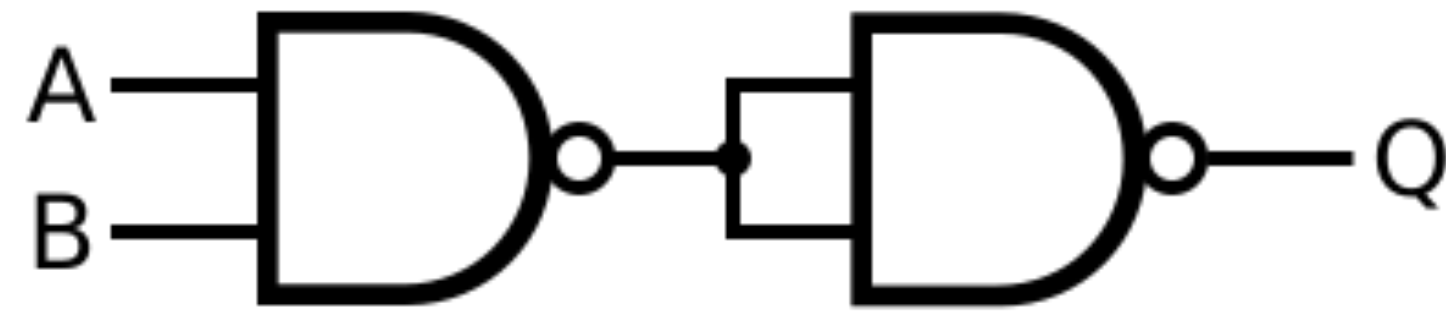
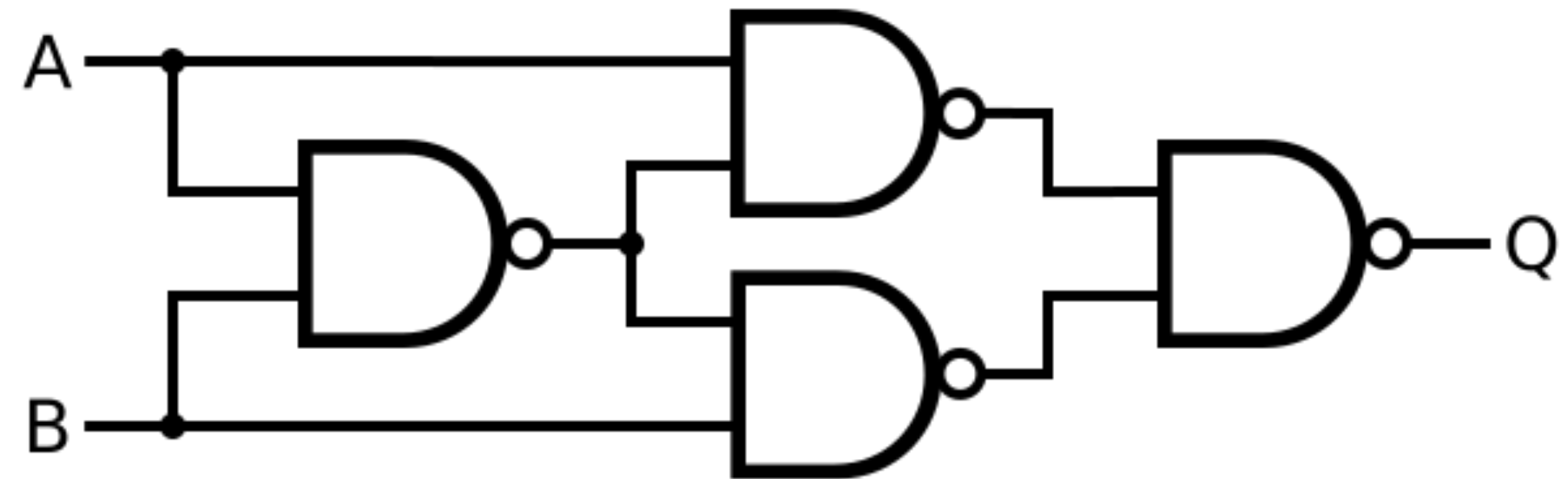


NAND is all you need

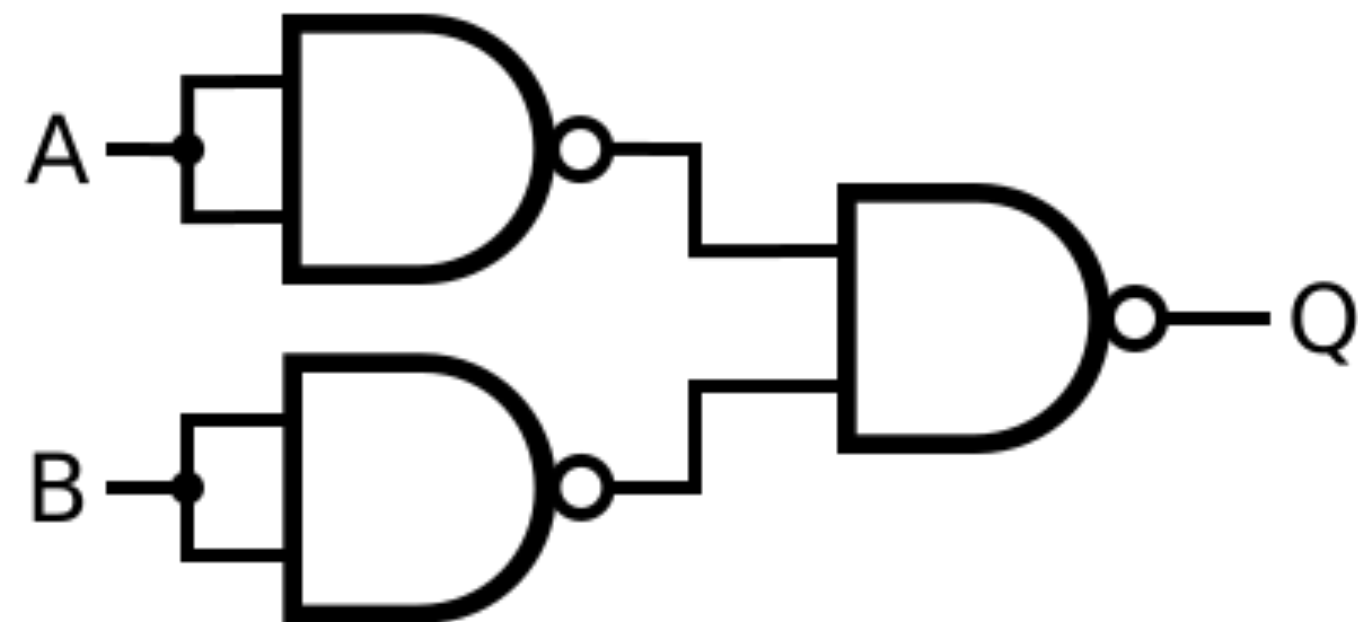
Can make all other gates from NAND



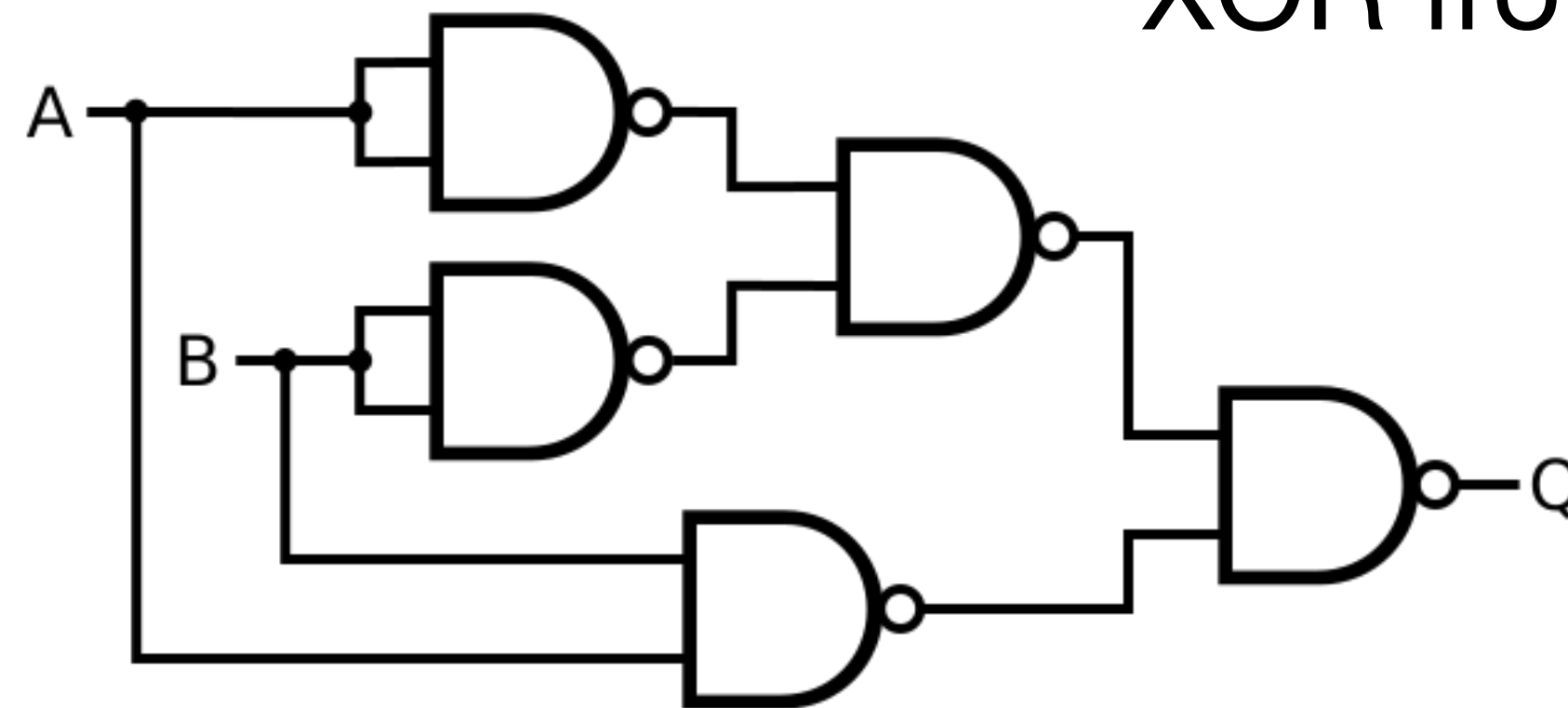
AND from NAND



XOR from NAND



OR from NAND



XNOR from NAND



NOT from NAND

2. Transistors