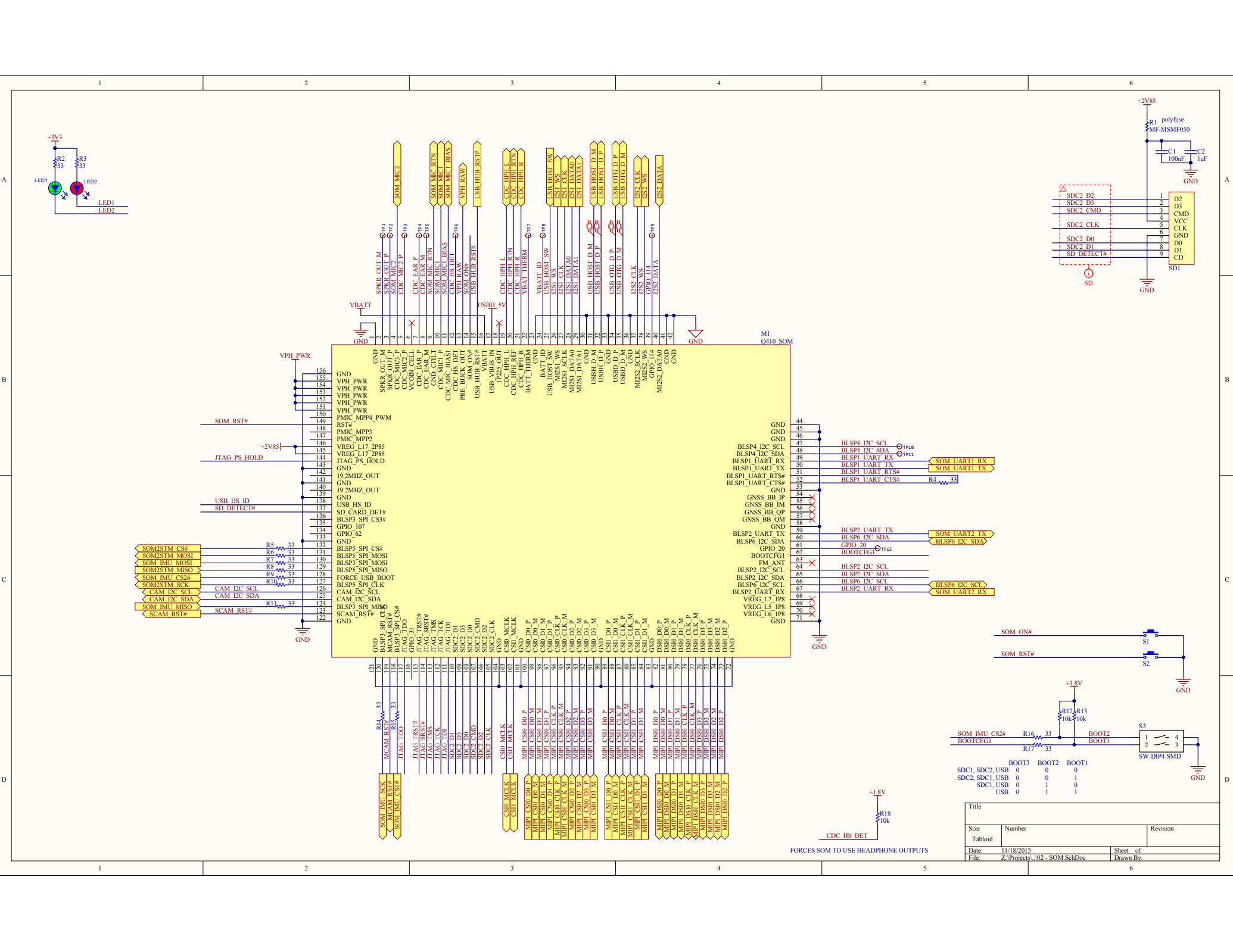
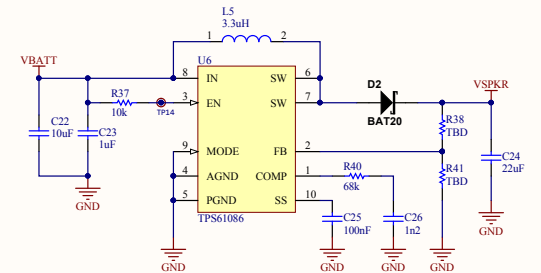
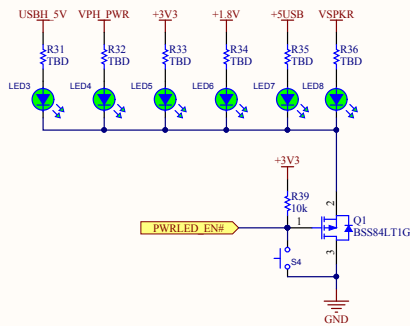
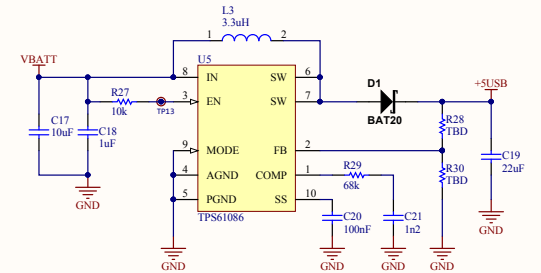
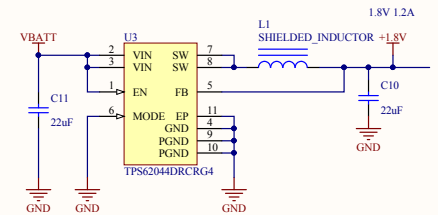
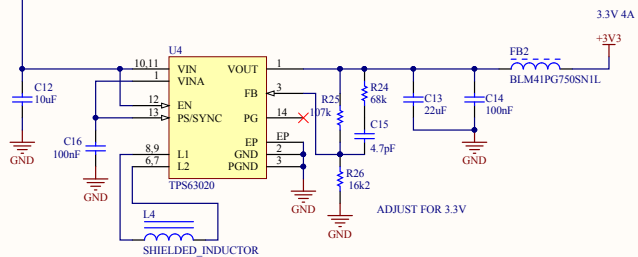
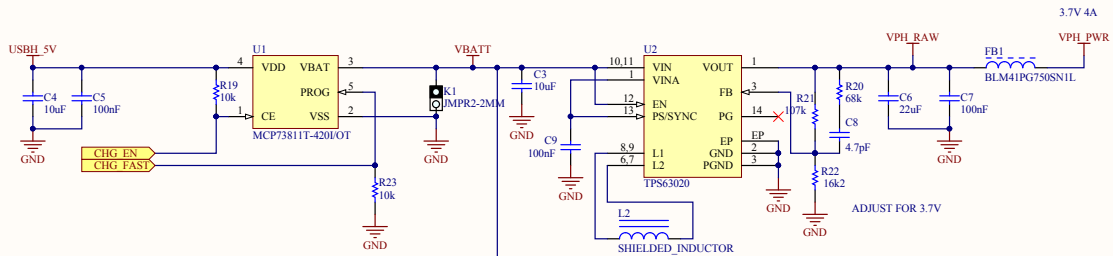


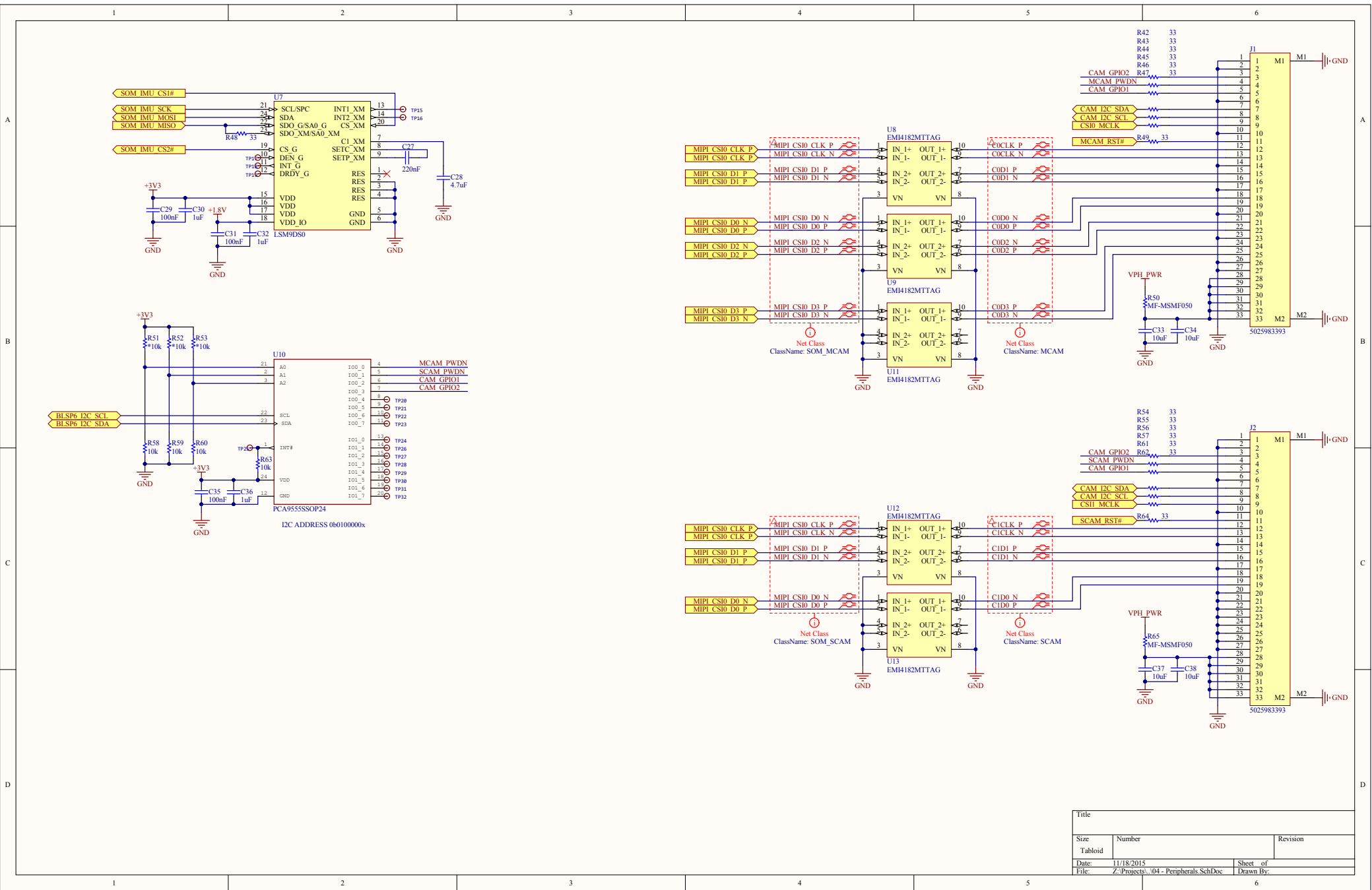
1	2	3	4	5	6
A					A
B					B
C					C
D					D
1	2	3	4	5	6

Title		
Size	Number	Revision
Tabloid		
Date:	11/18/2015	Sheet of
File:	Z:\Projects\...01 - Block Diagram.SchDoc	Drawn By:

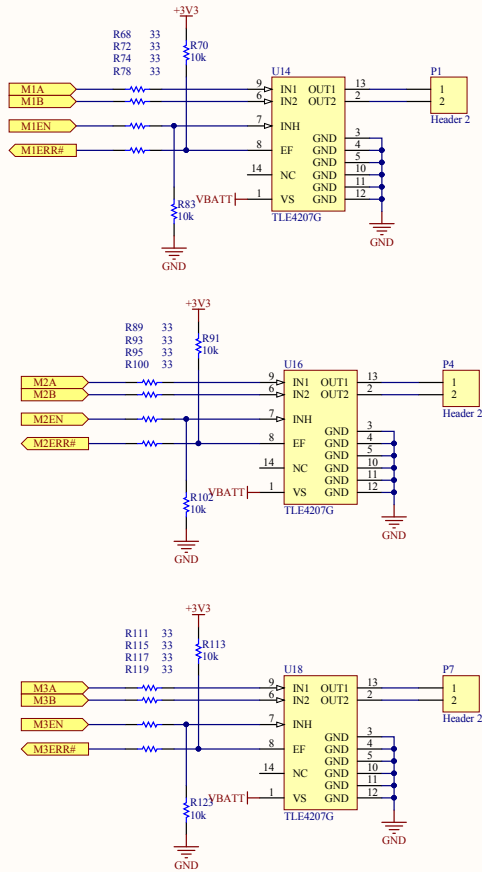




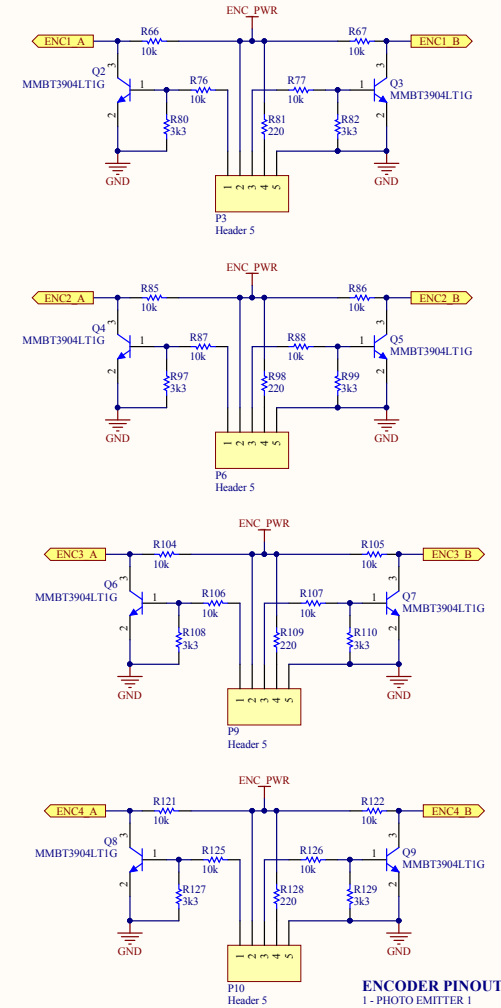
Title		
Size	Number	Revision
Tabloid		
Date:	11/18/2015	Sheet of
File:	Z:\Projects\...03 - Power SchDoc	Drawn By:



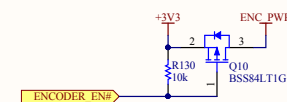
MOTOR DRIVERS



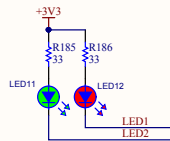
MOTOR POSITION ENCODERS



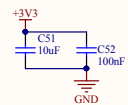
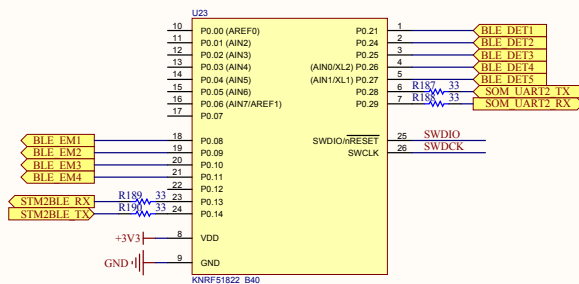
ENCODER PINOUT
 1 - PHOTO EMITTER
 2 - PHOTO COMMON COLLECTOR
 3 - PHOTO EMITTER 2
 4 - EMITTER ANODE
 5 - EMITTER CATHODE



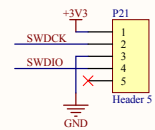
Title		
Size	Number	Revision
Tabloid		
Date:	11/18/2015	Sheet of
File:	Z:\Projects\...05 - Motor Drivers.SchDoc	Drawn By:



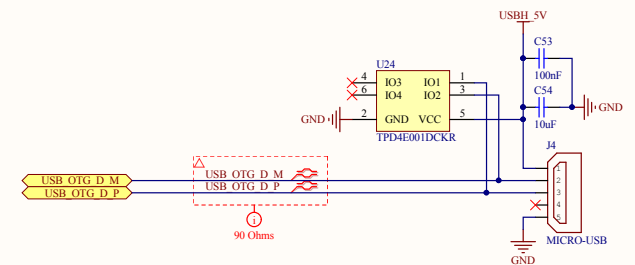
LED1
LED2



BLE SWD



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Size	Number	Revision
Tabloid		
Date: 11/18/2015	Sheet of	
File: Z:\Projects\...07 - BLE.SchDoc	Drawn By:	

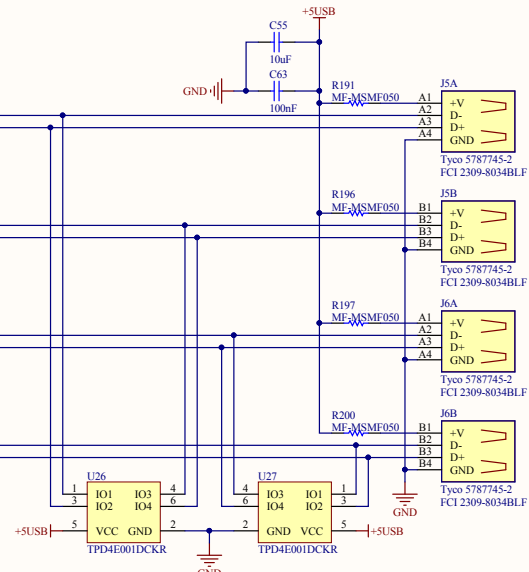


The schematic diagram illustrates the electrical connections for a USB2514AEZG module. Key components and connections include:

- Power Supply:** A +3V3 supply is connected to the module's VDD pins (U25 VDD1-VDD4) and the oscillator (Y2). Decoupling capacitors C56 (10nF), C57 (100nF), C58 (100nF), C59 (100nF), C60 (100nF), C61 (100nF), and C62 (100nF) are used for power filtering.
- Oscillator:** A 24MHz oscillator (Y2) is connected to the CLK pin of the USB2514AEZG IC. It includes a 100nF capacitor (C64) and a 33Ω resistor (R192).
- USB Host Interface:** The module's DM1, DP1, DM2, and DP2 pins are connected to the USB HOST D M and D P lines. The RST# pin is connected to the USB HUB RST# line.
- I2C Interface:** The module's SCL and SDA pins are connected to the BLSP6 I2C SCL and SDA lines.
- Strapping:** The module's STRAPPING SELO and MODE pins are connected to the BLSP6 I2C SCL and SDA lines, respectively.
- Test Points:** Test points TP33, TP34, TP35, TP36, TP38, TP39, TP40, and TP41 are provided for testing.

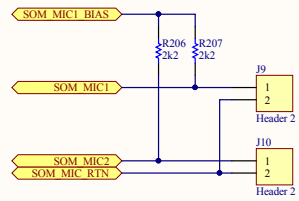
STRAPPING SELO MODE

SELO	MODE	Configuration
0	0	INTERNAL DEFAULTS
0	1	I2C SLAVE 000101000
1	0	INTERNAL DEFAULTS+LEDS
1	1	I2C MASTER

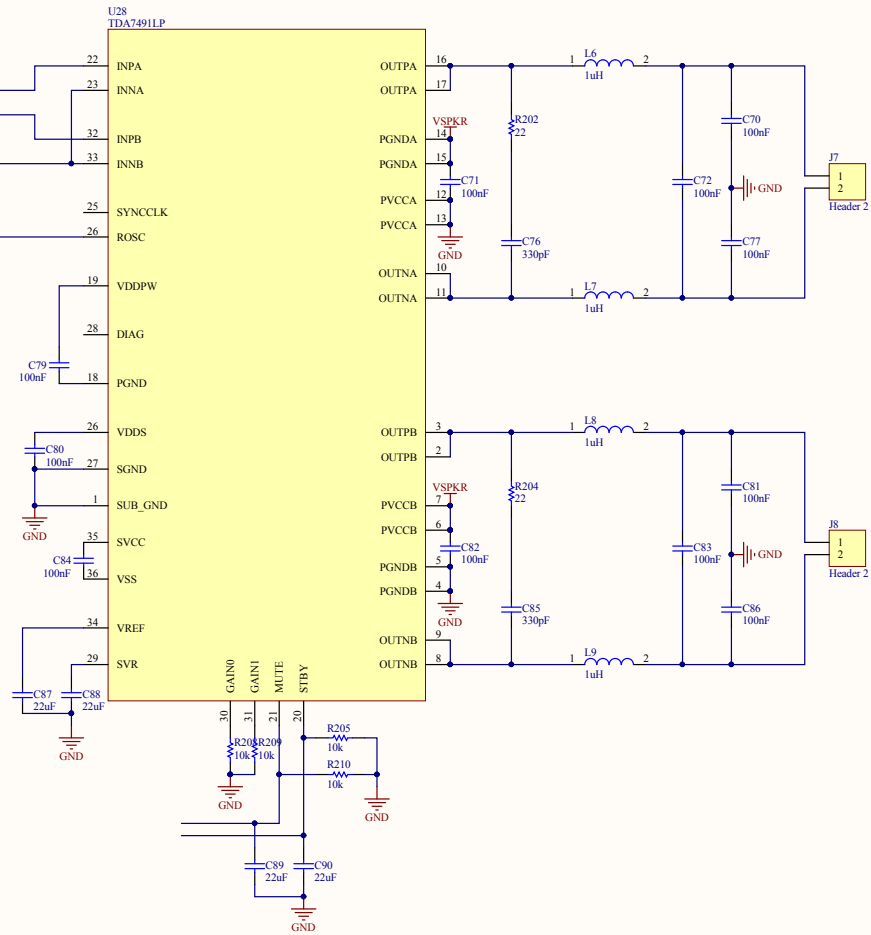


Title		
Size Tabloid	Number	Revision
Date:	11/18/2015	Sheet of
File:	Z:\Projects\008 - USB SchDoc	Drawn By:
		6

SOM ANALOG AUDIO IN



SOM ANALOG AUDIO OUT



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Date:	11/18/2015	Sheet of
File:	Z:\Projects\...09 - Audio Amplifier SchD	Drawn By: