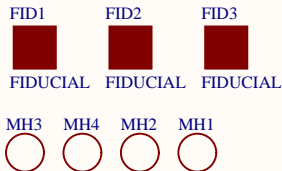
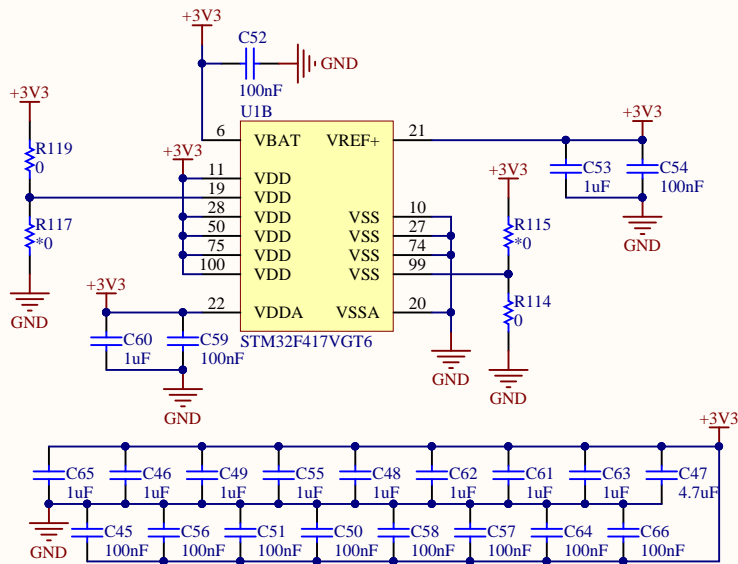
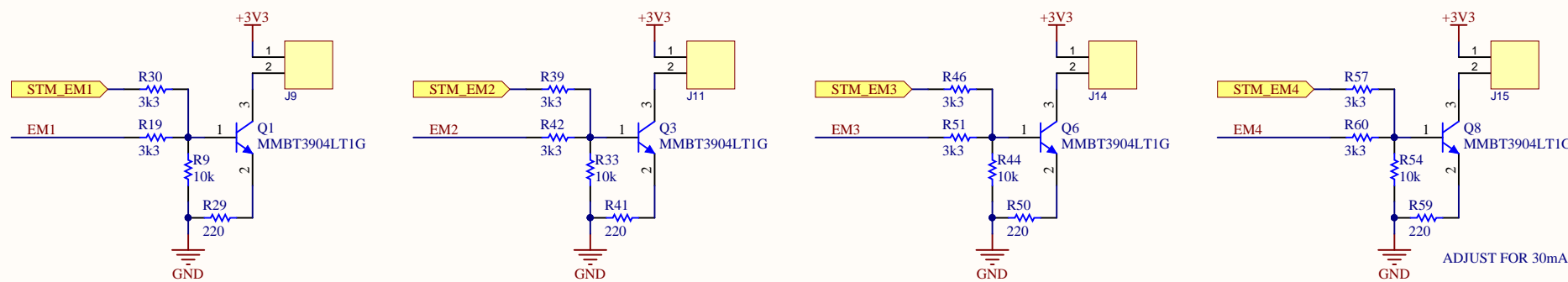
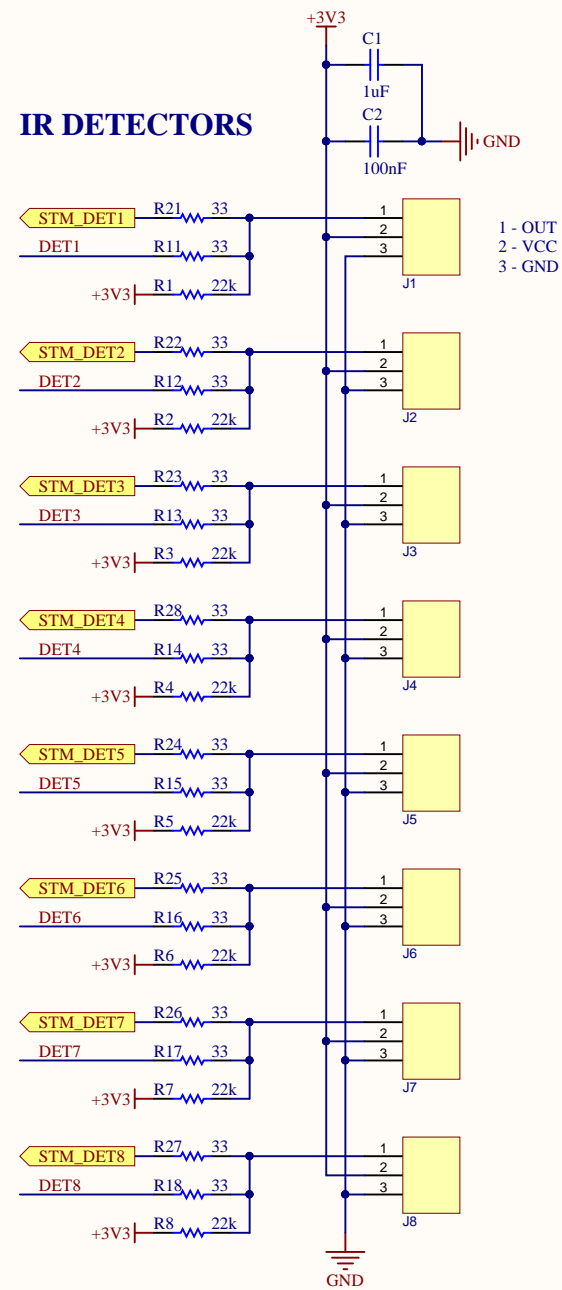
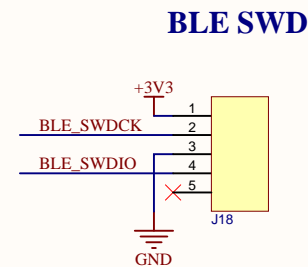
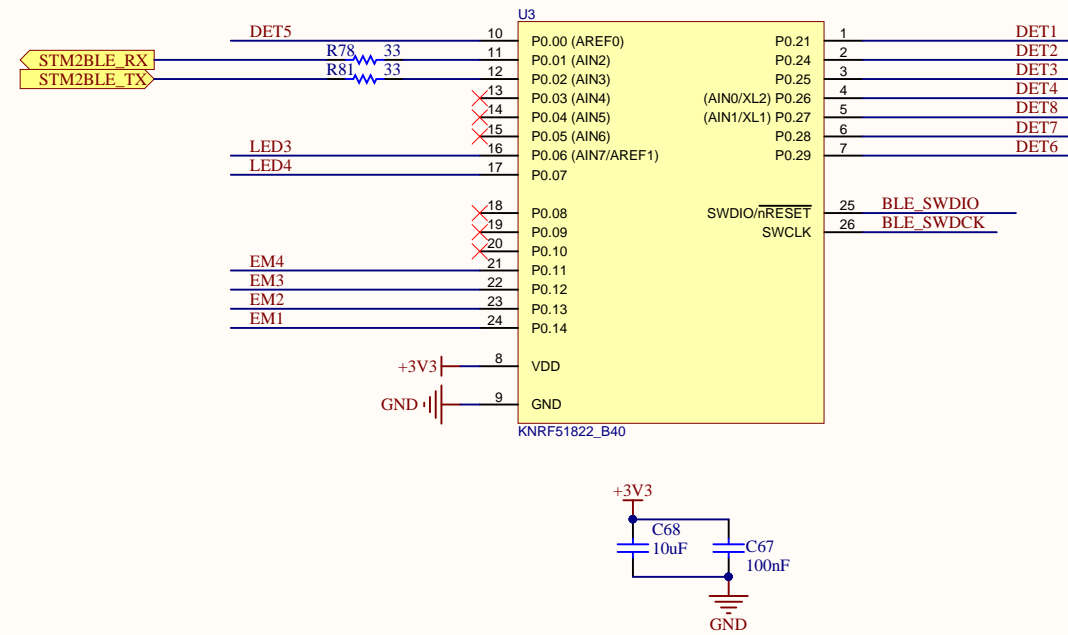
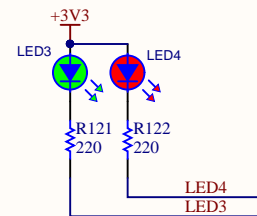


The diagram shows a circuit for selecting boot mode. A +3V3 supply is connected to a network of resistors and a switch. The switch, labeled S1 and SW-DIP4-SMD, has four pins. Pins 1 and 2 are connected to the +3V3 supply through 10k resistors (R86 and R85 respectively). Pins 3 and 4 are connected to GND. The circuit is used to select between BOOT1 and BOOT0 boot modes.

	BOOT1	BOOT0
Flash	x	0
System	0	1
SRAM	1	1



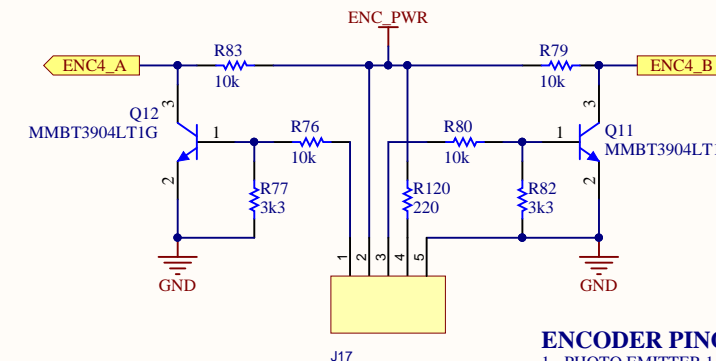
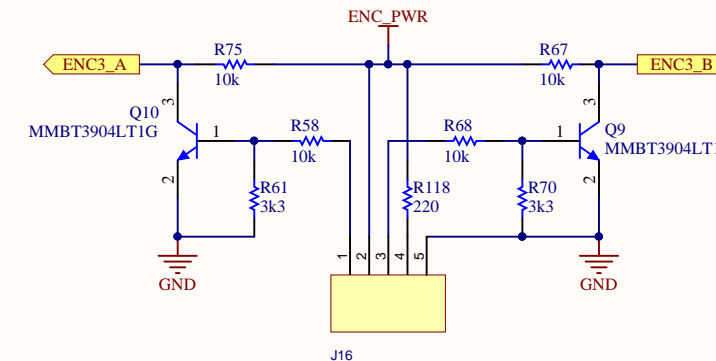
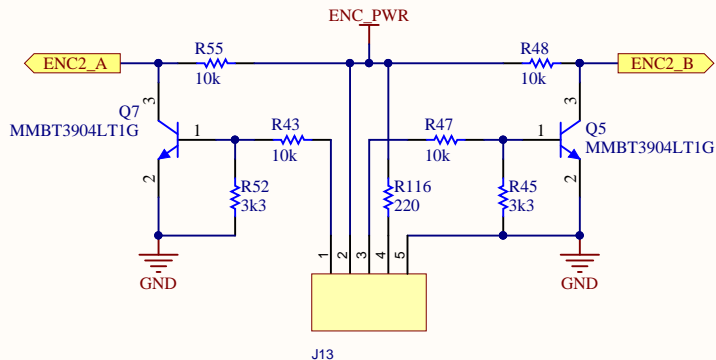
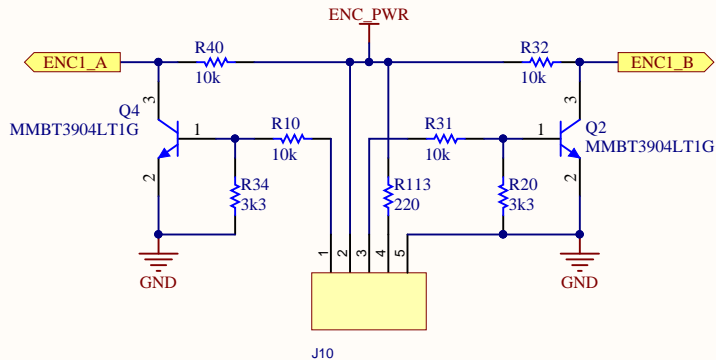
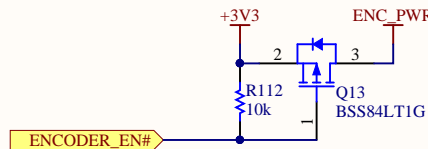
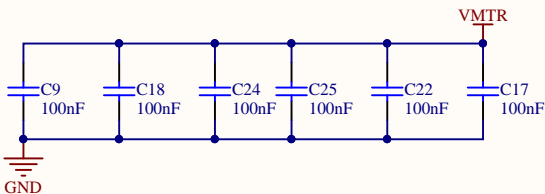
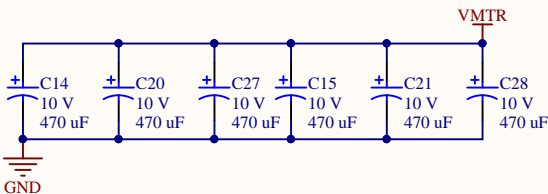
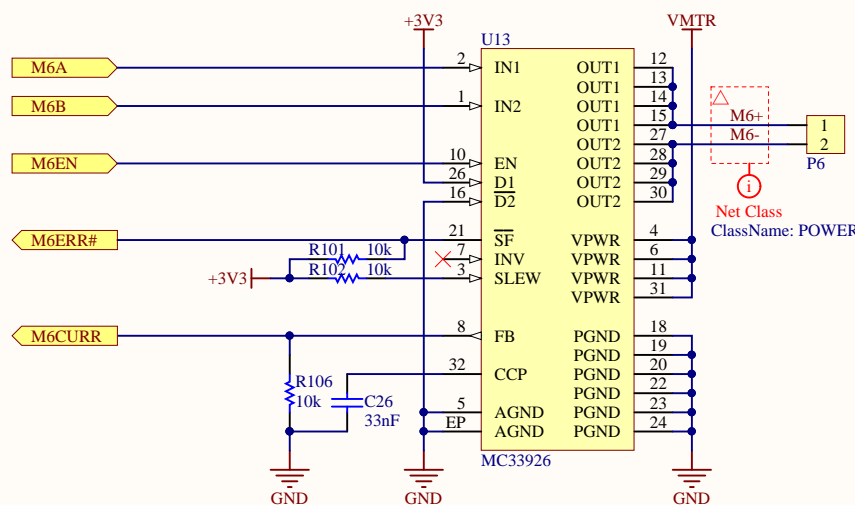
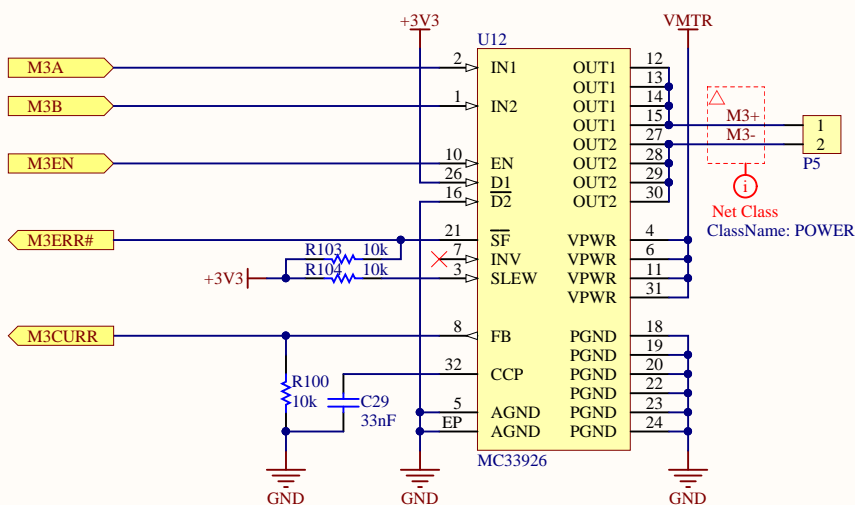
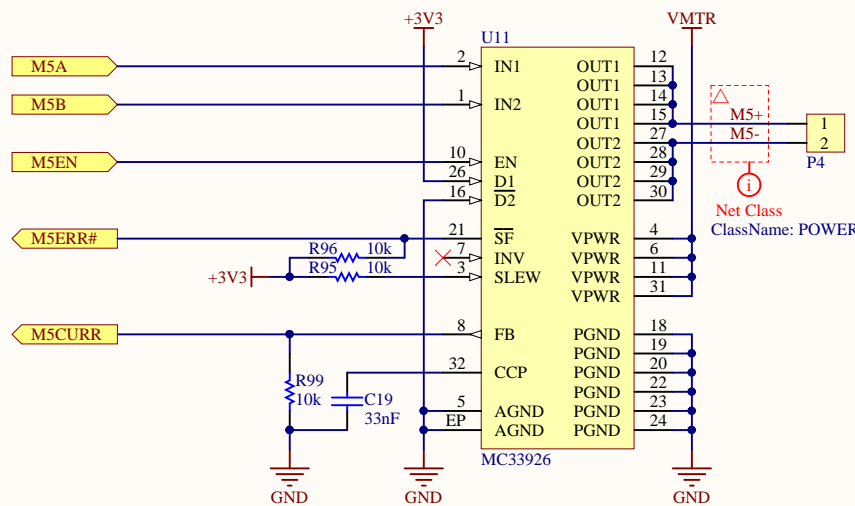
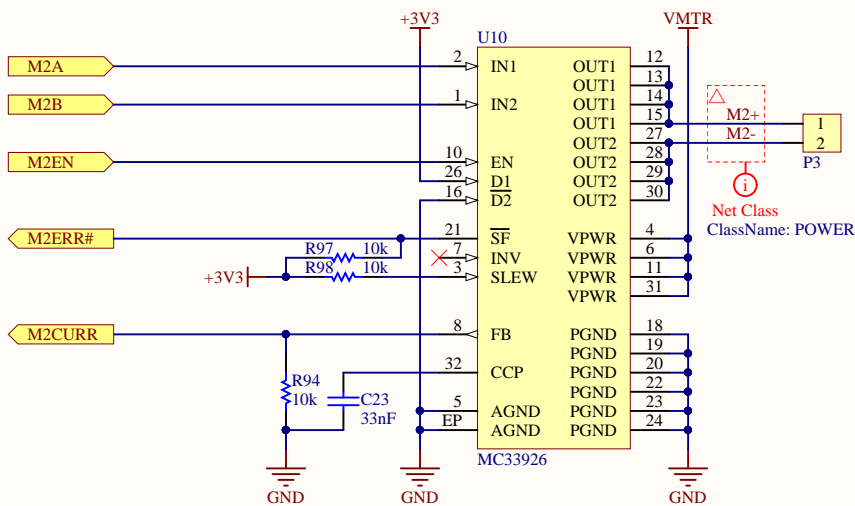
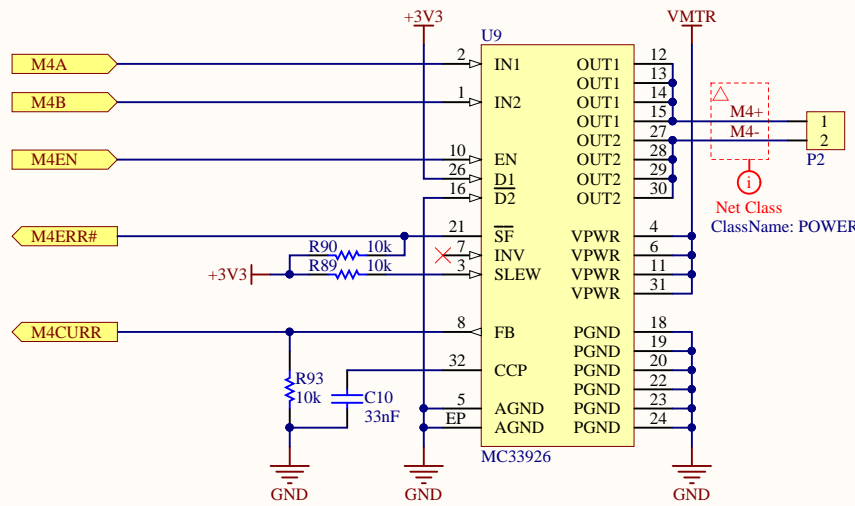
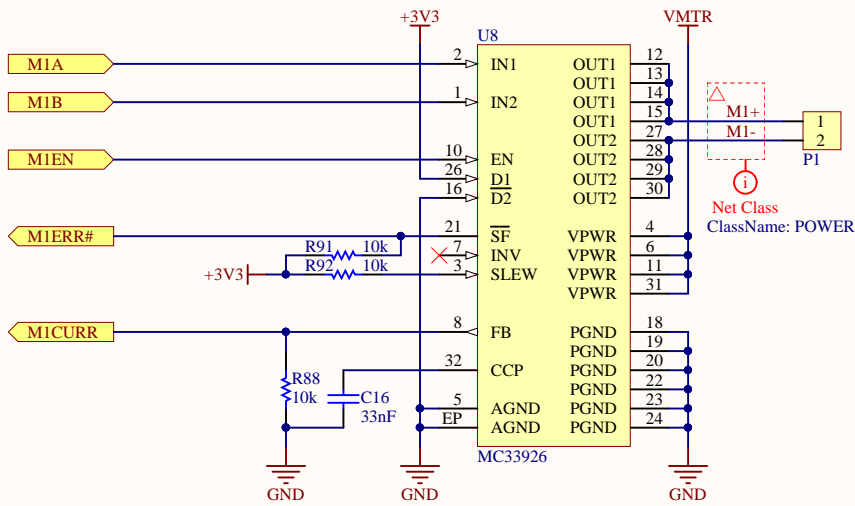
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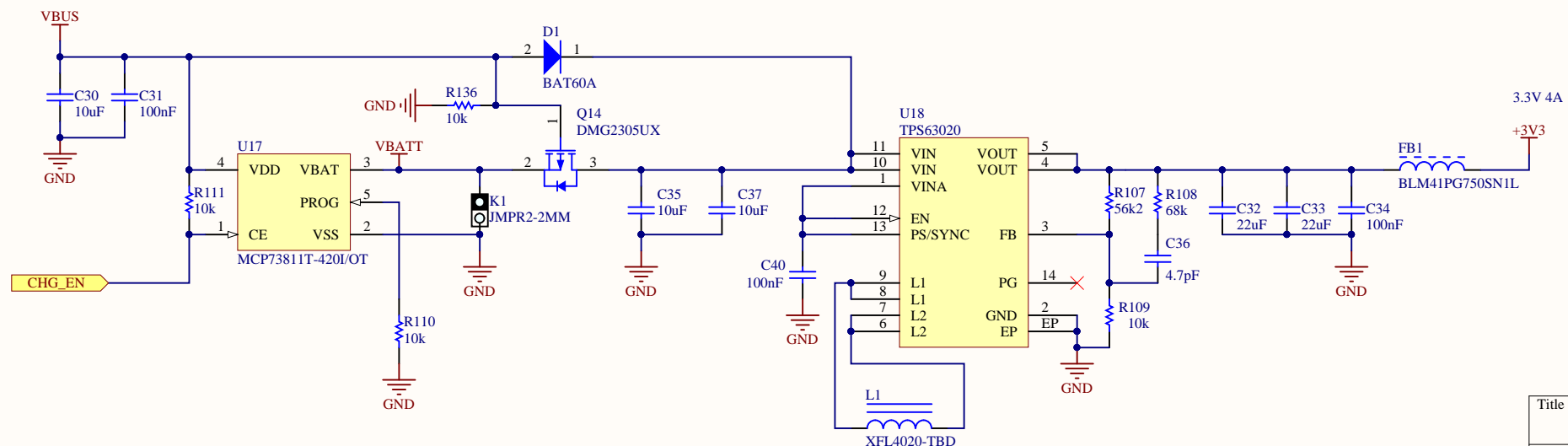
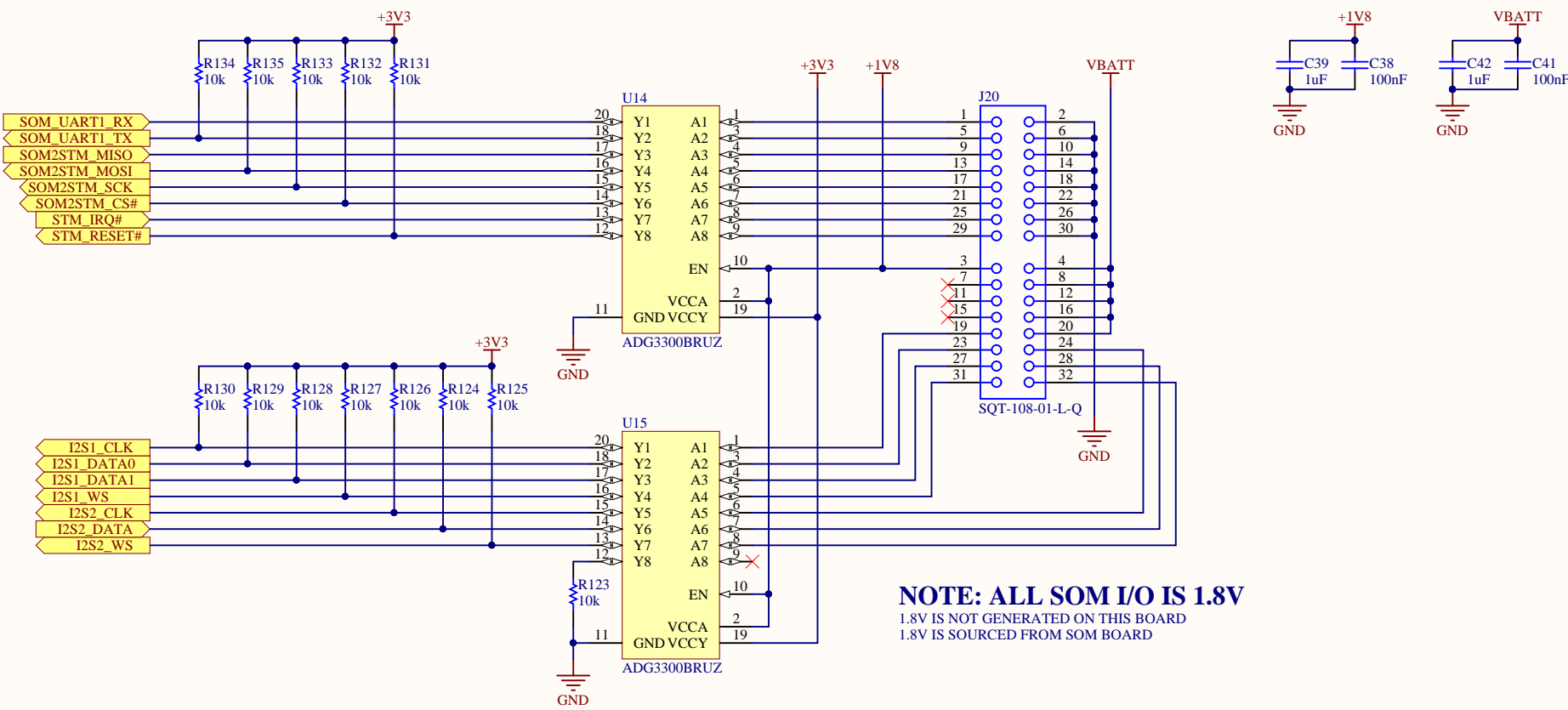
MOTOR DRIVERS

MOTOR POSITION ENCODERS



ENCODER PINOUT
1 - PHOTO EMITTER 1
2 - PHOTO COMMON COLLECTOR
3 - PHOTO EMITTER 2
4 - EMITTER ANODE
5 - EMITTER CATHODE

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