1) A) 00110101  
210 
$$= (1 \times 15) + (1 \times 14) + (1 \times 14) + (1 \times 16)$$
  
34 + 16 + 4 + 1  
10  $= 55362 = 12861$ 

$$= 120 \ + (1 \times 1_{d}) + (1 \times 1_{d})$$
B)  $10010110$ 

$$= 90A$$

$$198 = PA = 8$$

$$(1 \times 93) + (1 \times 94) + (1 \times 93) + (1 \times 94)$$

$$C) 11001100$$

- 3) A) 4095 -> 4096 -> Log, 4096 = 14 bits
  - B) 65534 -> 65535 -> 6526535 = 16 bits
  - c) 44319 > Log 443319 = 15.369 bits -> 16 bits
- 4) A) 0011 0101 1101 1010
  - B) 1100 1110 1010 0011
    - c) 1111 1110 1101 1011
- 5)A) 0 1 2 6 F 9 D 4
  - B) 6 A C D F A 9 5
  - C) F 6 B D C 2 A

198 603311981131

7) Letter G:
Decimal: 71
Hex: 47
Binary: 0100 0111

8) Symbol, t Decimal: 33843 Hex: 5977 Binary: 0101 1001 0010 0111

a) multiplexors use a ninputs to connect to a single cutput. The selection of input is decided by N selector bits. Therefor a 4 input multiplexor has Log, 4 = 2 selector bits