

Results

Results for “DebraizeFixed_Wrapper” entity:

A) Resource Utilizations

Component	A2B	B2A	A2BQ	B2AQ
LUTS	72	63	47	41
Slices	23	19	12	12
Flip-Flops	0	0	0	0
LUT-FF pairs	0	0	0	0
BRAMs	0	0	0	0
DSP units	0	0	0	0

- For A2B and B2A, results are generated for n=4 and k=8. However, for A2BQ and B2AQ, results are generated for n=2 and k=8
- Implementation results have been reported.

B) Timing Analysis

Parameter	A2B	B2A	A2BQ	B2AQ
Minimum Clock Period (ns)	4.24	3.8	4.58	4.1
Maximum Clock Frequency (MHz)	235	263	218	243
Maximum Throughput	Varys based on the n, k, and input baud rates			