

HEVC (High Efficiency Video Coding)

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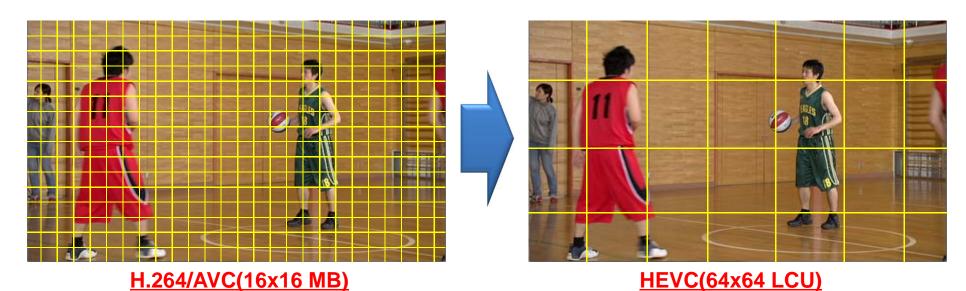


Tree Coding Units (8x8 ~ 64x64)



1. Tree Coding Units ($8x8 \sim 64x64$)

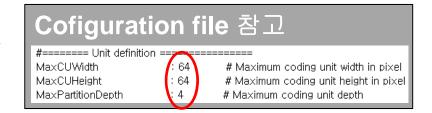
- 영상을 부호화하는 기본 단위의 크기를 비교
 - BasketballPass_416x240_50.yuv

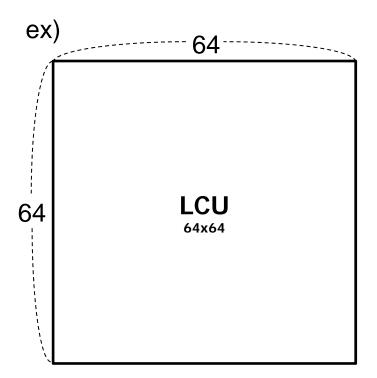


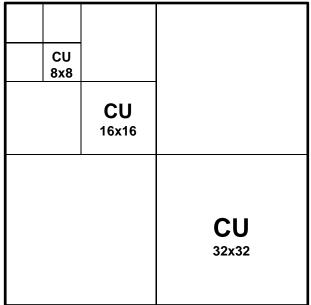
■ HEVC는 LCU를 시작으로 Quad-Tree 구조로 ~8x8까지 분할된 CU를 사용

1. Tree Coding Units (8x8 ~ 64x64)

- Tree Coding Units??
 - Quad-Tree 구조를 사용함
 - All conditions (AI, LC, HE) 에서 동일함
 - ✓ 64x64~8x8까지 총 4개의 크기의 CU









Prediction Units



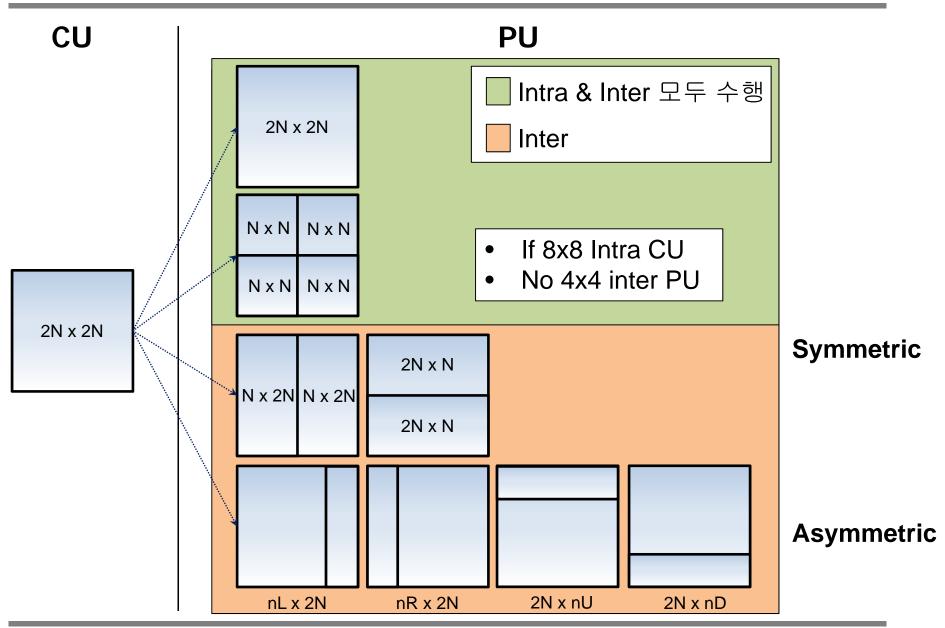
2. Prediction Units

Prediction Units?

- 예측을 수행하는 단위
- 각 CU에 대한 Inter prediction과 Intra prediction을 각각 수행

<u>Inter</u> Prediction Units	<u>Intra</u> Prediction Units
 Square, Rectangle Symmetric Asymmetric(Not Main Profile) 	> Square

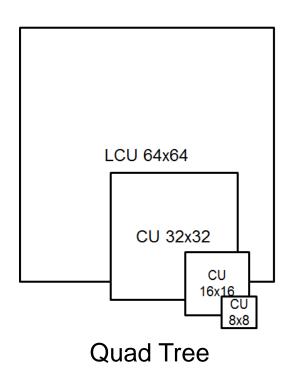


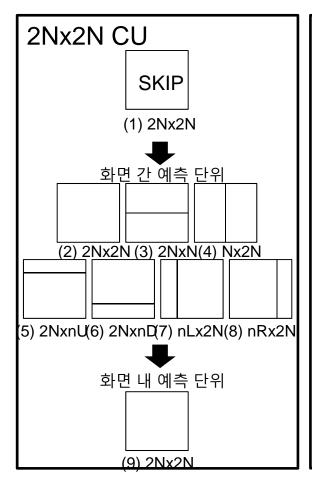


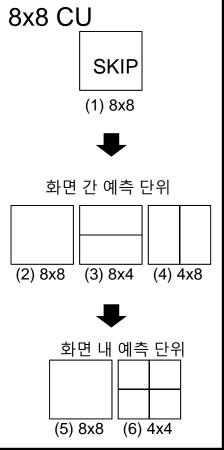


2. Prediction Units

PU encoding order









J0579 Bog on limits

 J0225 and J0335 16bit range constraint (clipping) for both horizontal and vertical MVs.

Item	Syntax Element	Туре	Min Value	Max Value	Proposed Min	Proposed Max	Notes	Decision:
16	abs_mvd _minus2	EGk(v)	0	??	0	Indirectly bound by re quiring -both mvd_x and mvd_y be in the	bound motio n vector (m	



J0086 disallow bi-predictive mode for 8x4 and 4x8 inter PUs.

 The CABAC binarization table of inter_pred_idcof 8x4 and 4x8 inter PUs

Slice_type	inter_pred_idc	Name of inter_pred_idc	bin string
Р	inferred	Pred_L0	-
В	0	Pred_L0	0
	1	Pred_L1	1
	2	Pred_BI	-

The CABAC binarization table for inter_pred_idc of inter PUs of 8x8 and larger (same as HM7.0)

Slice_type	inter_pred_idc	Name of inter_pred_idc	bin string
Р	inferred	Pred_L0	-
В	0	Pred_L0	00
	1	Pred_L1	01
	2	Pred_BI	1





A _{-1,-1}		A _{0,-1}	a _{0,-1}	b _{0,-1}	C _{0,-1}	A _{1,-1}		A _{2,-1}
A _{-1,0}		A _{0,0}	a _{0,0}	b _{0,0}	C _{0,0}	A _{1,0}		A _{2,0}
d _{-1,0}		d _{0,0}	e _{0,0}	f _{0,0}	g _{0,0}	d _{1,0}		d _{2,0}
h _{-1,0}		h _{0,0}	i _{0,0}	j _{0,0}	k _{0,0}	h _{1,0}		h _{2,0}
n _{-1,0}		n _{0,0}	p _{0,0}	q _{0,0}	r _{0,0}	n _{1,0}		n _{2,0}
A _{-1,1}		A _{0,1}	a _{0,1}	b _{0,1}	C _{0,1}	A _{1,1}		A _{2,1}
A _{-1,2}		A _{0,2}	a _{0,2}	b _{0,2}	C _{0,2}	A _{1,2}		A _{2,2}

Integer samples

- shaded blocks with upper-case letters
- Fractional sample positions
 - Un-shaded blocks with lower-case letters
 - For quarter sample luma interpolation







p(-1)

p(1)

p(2)

p(3)

p(4)



p(0)

p(2)

p(3)

p(5)

p(6)

p(7)

Forward DCT

$$F(u) = c(u) \sum_{l=0}^{N-1} p(l) \cos(\frac{(2l+1)u\pi}{2N})$$

Inverse DCT

$$p(x) = \sum_{u=0}^{N-1} c(u)F(u)\cos(\frac{(2x+1)u\pi}{2N})$$

 α : Fractional point

$$p(\alpha') = \sum_{u=0}^{N-1} c(u) F(u) \cos(\frac{(2\alpha'+1)u\pi}{2N})$$

 $N = Tap \ length$

$$\alpha' = \alpha + (\frac{N}{2} - 1)$$

$$c(0) = \frac{1}{\sqrt{N}}, c(k) = \sqrt{\frac{2}{N}}, k = 1, ...N - 1$$

- Interpolation filter coefficients
 - Luma

α	filter(α)
1/4	{-1, 4, -10, 58, 17, -5, 1, 0}
1/2	{ -1, 4, -11, 40, 40, -11, 4, -1 }

• Chroma

α	filter (α)
1/8	{ -2, 58, 10, -2,}
1/4	{ -4, 54, 16, -2,}
3/8	{ -6, 46, 28, -4,}
1/2	{ -4, 36, 36, -4,}

- Luma interpolation process
 - 1D interpolation filter
 - \checkmark For fractional positions " $a_{(0,0)}$ ", " $b_{(0,0)}$ " and " $c_{(0,0)}$ ", horizontal 1D filter is used.
 - ✓ For fractional positions " $d_{(0,0)}$ ", " $h_{(0,0)}$ " and " $n_{(0,0)}$ ", vertical 1D filter is used.
 - ✓ The input of 1D interpolation function is integer position values.
 - \checkmark The output is interpolated value X, which has fractional position α .
 - \checkmark Ex. 1/2 position "b_(0,0)"
 - 8-tap separable DCTIF coefficient of 1/2 position

$$b_{(0,0)} = \{-1 \times A_{(-3,0)} + 4 \times A_{(-2,0)} - 11 \times A_{(-1,0)} + 40 \times A_{(0,0)} + 40 \times A_{(1,0)} - 11 \times A_{(2,0)} + 4 \times A_{(3,0)} - 1 \times A_{(4,0)} + 32\} / 64$$

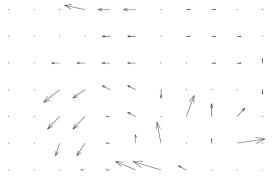


- 2D separable interpolation filter
 - ✓ For remaining positions first horizontal 1D filter is applied for extended. block, and then vertical 1D filter is used.
 - ✓ Ex. 1/4 position " $e_{(0,0)}$ "
 - 2D separable Interpolation
 - 8*horizontal 1D filter + 1*vertical 1D filter
- Chroma interpolation process is the same as Luma.





- The reason for motion vector prediction
 - High relevance with MV of neighboring partition.
 - Sending MVD is more efficient than sending MV.



- Motion Vector Prediction
 - The process of searching Motion Vector Predictor (MVP or pMV)
 - **Motion Vector Predictor (MVP)**
 - ✓ Predicted vector
 - √ HM4.0 : 3 MVP candidates
 - Motion Vector Difference (MVD)
 - ✓ Difference between MVP and MV
 - \checkmark MVD = MV MVP



- Decoder receives
 - ref_idx_l0, ref_idx_l1; reference index
 - mvd info
 - mvp_l0_flag, mvp_l1_flag





- 1. Search for Spatial Candidates
- 2. Remove redundant MVPs
- 3. Temporal Candidate search if # of spatial candidates < 2
- 4. Additional Candidate list
 - Zero vector candidates are created by combining zero vector and refIdx

amvp_flag	L0
0	mvL0_A
1	-

amvp_flag	L1
0	mvL1_A
1	-

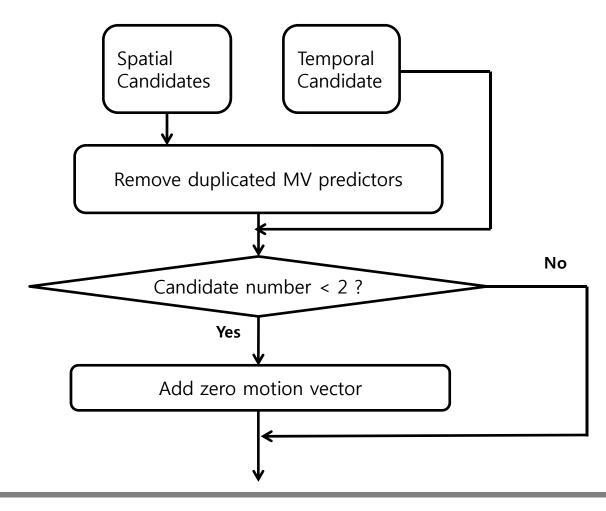
Add zero vector



amvp_flag	L0
0	mvL0_A
1	(0, 0)

amvp_flag	L1
0	mvL1_A
1	(0, 0)

Simplification AMVP List Construction





- 4. Decision of MVP before Motion Estimation (ME start decision from the previous MVP candidates)
 - Distortion : SAD
 - Rate: mvp_flag cost (1 bit)
 - RDCost = Distortion+(Bits*λ + 0.5)>>16; 2개후보에 대하여 2번계산 $\checkmark \lambda = 492942$
- Best MV 결정: ME algorithm MV decision
 - SAD+(Bits*λ)
- 6. Decision of the best MVP candidate after Motion Estimation
 - MVP index 결정: Smallest MVD 결정 mvd=Best MV-MVP of mvp_index[i]

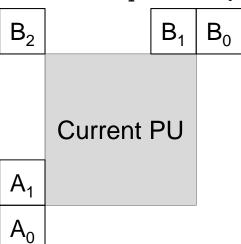


- 7. Finally the best MVP with RDO value after competing against the merge modes with RDO values
 - RDO: SATD+ λ *R



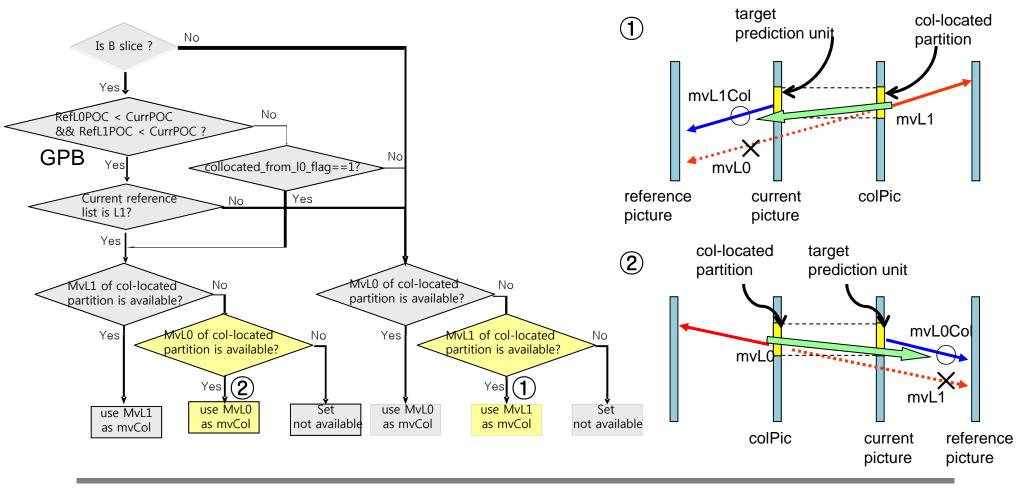
- Spatial MVP Candidates
 - mvLXA: Left spatial candidate
 - ✓ Derivation : $(A_0 (\rightarrow A_1))$ → (scaling) $(A_0 \rightarrow A_1)$
 - mvLXB: Above spatial candidate
 - ✓ Derivation : $(B_0 \rightarrow B_1 \rightarrow B_2)$ → (isScaledFlagLX == 0) $(B_0 \rightarrow B_1 \rightarrow B_2)$
 - isScaledFlagLX = $(A_0 == available && A_0 != Intra) ||$

$$(A_1 == available && A_1 != Intra)$$



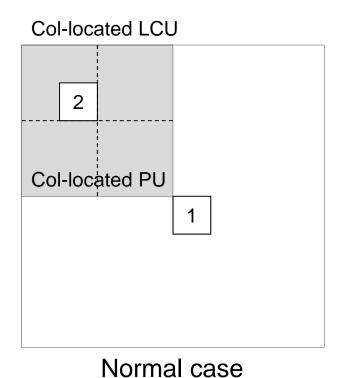


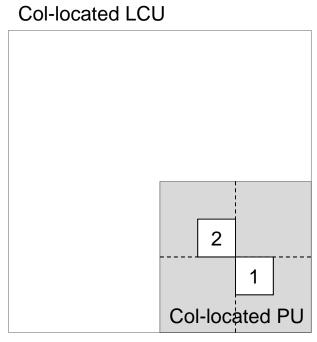
Temporal MVP Candidate





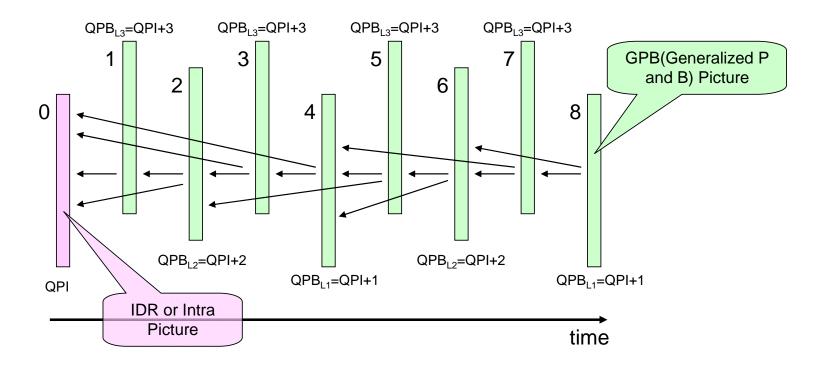
Position of temporal MVP Candidate





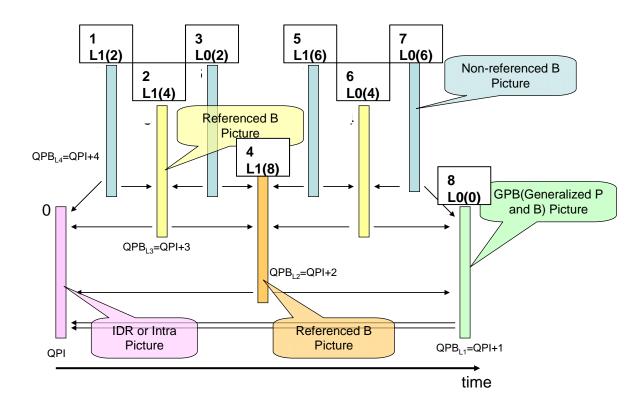
Right-bottom PU in LCU

- Low-delay 구조의 Col-located block
 - List0[0] Reference





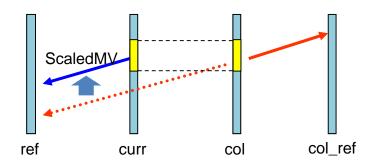
■ Random access 구조의 Col-located block



Computation by uni-directional prediction



MV Scaling



ScaleFactor

$$ScaleFactor = \frac{POC_{curr} - POC_{ref}}{POC_{col} - POC_{col_ref}} = \frac{TDB}{TDD}$$

Division-free Scaling

 $ScaleFactor = clip(-4096, 4095, (TDB \times tX + 32)) >> 6)$

$$tX = \frac{2^{14} + \left| \frac{TDD}{2} \right|}{TDD}$$

ScaledMV

$$ScaledMV = sign(ScaleFactor \times MV) \times \\ ((abs(ScaleFactor \times MV) + 127) >> 8)$$

• ex)

$$\checkmark$$
 ScaleFactor \times MV = 1010000000₍₂₎

$$\checkmark$$
 ScaledMV = $10_{(2)}$



- Decoder always receives
 - Merge_flag
 - Merge_index
- Merge Skip (2Nx2N)
 - 2Nx2N Merge mode로 5번 RDO를 수행 (when merge candidates=5) ✔ cbf가 0일 경우 Merge Skip mode로 RDO 수행

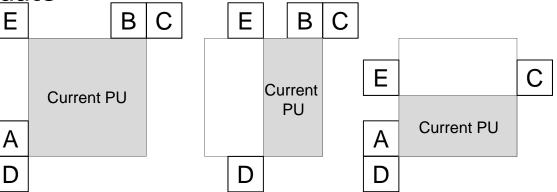


- 1. Search for spatial candidates S_0 , S_1 , S_2 , S_3
- 2. Remove redundant candidates
- 3. Search for temporal candidate *Col*
- Add Candidate list
- 4. MRGCost = MRGError + λ*MRGBits ; 5번 실행 후 the best 선택
 - MRGError : Hadamard (SATD)
 - MRGBits: Truncated Unary Code, MaxNum is signaled at Slice header

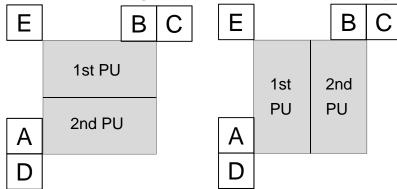
MrgIdx	MaxNum = 5	MaxNum = 4	MaxNum = 3	MaxNum = 2	MaxNum = 1
0	0	0	0	0	N/A
1	10	10	10	10	
2	110	110	110		
3	1110	1110			
4	11110				

Spatial Merge Candidate

• 16x16 - 64x64



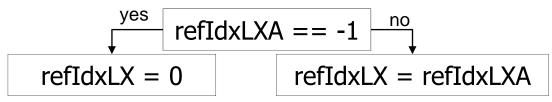
8x8 (log2_parallel_merge_level_minus2 > 0)



- Derivation : A \rightarrow B \rightarrow C \rightarrow D \rightarrow E (one of A,B,C,D is not available)
 - ✓ available ,non-Intra candidate

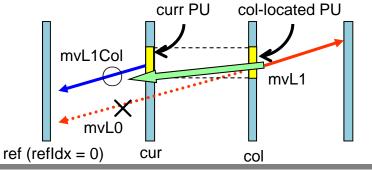


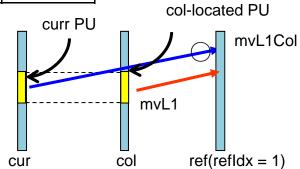
- To get the reference index of Temporal Merge Candidate
 - refIdxLX



- \checkmark 2nd PU (only in Nx2N) : refIdxLX = 0
- Derivation of temporal merge candidate
 - Same process with TMVP
 - √ ex) reference index :









Α

1st

PU

2nd

PU

- Additional Candidate list
 - 1. Combined bi-directional Merge candidate

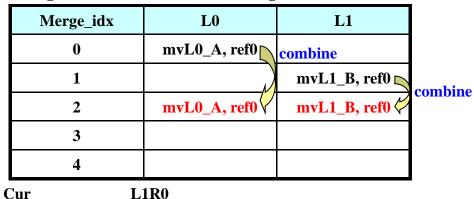
L₀R₀

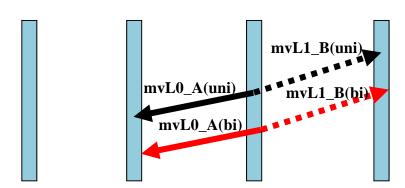
Two candidates in original candidates, which have mvL0 and refIdxL0 or

Original Merge candidate list

Merge candidate list after adding combined candidates

Merge_idx	L0	L1
0	mvL0_A, ref0	-
1	-	mvL1_B, ref0
2		
3		
4		







- Additional Candidate list
 - 2. Zero vector Merge/AMVP candidate
 - Zero vector Merge/AMVP candidates are created by combining zero vector and refIdx

Original Merge candidate list

Merge_idx	L0	L1
0	mvL0_A, ref0	-
1	-	mvL1_B, ref0
2	mvL0_A, ref0	mvL1_B, ref0
3	-	-
4	-	-

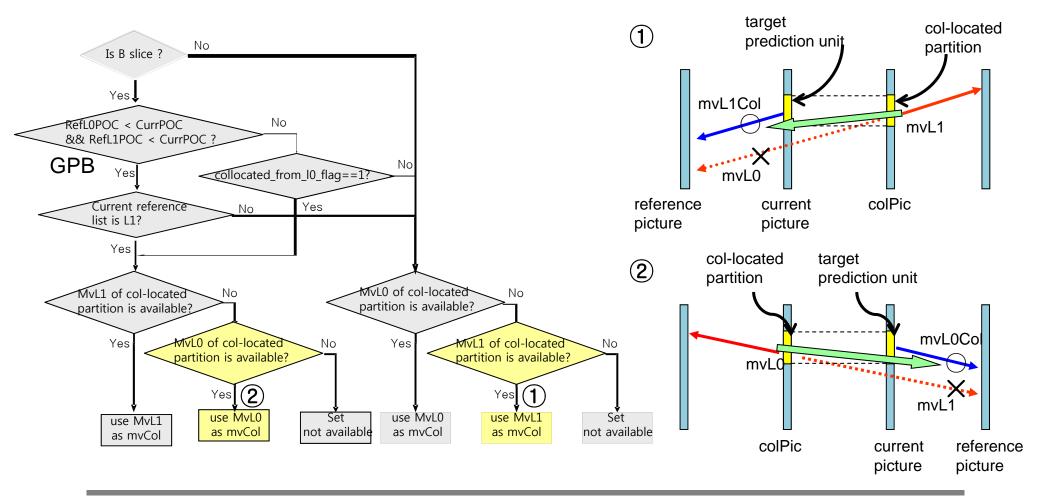
Merge candidate list after adding new ones

Merge_idx	L0	L1
0	mvL0_A, ref0	
1		mvL1_B, ref0
2	mvL0_A, ref0	mvL1_B, ref0
3	(0,0), ref0	(0, 0), ref0
4	(0,0), ref1	(0, 0), ref1



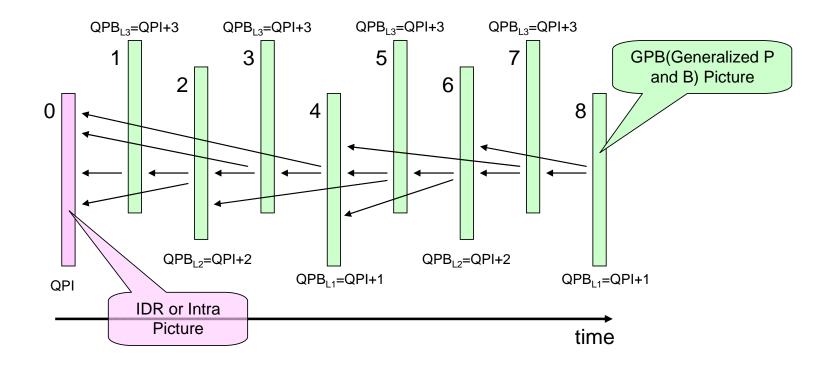


Temporal MVP Candidate



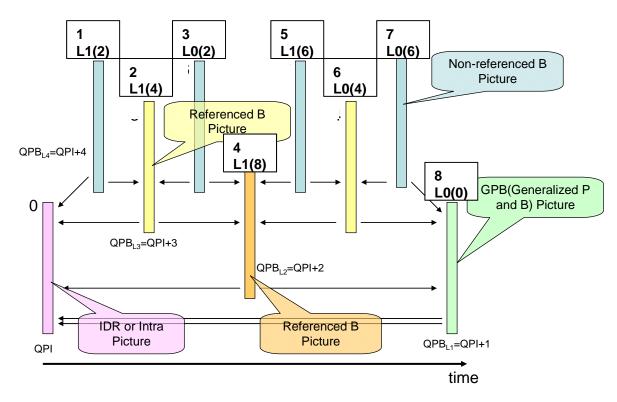


- **Col-located block**
 - Low-delay 구조
 - ✓ List0[0] Reference





- **Col-located block**
 - Random access 구조



Computation by uni-directional prediction



Main Profile

Main Profile	Profile not-defined
Coding Unit 8x8 up to 64x64 in tree structure	-
Prediction Unit 2Nx2N, 2NxN, Nx2N, NxN, Asymmetric Motion Partition(2NxnU, 2NxnD, nLx2N, nRx2N)	Inter4x4 PU
Transform unit tree(3 level max)	Non-square quadtree (4x16, 8x32)
Transform block size of 4x4 to 32x32 samples 4x4 intra mode dependent DCT/DST	-
Angular Intra Prediction (34 modes)	LM Chroma mode
Luma: DCT-based interpolation (Half pel : 8-tap, Quarter pel : 7-tap) Chroma: DCT-based interpolation filter (4-tap)	-
Advanced motion vector prediction PU based Motion vector merge/CU based skip	-
Simplified Deblocking Filter	-
Sample Adaptive Offset	Adaptive Loop Filter
CABAC	-
High-level parallelism : Tile, Wavefront	-

