

Basic Pollard Rho Algorithm Implementation On CUDA Device

Martin Beránek

Faculty of Information Technology – Czech Technical University in Prague

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1 Introduction

Factorisation problem of a huge number resolved into massive parallel solutions. Large number of algorithms are currently state-of-art and are continuously developed into better forms. This short article is focused on implementation of Pollard-Rho algorithm on **CUDA** device. In first part there is a definition of algorithm. Next the article focuses on options of parallelism on **CUDA** device. Results are measured in multiple instances and compared in graphs.

2 Definition of the algorithm

The ρ algorithm (named after the shape of curves symbolising two functions trying to reach themselves in projective space) is based on finding cycle. In t random numbers of x_1, x_2, \dots, x_t in range $[1, n]$ will contain repetition with probability of $P > 0.5$ if $t > 1.777n^{\frac{1}{2}}$

3 CUDA Solution

4 Conclusion