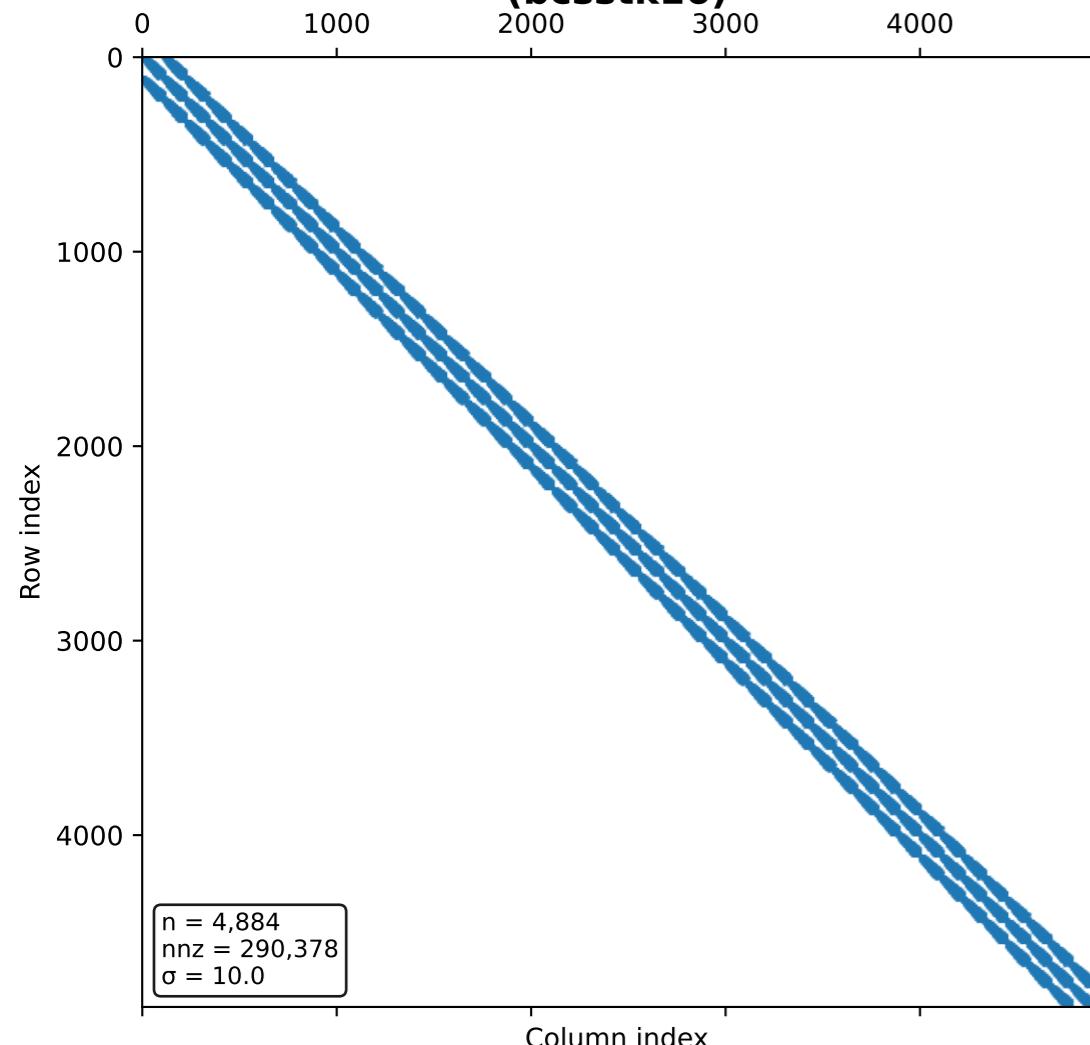
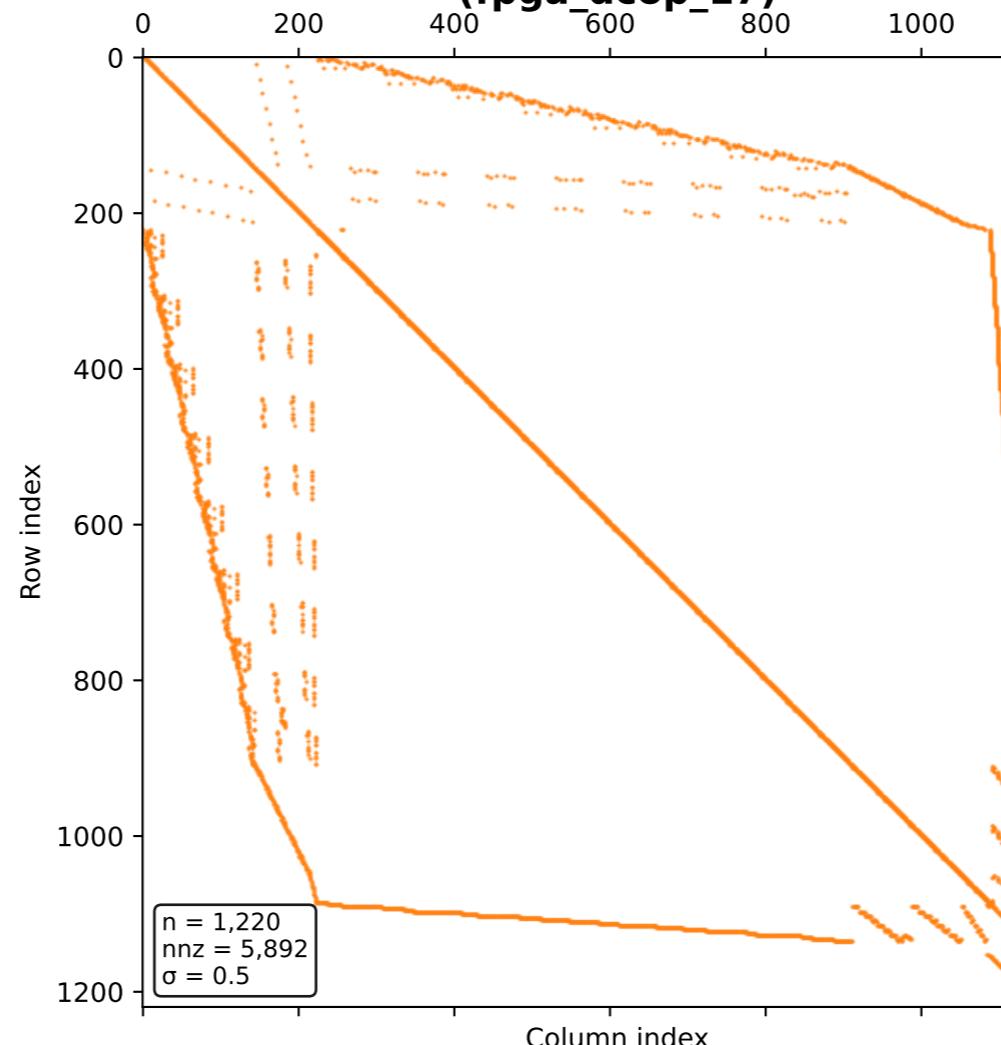


**(a) Symmetric Positive Definite
(bcsstk16)**



**(b) Nonsymmetric
(fpga_dcop_17)**



**(c) Large and Sparse
(FEM_3D_thermal2)**

