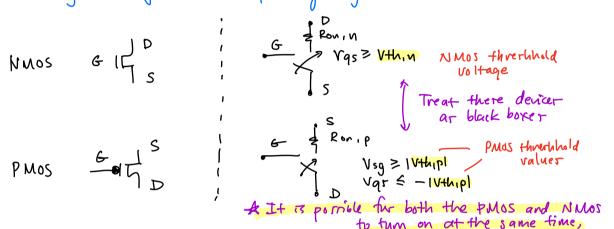
To do: Transistors & RC Models

Email: nareauphol·lin @

- 1) PMOS, NMOS, & Transistors
- Introduction to RC circuits and Differential equations

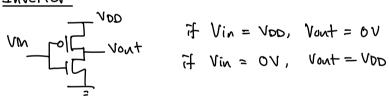
Transister - PMOS/NMOS

Motivating Q: Why can't our computer go any farter?

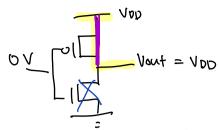


A It is porrible for both the PMOS and NMOS to turn on at the same time, to turn on be off at the same time

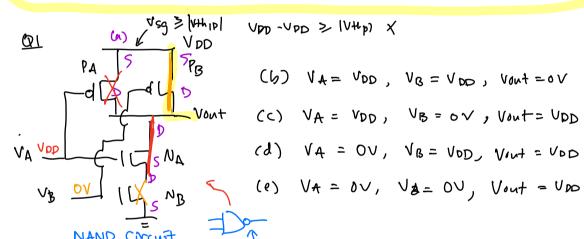




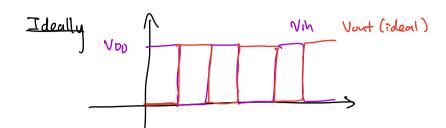
- lets check the conditions:

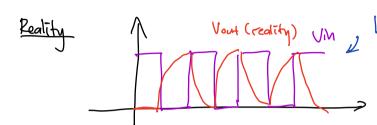


Trick: To solve for NMOS /PMOS circuits quickly, just remember the inverter. It only works if we know ow inputs are high/low (VOO/O) input low -> output high [PMOS on, NMOS off] input high - output low [PMOS off, NMOS on]



THE CITCUIT			
(1)	<u> </u>	VB	1 Vont
	o	6	VDD
	O	JOD	NOD NDD
	UPD	b	Vov
	U00	$_{\alpha a} \lor$	10





how fart it riser/falls depends on the RC time constant (Z)

To answer our mobiliating question: Be cause we are limited by capacitors!

<u>Q2</u>

$$(a) I_{c(t)} = C \frac{dV_{c(t)}}{dt}$$

$$I(t) = C \frac{dV_{c(t)}}{dt}$$

$$- + I(t)$$

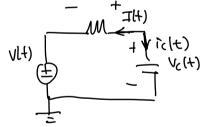
$$I(t) = C \frac{dV_{c}(t)}{dt}$$

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$$I(t) = C \frac{dV_{c}(t)}{dt}$$

$$V(t) - V_{c}(t) = C \frac{dV_{c}(t)}{dt}$$

$$CR$$



$$\frac{1}{4} \frac{1}{1} \frac{1}$$

$$kUL: V(t) - VR(t) - VC(t) = 0$$

 $V(t) - i(t) \cdot R - VC(t) = 0$

$$V(t) - C\frac{dVc(t)}{dt} \cdot R - Vc(t) = 0 \qquad V(t) - Vc(t) = C\frac{dVc}{dt}$$

$$V_{\frac{(t)-V_{c(t)}}{R}} = C_{\frac{dV_{c}}{dt}}$$