

EECS 16A DIS10B

email: moses.won(a)

OH: W 10AM-12PM PT (HWP)

Learning Objectives

① Implications of op amp model on analysis

(don't look at node voltage on output)
→ comes from a voltage source

② First op amp module: Buffer (aka voltage buffer)

③ Comparator behavior and practice

④ Vocabulary: "gain" - multiplier/scaling factor

⑤ Golden Rules - application

Playlist: (bit.ly/1bajukebox)

① Never Gonna Give you up
(Anonymas)

② Sweet Home Alabama
(Vaibhav Agrawal)

③ Sofia - Clairo
(Anonymas)

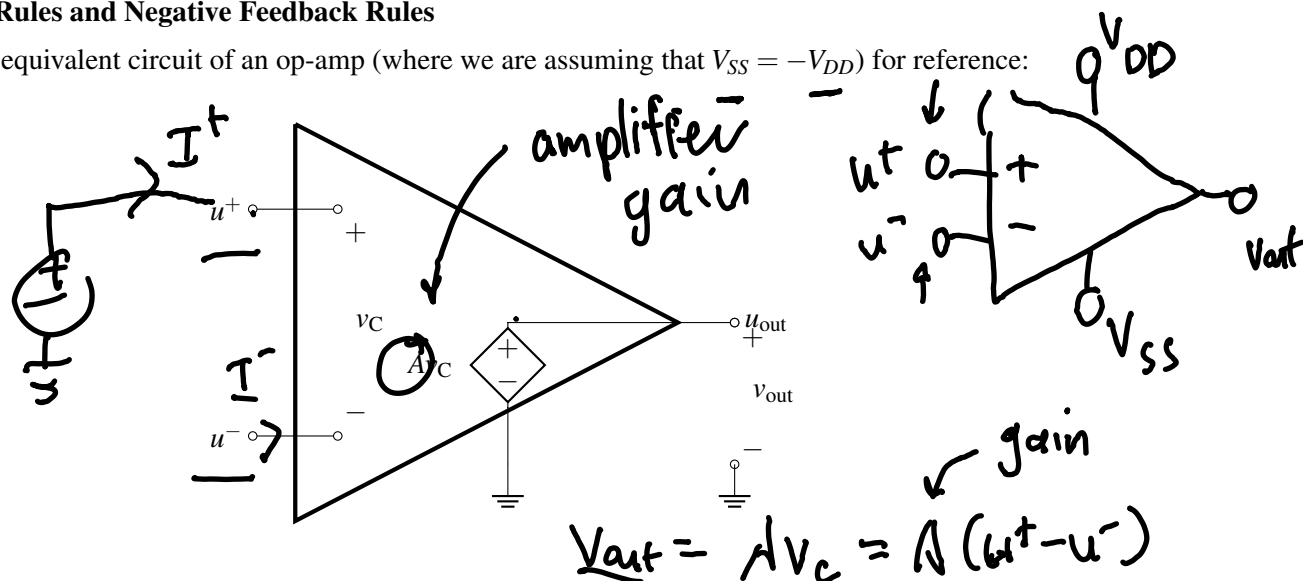
EECS 16A
Fall 2020

Designing Information Devices and Systems I

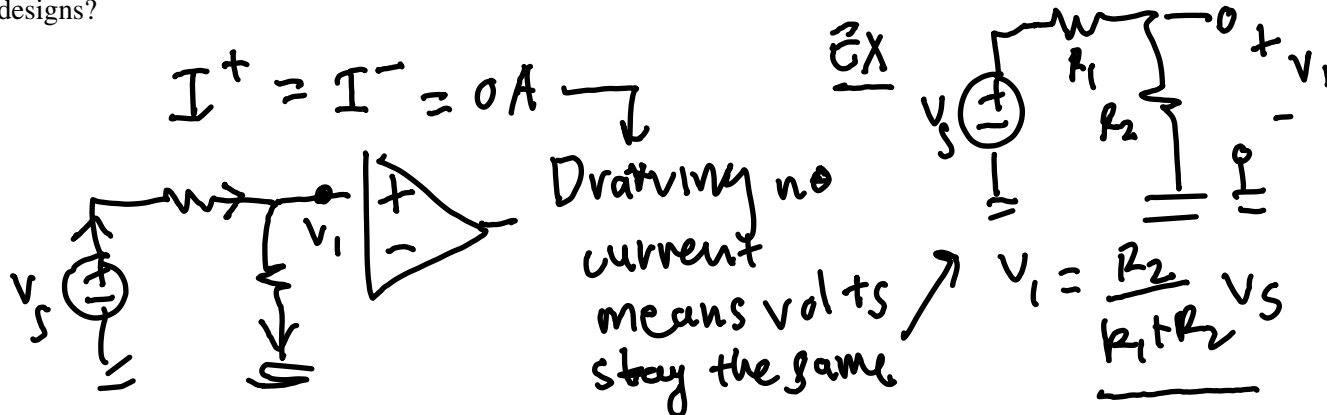
Discussion 10B

1. Op-Amp Rules and Negative Feedback Rules

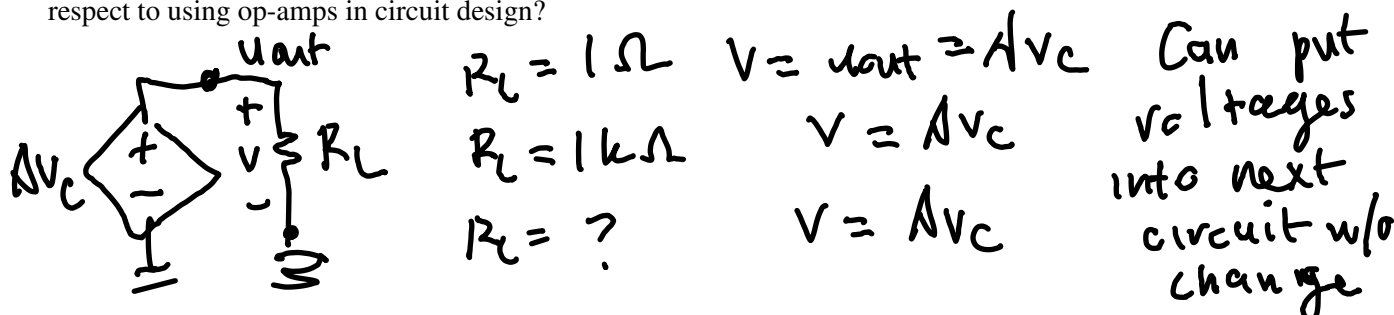
Here is an equivalent circuit of an op-amp (where we are assuming that $V_{SS} = -V_{DD}$) for reference:



- (a) What are the currents flowing into the positive and negative terminals of the op-amp (i.e., what are I^+ and I^-)? Based on this answer, what are some of the advantages of using an op-amp in your circuit designs?



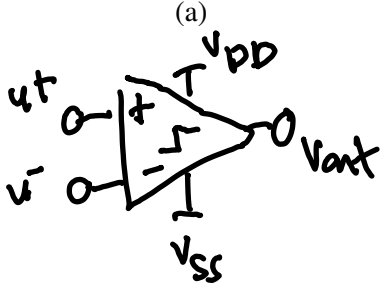
- (b) Suppose we add a resistor of value R_L between u_{out} and ground. What is the value of v_{out} ? Does your answer depend on R_L ? In other words, how does R_L affect $A v_c$? What are the implications of this with respect to using op-amps in circuit design?



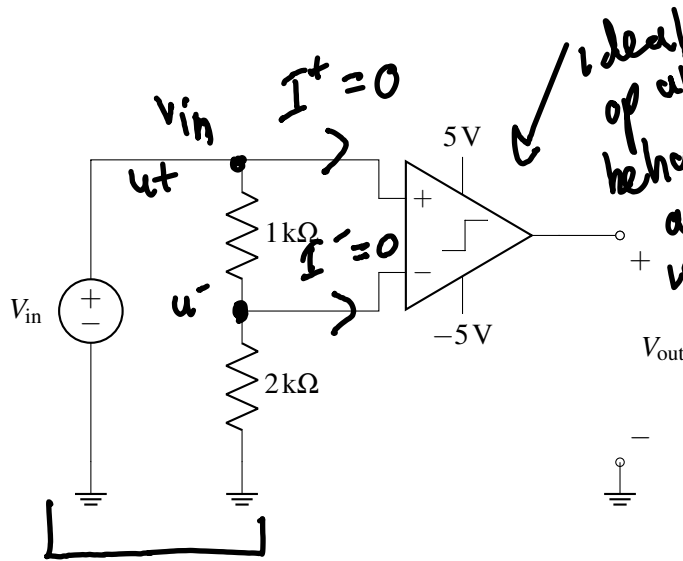
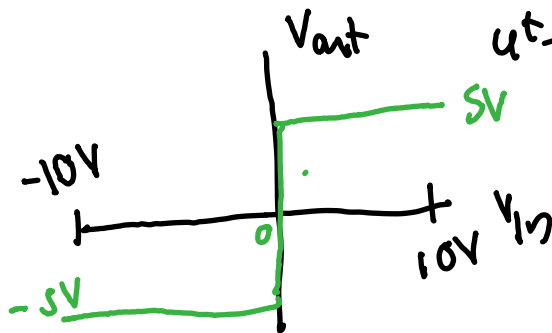
2. Comparators

For each of the circuits shown below, plot V_{out} for V_{in} ranging from -10V to 10V for part (a) and from 0V to 10V for part (b).

(a)



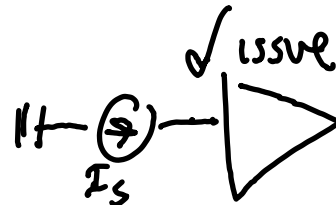
$$\begin{cases} u^+ - u^- > 0 & V_{out} = V_{DD} \\ u^+ - u^- < 0 & V_{out} = V_{SS} \\ u^+ > u^- \\ u^+ < u^- \end{cases}$$



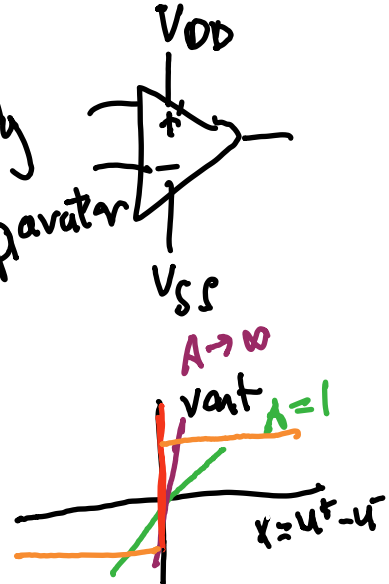
$$u^+ = V_{in}$$

$$u^- = \frac{2}{3} V_{in}$$

$$u^+ - u^- = V_{in} - \frac{2}{3} V_{in} = \frac{1}{3} V_{in}$$



ideal op amp behavior as comparator

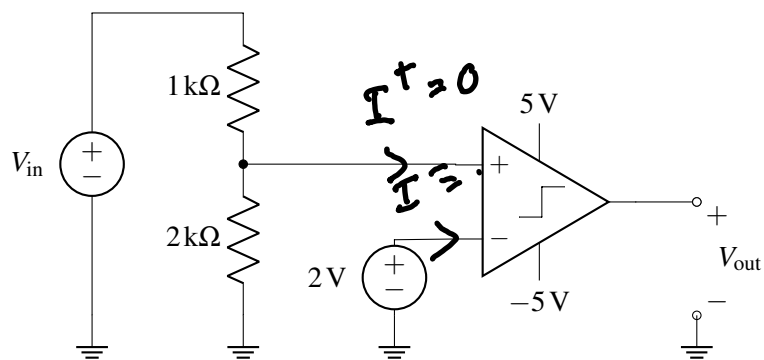


$$V_{out} = A (u^+ - u^-)$$

$$V_{out} = A x$$



(b)



$$u^+ = \frac{2}{3}V_{in}$$

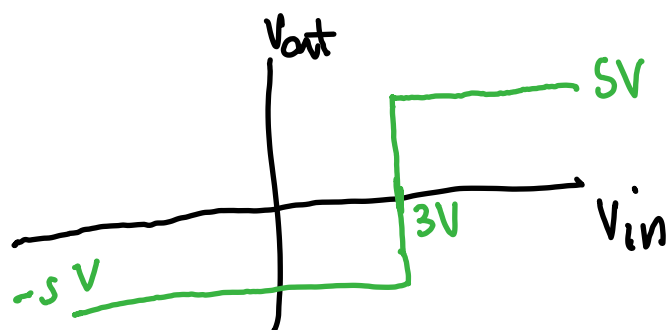
$$u^- = 2V$$

$$u^+ - u^- = \frac{2}{3}V_{in} - 2V$$

$$\frac{2}{3}V_{in} - 2V \geq 0 \quad V_{out} = 5V$$

$$\frac{2}{3}V_{in} > 2V$$

$$V_{in} > 3V \quad V_{out} = 5V$$



$$\frac{2}{3}V_{in} - 2V < 0 \quad V_{out} = -5V$$

$$\frac{2}{3}V_{in} < 2V$$

$$V_{in} < 3V \quad V_{out} = -5V$$

