

(P1)

Lecture 1

- Today:
- * EECS 16B intro
 - * Logistics
 - + Zoom proctoring all exams
 - + Lecture zoom organization (chat and Q&A)
 - + website: eecs16b.org
 - + STEM program, DSP
 - * EECS 16A review
 - * Thevenin & Norton Equivalents
 - * Intro to transistors

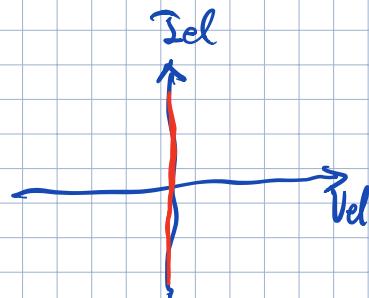
Key circuit elements:



$$Vel = 0$$

$$I_{el} = ?$$

set by ext. clt

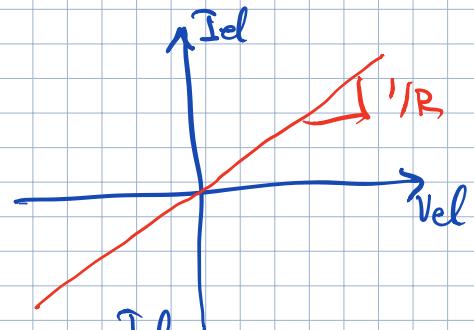


② Resistor



$$Vel = R \cdot I_{el}$$

(Ohm's law)



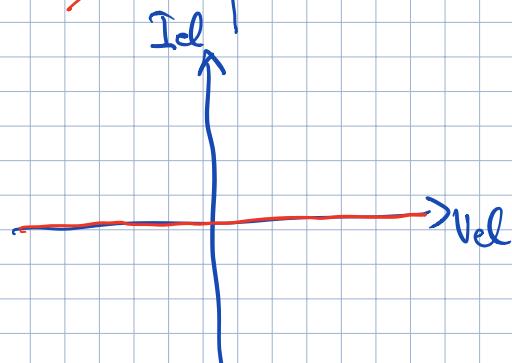
③ "Open" circuit



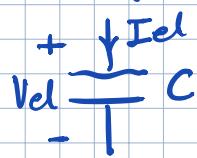
$$I_{el} = 0$$

$$Vel = ?$$

set by the
ext. clt



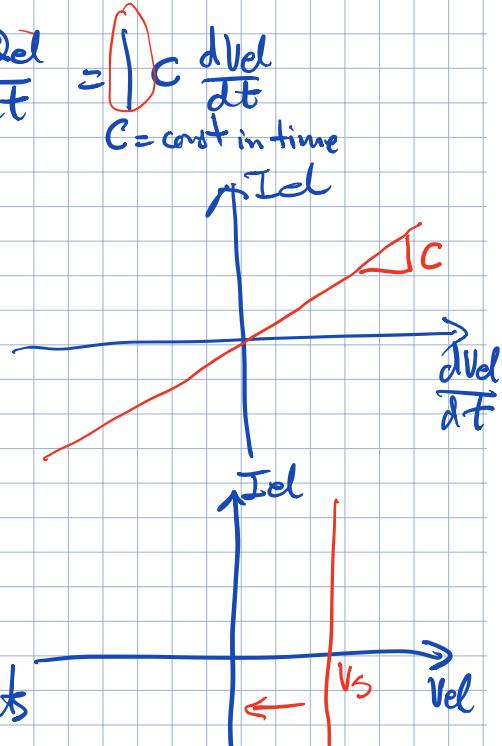
④ Capacitor



$$Q_{el} = C \cdot V_{el}$$

know: $I_{el} = \frac{dQ_{el}}{dt} = C \frac{dV_{el}}{dt}$

$C = \text{const in time}$



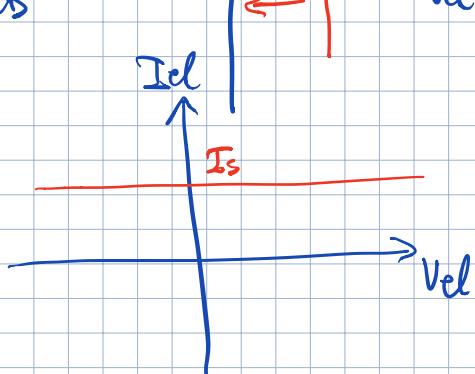
⑤ Voltage source



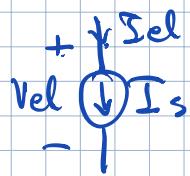
$$V_{el} = V_s$$

$$I_{el} = ?$$

set by ext. cirts



⑥ Current source

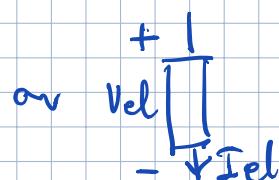
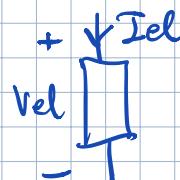


$$I_{el} = I_s$$

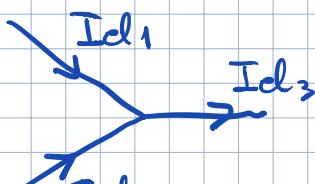
$$V_{el} = ?$$



Passive sign convention:



KCL:



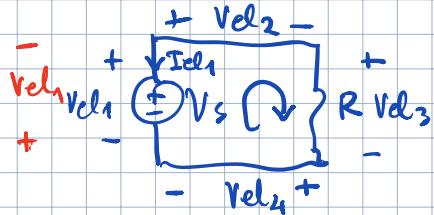
$$I_{el1} + I_{el2} - I_{el3} = 0$$

$$I_{el1} + I_{el2} = I_{el3}$$

Vel_2

$\text{Vel}_1 \text{ Vel}_3 \text{ Vel}_4$

KVL: Sum of voltages around the loop is 0.



$$\text{Vel}_1 - \text{Vel}_2 - \text{Vel}_3 - \text{Vel}_4 = 0$$

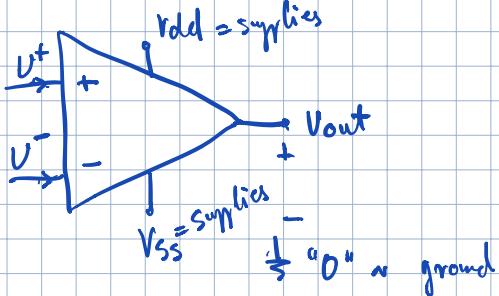
$$\text{Vel}_1 = \text{Vel}_3$$

$$-\text{Vel}_1 - \text{Vel}_2 - \text{Vel}_3 - \text{Vel}_4 = 0$$

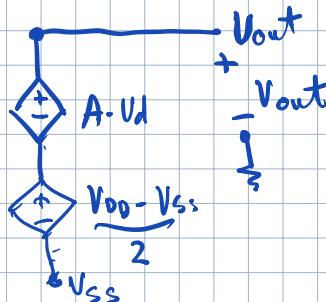
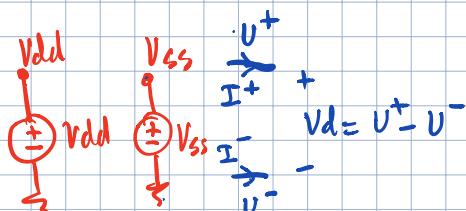
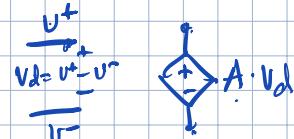
$$-\text{Vel}_1 = \text{Vel}_2$$

Op-amp element:

Symbol :



New element:
voltage-controlled
voltage source

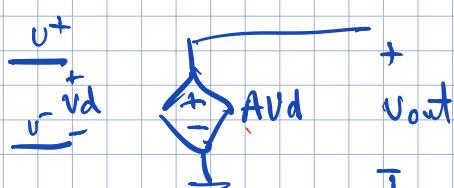


$$V_{\text{out}} = \begin{cases} V_{\text{dd}}, & V^* > V_{\text{dd}} \\ V_{\text{ss}} + \frac{V_{\text{dd}} - V_{\text{ss}}}{2} + A \cdot V_d, & V_{\text{ss}} \leq V^* \leq V_{\text{dd}} \\ V_{\text{ss}}, & V^* \leq V_{\text{ss}} \end{cases}$$

Negative feedback:

Golden rules:

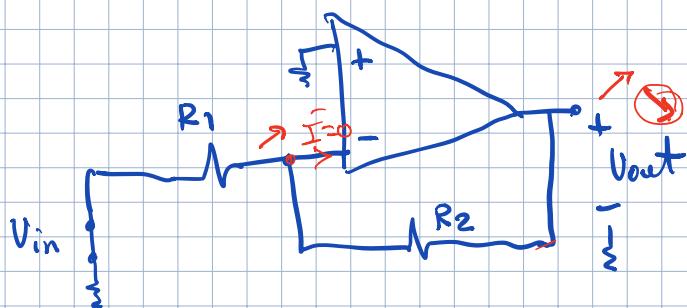
$$\text{GR1: } I^+ = I^- = 0$$



$$GR2: \underline{U^+ = U^-} \text{ (only when } NFB \& A \rightarrow \infty)$$

NFB example: Inverting amplifier

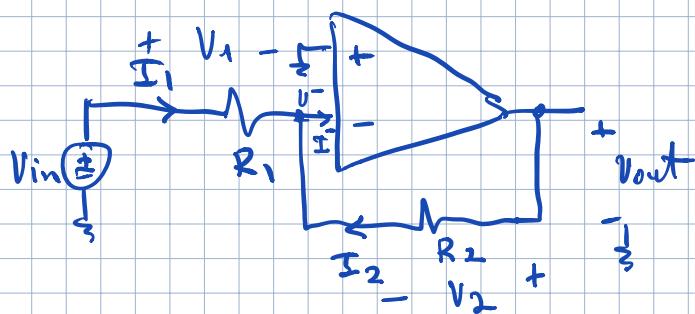
Test NFB:



① Null indep. sources

② Apply a charge at the output

③ Trace the feedback back to the output & verify that the charge is cancelled



$$V_{out} = f(V_{in}) = ?$$

$$V_{out} = -\frac{R_2}{R_1} V_{in}$$

$$KCL: I_1 + I_2 - I^- = 0$$

$$GR1: I^- = 0$$

$$\therefore I_1 + I_2 = 0$$

$$NFB \rightarrow GR2: U^+ = U^-$$

$$U^+ = 0 \Rightarrow U^- = 0$$

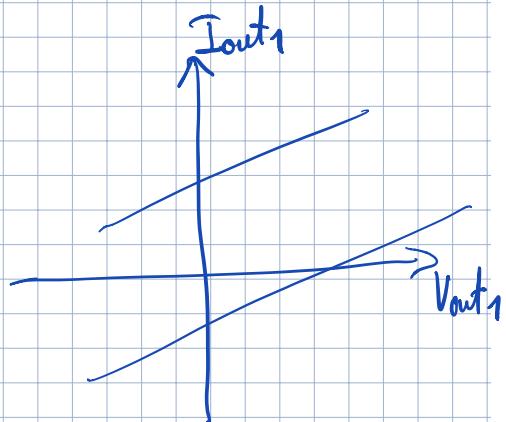
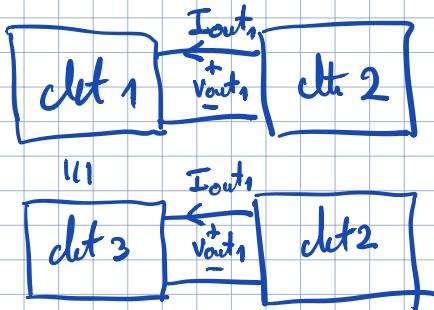
$$\text{Ohm's law: } I_1 = \frac{V_1}{R_1} = \frac{V_{in} - U^-}{R_1} = \frac{V_{in}}{R_1}$$

$$I_2 = \frac{V_2}{R_2} = \frac{V_{out} - U^-}{R_2} = \frac{V_{out}}{R_2}$$

$$\frac{V_{in}}{R_1} + \frac{V_{out}}{R_2} = 0$$

$$V_{out} = -\frac{R_2}{R_1} V_{in}$$

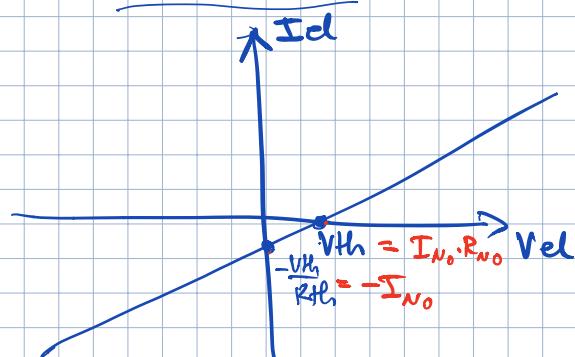
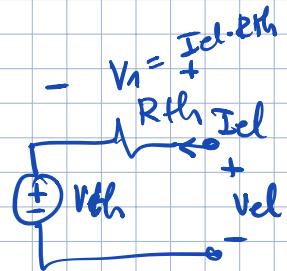
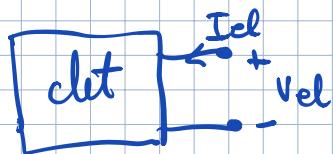
Equivalence :



In clts, two elements are equivalent if they have the same I-V characteristics.

Need a min of two elements (a resistor and a source) to create any I-V line.

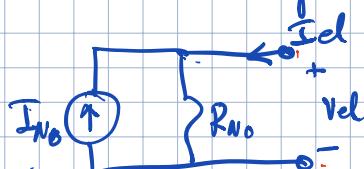
Thevenin equivalent :



$$V_{el} = V_{th} + I_{cl} \cdot R_{th}$$

$$I_{cl} = \frac{V_{el} - V_{th}}{R_{th}}$$

Norton equivalent:



To find V_{th} :

"Connect" an "open-circuit" across terminals
and measure $V_{open\text{-circuit}} = V_{th}$.

To find R_{th} :

zero-out ("null") indep. sources, and
then apply V_{test} and measure I_{test}
or apply I_{test} and measure V_{test}

$$R_{th} = \frac{V_{test}}{I_{test}}$$