

# Welcome to EECS 16A!

## Designing Information Devices and Systems I



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**Fall 2022**

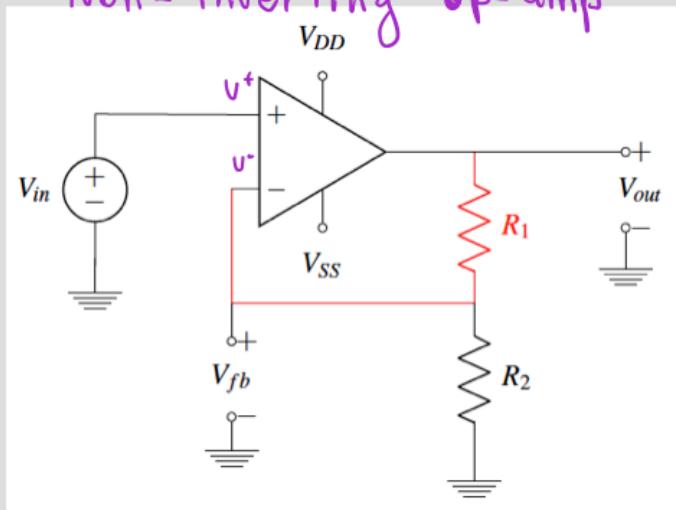
**Module 2**  
**Lecture 11**  
**Op-amp circuit analysis (Note 19)**



# Last Lecture...

## Op-Amp in negative feedback

Non-inverting op-amp



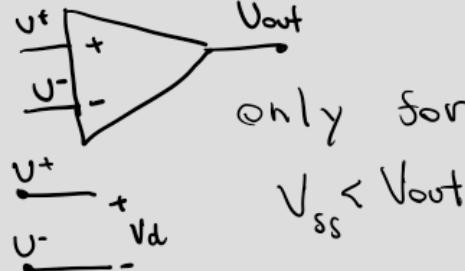
$$(1) \quad V_d = V^+ - V^- = V_{in} - V_{fb}$$

$$(2) \quad V_{out} = AV_d$$

$$(3) \quad V_{fb} = \frac{R_2}{R_1 + R_2} \cdot V_{out}$$

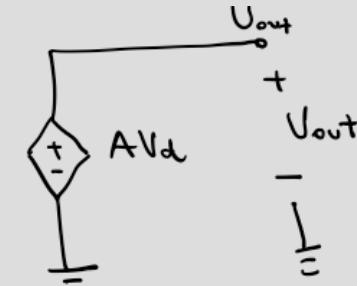
"BUFFER circuit"

Model:



only for

$$V_{SS} < V_{out} < V_{DD}$$



Simpler model as the second source is not "needed".

$$V_{out} = A (V_{in} - f \cdot V_{out})$$

$$V_{out} (1 + AF) = A V_{in}$$

$$A_v = \text{Gain} = \frac{V_{out}}{V_{in}} = \frac{A}{1+AF}$$

$$A_v = \frac{1}{f} \xrightarrow{A \rightarrow \infty} \frac{R_1 + R_2}{R_2} = 1 + \frac{R_1}{R_2}$$

# Golden Rules of Op-Amps

For our design we want  $A = 3$

$$V_d = \frac{V_{out}}{A} \quad \text{if } A \rightarrow \infty$$

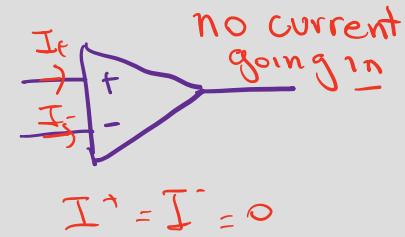
$$V_d = \frac{1}{A} \cdot \frac{A}{1+A_f} V_{in} = \frac{V_{in}}{1+A_f} = 0$$

In NFB :  $V^+ = V^-$  and  $A \rightarrow \infty$

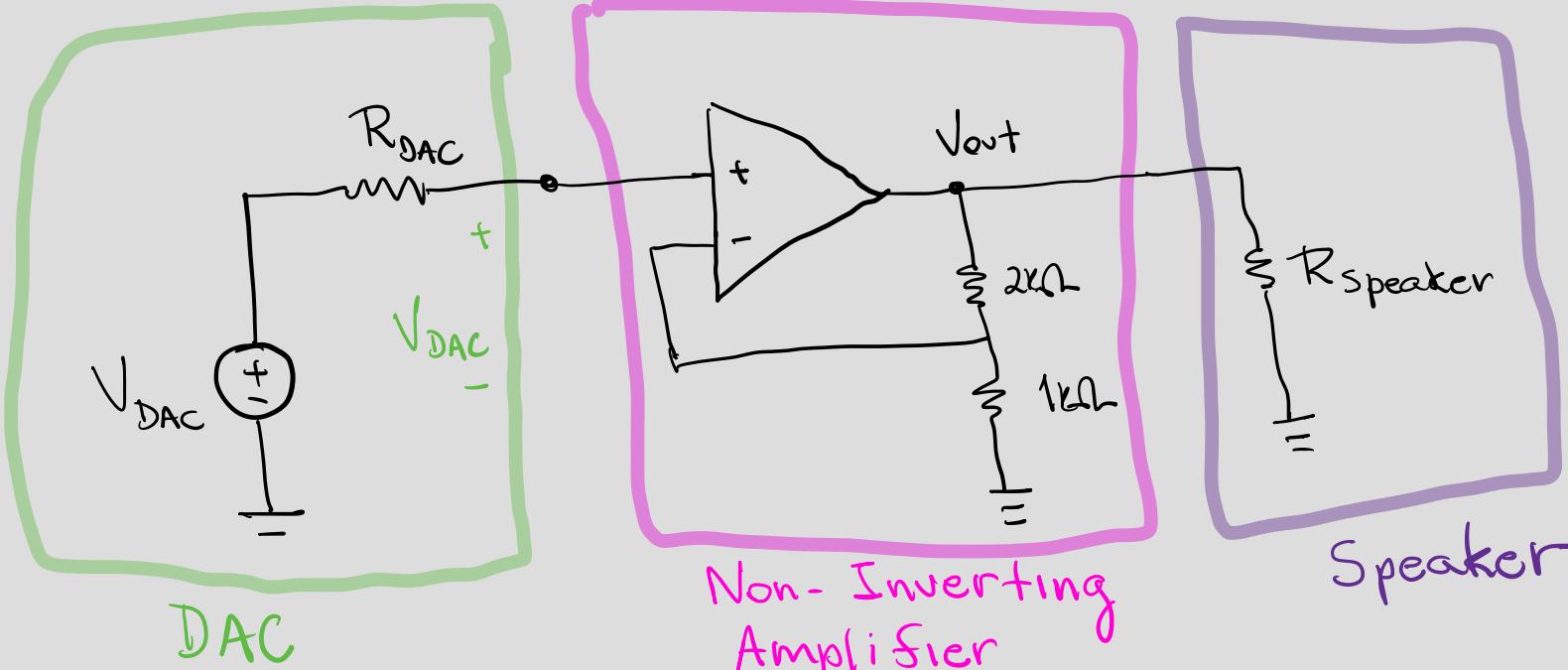
Rules: (Golden Rules)

(1)  $I^+ = I^- = 0$  (always true)

(2)  $V^+ = V^-$  (only in NFB &  $A \rightarrow \infty$ )



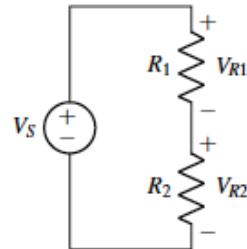
# Let's go back to playing music



Party time!  
Yay!

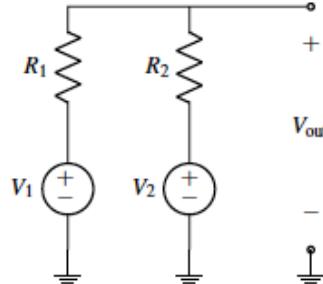
# Today

Voltage Divider



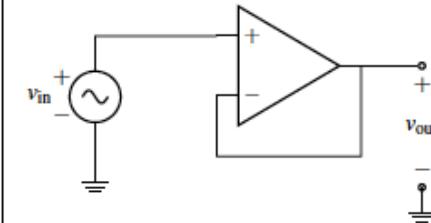
$$V_{R2} = V_S \left( \frac{R_2}{R_1 + R_2} \right)$$

Voltage Summer



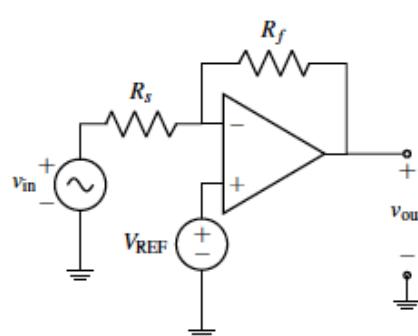
$$V_{out} = V_1 \left( \frac{R_2}{R_1 + R_2} \right) + V_2 \left( \frac{R_1}{R_1 + R_2} \right)$$

Unity Gain Buffer



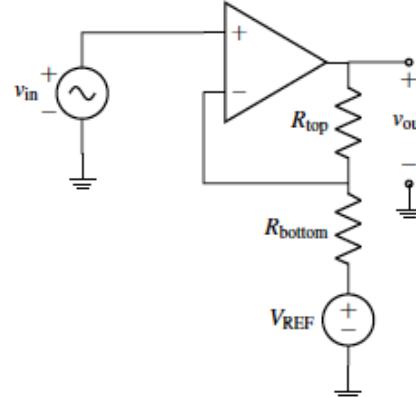
$$\frac{v_{out}}{v_{in}} = 1$$

Inverting Amplifier



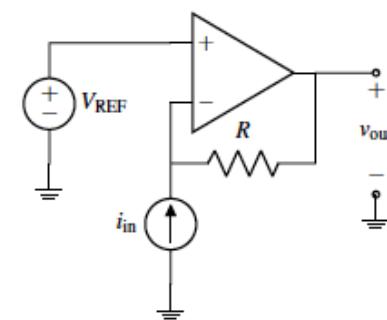
$$v_{out} = v_{in} \left( -\frac{R_f}{R_s} \right) + V_{REF} \left( \frac{R_f}{R_s} + 1 \right)$$

Non-inverting Amplifier



$$v_{out} = v_{in} \left( 1 + \frac{R_{top}}{R_{bottom}} \right) - V_{REF} \left( \frac{R_{top}}{R_{bottom}} \right)$$

Transresistance Amplifier



$$v_{out} = i_{in}(-R) + V_{REF}$$

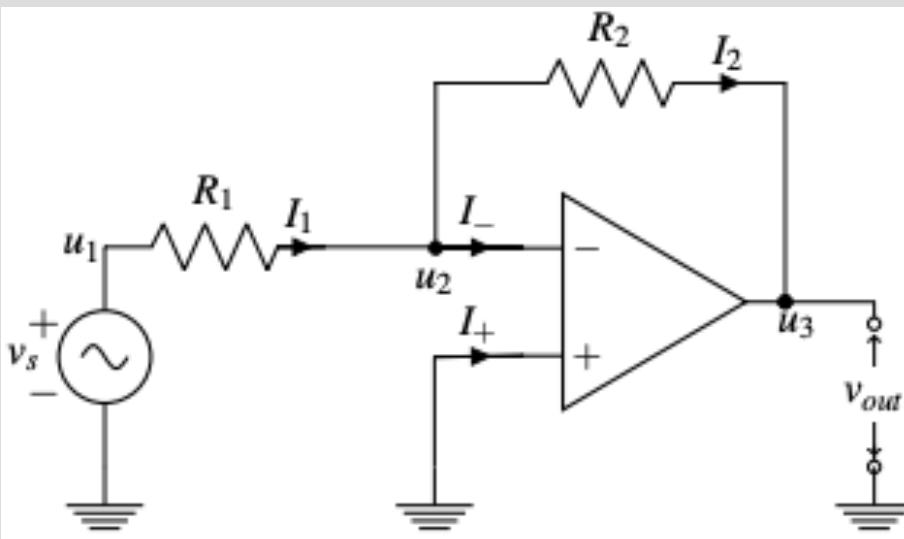
# Checking for Negative Feedback (Determining the polarity of NFB)

Step 1 – Zero out all independent sources : replacing voltage sources with wires and current sources with open circuits as in superposition

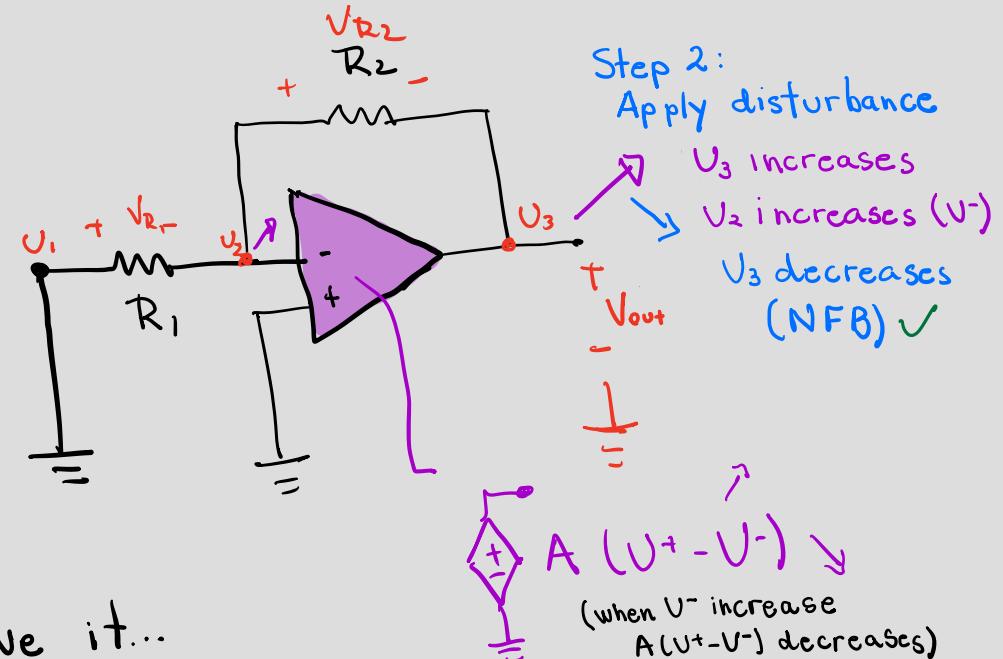


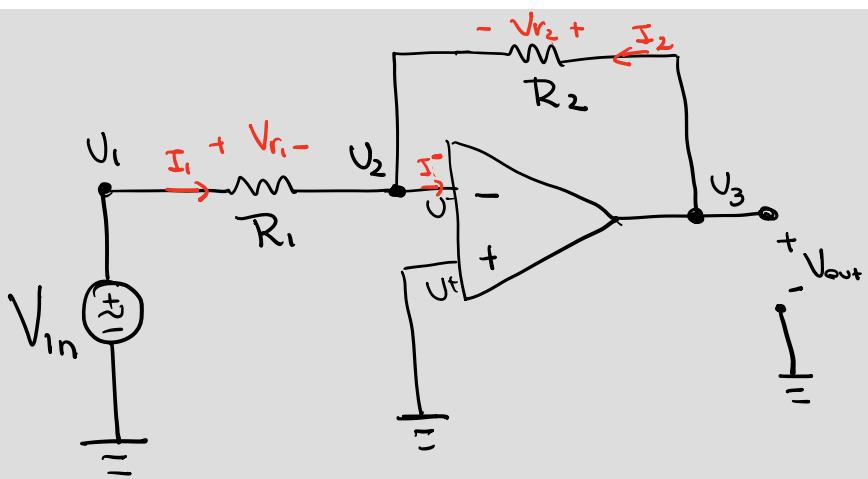
Step 2 – Wiggle the output and check the loop – to check how the feedback loop responds to a change.

- if the error signal decreases, the output must also decrease. **The circuit is in negative feedback**
- if the error signal increases, the output must also increase. **The circuit is in positive feedback**



Now lets solve it...





NFB  $\Rightarrow$  GR #2 applies  
 $V^+ = V^-$

- ①  $V_1 = V_{in}$   
 $V_3 = V_{out}$   
 $V_2 = 0$  (circuit in NFB  $\Rightarrow$  GR#2 applies  $V^+ = V^-$   
 $\hookrightarrow V_2 = V^-$  we know  $V^+ = 0 \Rightarrow V^- = 0$   
 $V^- = V_2 \Rightarrow V_2 = 0$ )

② Element Definitions:

$$V_{R1} = I_1 R_1$$

$$V_{R2} = I_2 R_2$$

Voltage Def.  
 $V_{R1} = U_1 - U_2 = V_1 = V_{in}$   
 $V_{R2} = U_3 - U_2 = V_3 = V_{out}$

③ (KCL)  
 $I_1 + I_2 = 0$  (GR#1)

$$I_1 + I_2 = 0$$

Inverting  
Amplifier

$$V_{in} = V_1 = I_1 R_1$$

$$V_{out} = V_3 = I_2 R_2$$

$$I_1 + I_2 = 0$$

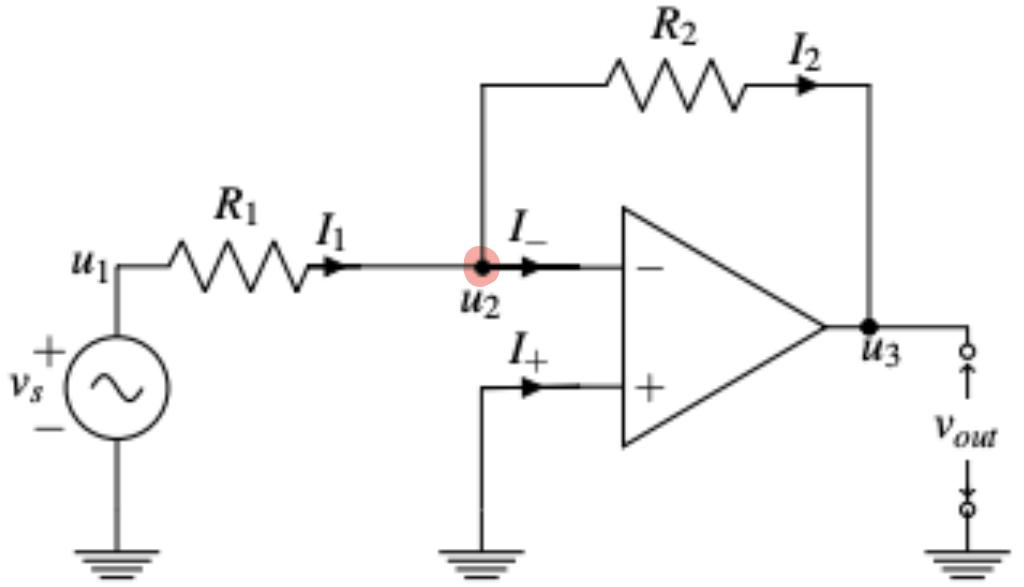
$$\frac{V_{in}}{R_1} + \frac{V_{out}}{R_2} = 0$$

$$V_{out} = R_2 \cdot \left( -\frac{V_{in}}{R_1} \right)$$

$$V_{out} = -\frac{R_2}{R_1} \cdot V_{in}$$

$A_V = \frac{V_{out}}{V_{in}} = -\frac{R_2}{R_1}$

# A faster way...



$$\text{GR2: } V^+ = V^-$$

$$V_2 \approx V^- \\ V^+ = 0 \Rightarrow V_2 = 0$$

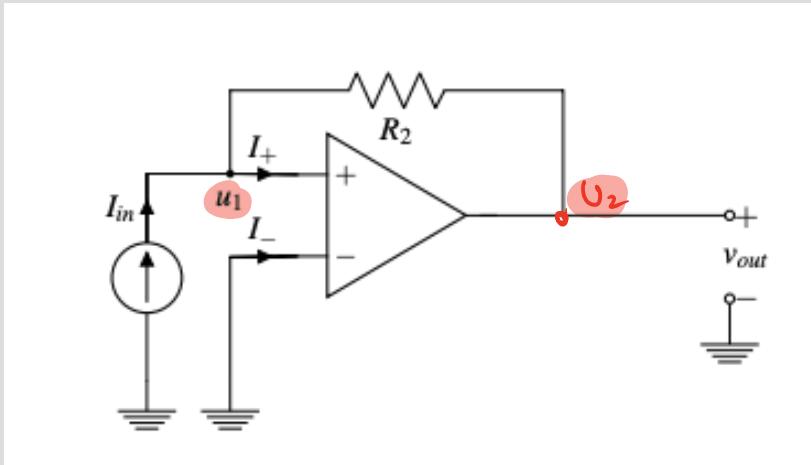
$$\text{GR1 + KCL} \quad (I_1 = I_2 + I^-)$$

$$\frac{V_2 - V_1}{R_1} = \frac{V_3 - V_2}{R_2} + I^- \\ -\frac{V_1}{R_1} = \frac{V_3}{R_2}$$

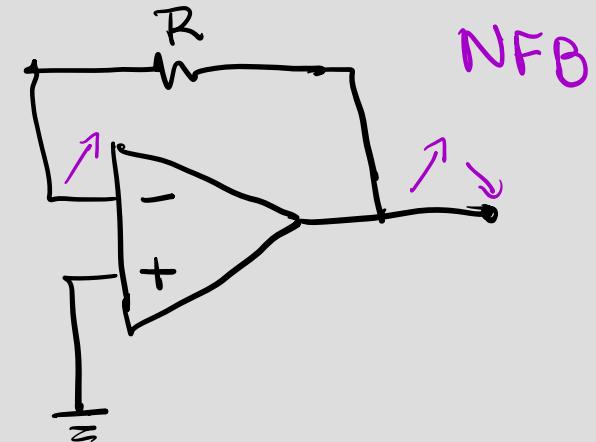
$$\frac{V_{out}}{V_{in}} = -\frac{R_2}{R_1}$$

# Example circuit 2 (trans-resistance amplifier)

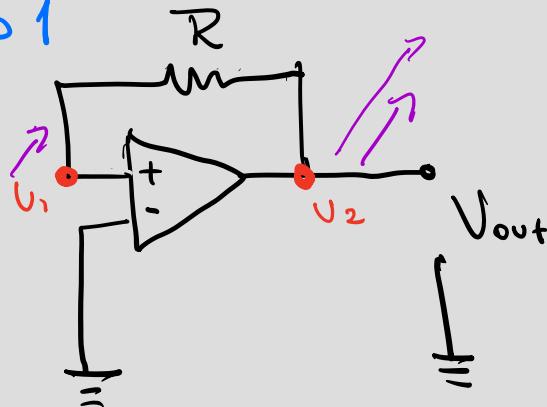
$$I^+ = Q \Rightarrow U_1 = U_2$$



Invert polarity  
⇒



Step 1

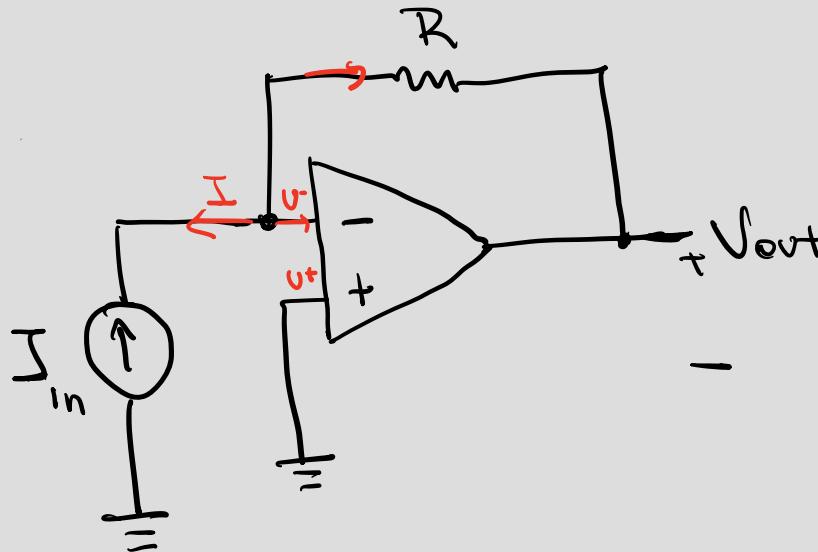


Step 2: Check for NFB

Increase output →

+ Moves up  
output increases  
by a lot

X Not in  
NFB



$$\text{NFB : } U^+ = U^-$$

$$U^+ = 0 \rightarrow U^- = 0$$

GR # 2

~~$$\frac{U^- - V_{out}}{R} + (-I_{in}) + I^- = 0$$~~

$$-\frac{V_{out}}{R} = I_{in}$$

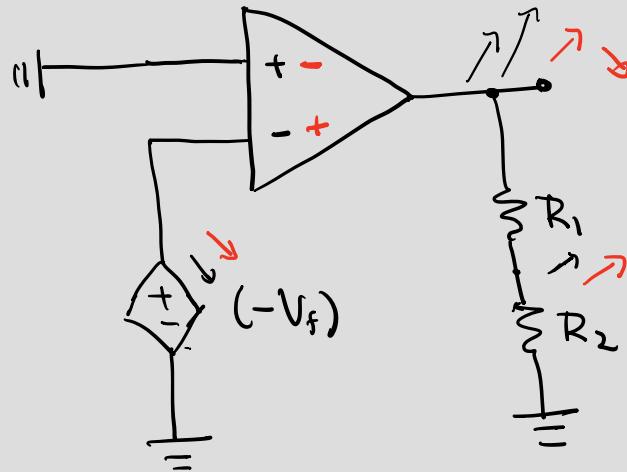
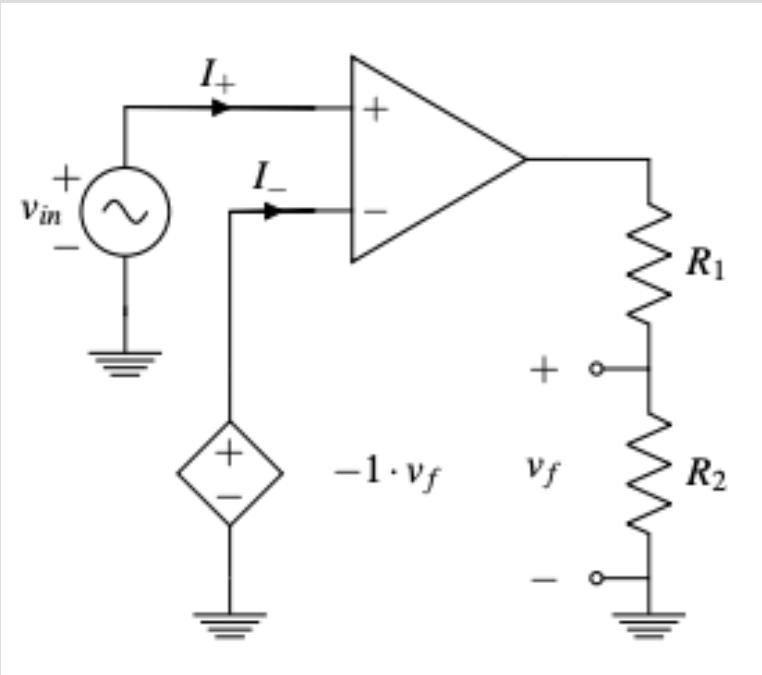
$$V_{out} = -I_{in} R$$

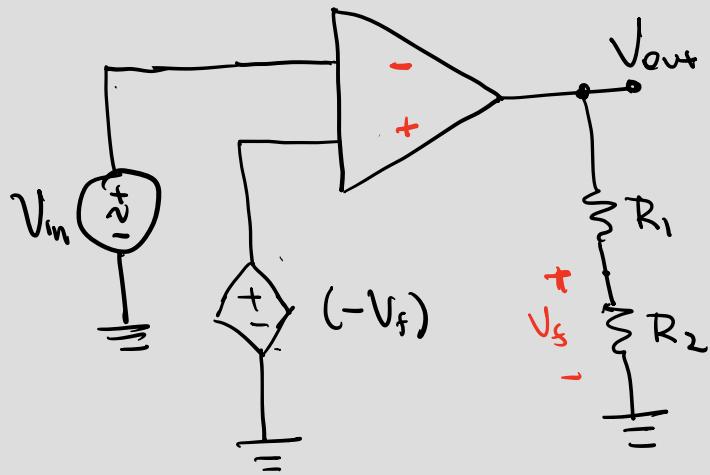
$$\frac{V_{out}}{I_{in}} = -R$$

The input is current ; output is voltage : we use this model in the lab for photo sensors !

## Example circuit 3 -

Check NFB :





Voltage Divider

$$V_f = \frac{R_2}{R_1 + R_2} \cdot V_{out}$$

NFB (GR #2)  $V^- = V^+$

$$\cancel{V_{in}} = -V_f \cancel{V^+}$$

$$V_{in} = -\frac{R_2}{R_1 + R_2} V_{out} \Rightarrow \frac{V_{in}}{V_{out}} = -\frac{R_2}{R_1 + R_2}$$

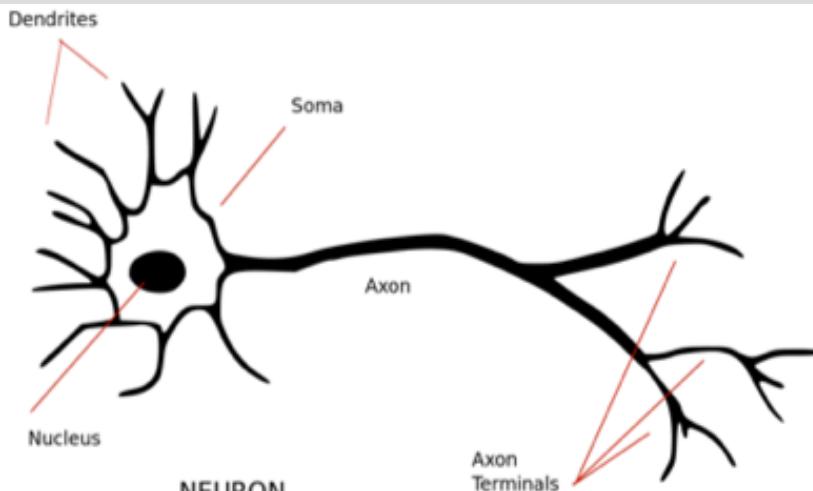
$$A_v = \frac{V_{out}}{V_{in}} = -\frac{R_1 + R_2}{R_2} = -\left(1 + \frac{R_1}{R_2}\right)$$

# Artificial Neuron

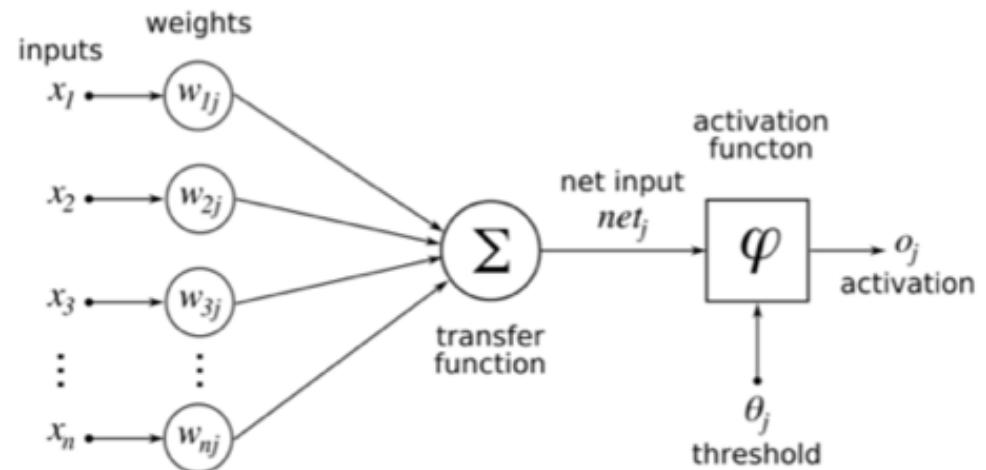
(Energy Efficient Neural Networks) — Yes we can!

- Neurons in our brain are interconnected.
- The output of a single-neuron is dependent on inputs from several other neurons.
- This idea is represented with vector-vector multiplication – the output is a linear combination of several inputs.
- An artificial neuron circuit must perform addition and multiplication.

$$\begin{bmatrix} a_1 & a_2 \end{bmatrix} \cdot \begin{bmatrix} v_1 \\ v_2 \end{bmatrix} = a_1 v_1 + a_2 v_2$$



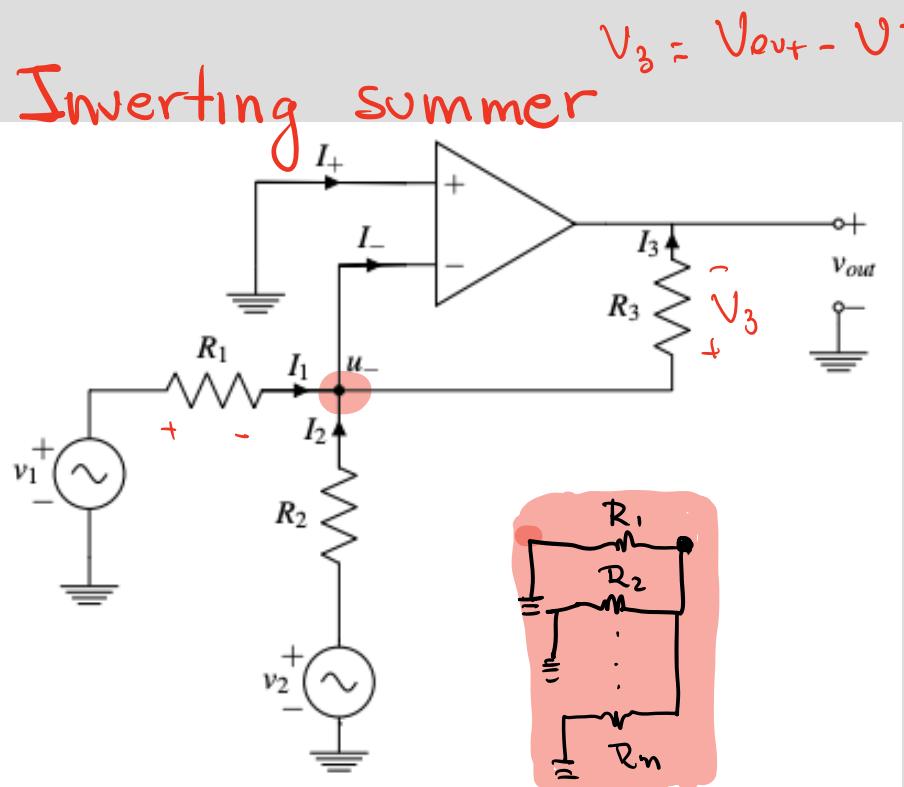
A biological Neuron



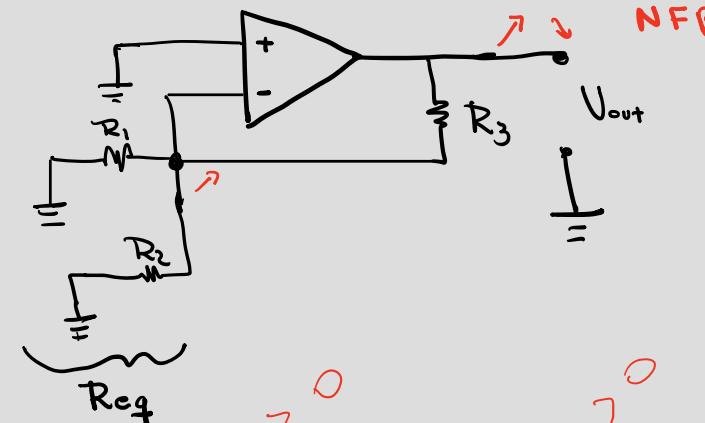
An Artificial Neuron

# Artificial Neuron

- Neurons in our brain are interconnected.
- The output of a single-neuron is dependent on inputs from several other neurons.
- This idea is represented with vector-vector multiplication – the output is a linear combination of several inputs.
- An artificial neuron circuit must perform addition and multiplication.



Check for NFB:



$$V^+ = V^- : GR2$$

$$V^+ = 0 \Rightarrow V^- \approx 0$$

KCL:  $\frac{V^- - V_1}{R_1} + \frac{V^- - V_2}{R_2} = \frac{I^-}{R_3} + \frac{V_{out} - V^-}{R_3}$

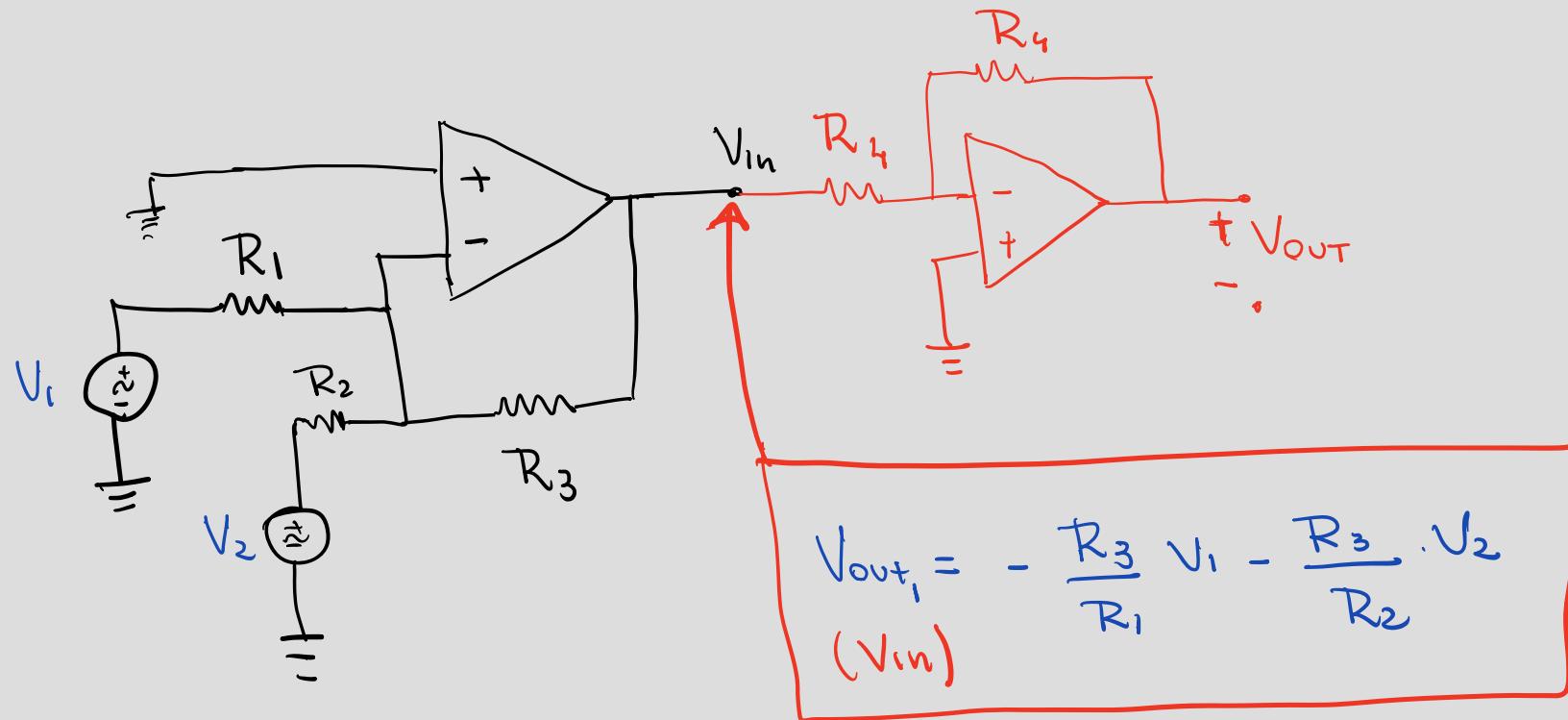
$$-\frac{V_1}{R_1} - \frac{V_2}{R_2} = \frac{V_{out}}{R_3}$$

$$V_{out} = -\frac{R_3}{R_1} \cdot V_1 + \left( -\frac{R_3}{R_2} V_2 \right) + \dots + \left( -\frac{R_3}{R_N} V_N \right)$$

\underbrace{\phantom{-\frac{R\_3}{R\_1} \cdot V\_1}}\_{\text{only negative weights}} \quad \underbrace{\phantom{-\frac{R\_3}{R\_2} V\_2}}\_{a\_{12} \cdot v\_2} \quad \underbrace{\phantom{-\frac{R\_3}{R\_N} V\_N}}\_{a\_{1N} \cdot v\_N}

All weights are negative : How can we make  $a_1$  and  $a_2$  positive?

Add another inverting amplifier circuit.



$$\frac{V_{out+}}{V_{in}} = - \frac{R_2}{R_1}$$

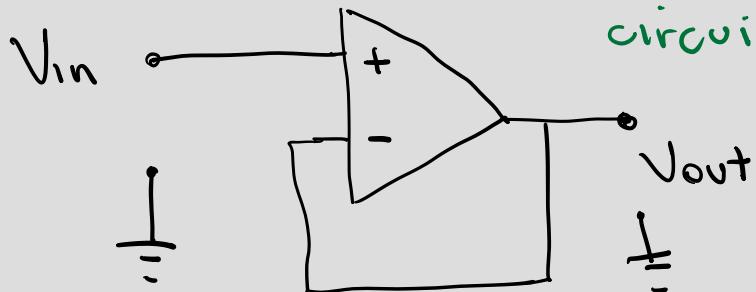
In result from  
Inverting amplifier

$$V_{out+} = - \frac{R_2}{R_1} \cdot V_{in}$$

$V_{out+} = - V_{in}$  (when  $R_1$  and  $R_2$  are the same)

# Unity Gain Buffer

↳ Allows us  
to isolate  
circuits



$$V^+ = V_{in}$$

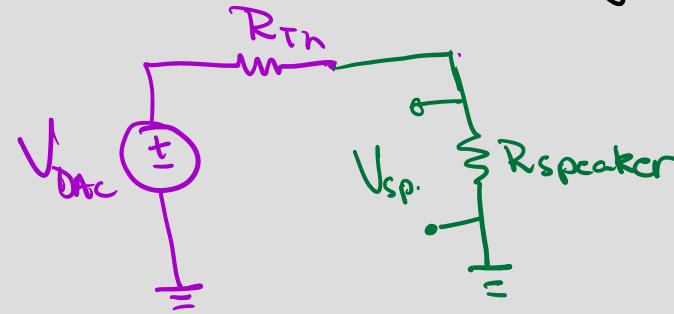
$$V^- = V_{out}$$

GR2

$$U^+ = U^-$$

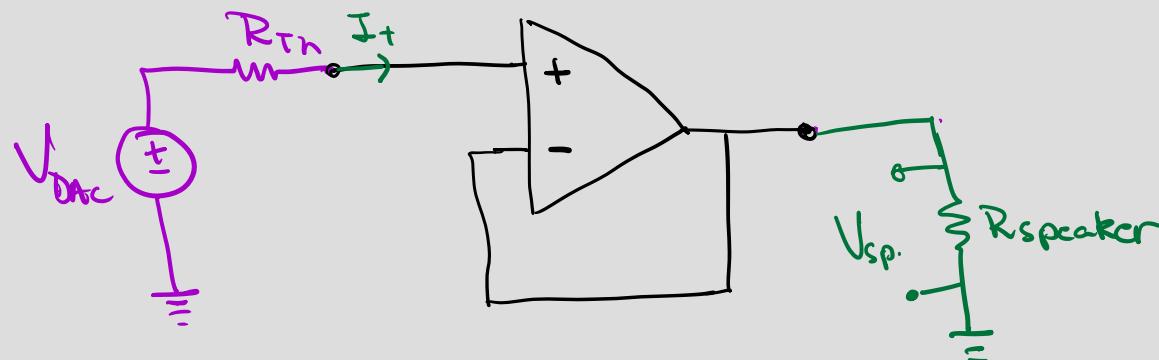
$$V_{in} = V_{out}$$

# Speaker Design



$$V_{speaker} = \frac{V_{DAC}}{126}$$

loading



$$I^+ = 0 \Rightarrow V^+ = V_{DAC}$$

$$V_{out} = V_{speaker} = U^- \Rightarrow U^+ = U^-$$

$$V_{DAC} = V_{speaker}$$