

LECTURE #5

CS 170

Spring 2021



Today start a new unit: algorithms for graphs.

Why graphs? They are a powerful abstraction.

Ex: • coloring maps

• scheduling constraints

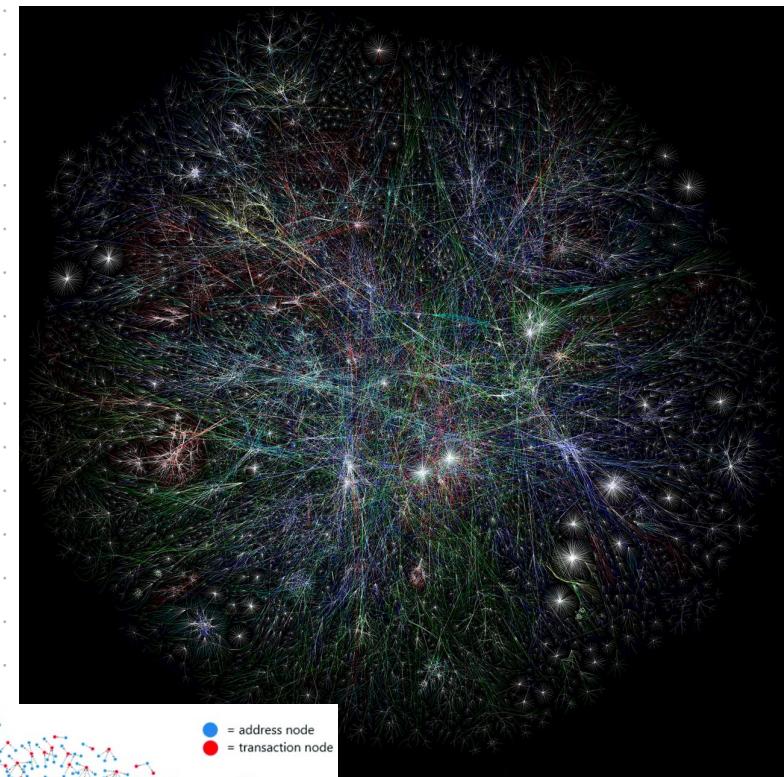
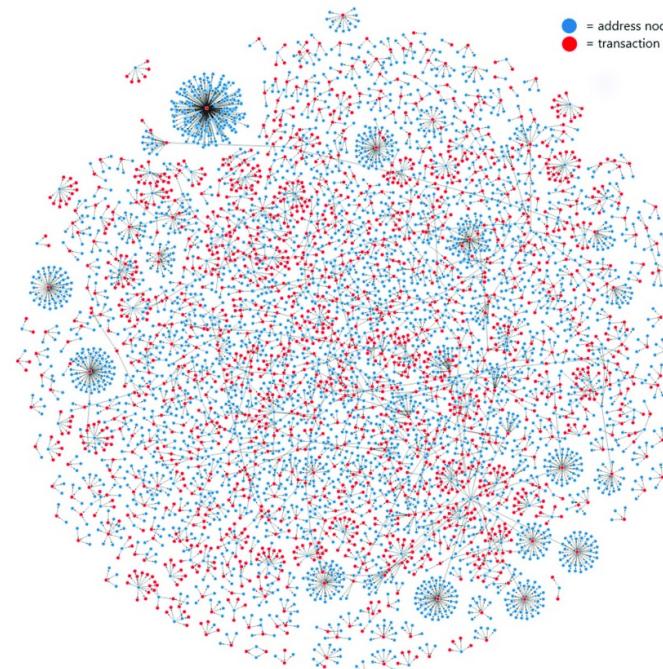
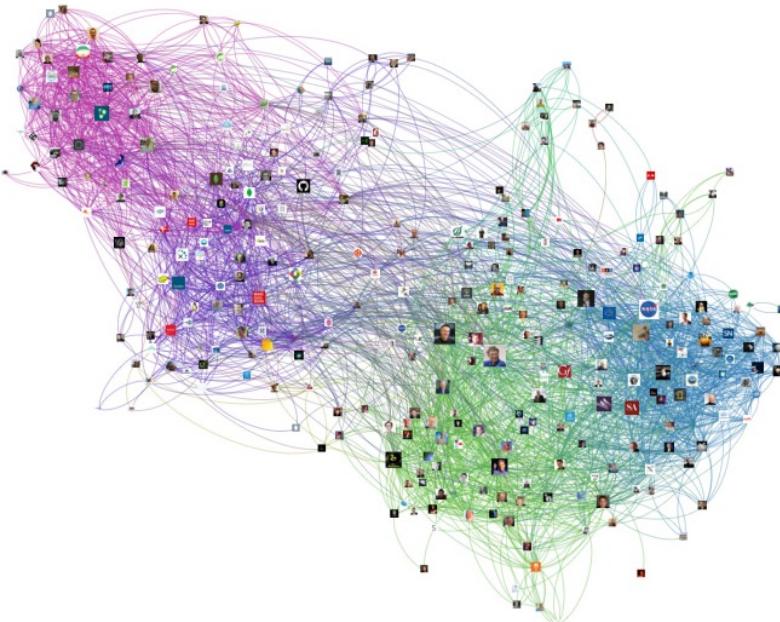
• web graph ($\sim 10^9$ vertices each with 10^2 edges)

• connectome ($\sim 10^{10}$ vertices each with 10^4 edges)

• Bitcoin transaction graph

• social networks

• road maps

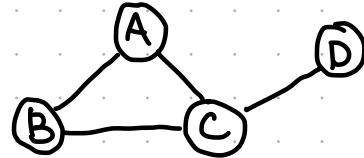


GRAPH REPRESENTATIONS

$$n := |V|, m := |E|$$

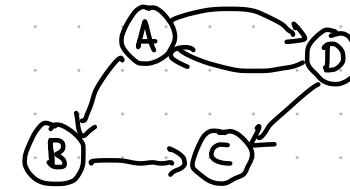
A graph is a pair (V, E) where V is the vertex set and $E \subseteq V \times V$ is the edge set.

- undirected graph:



edge relation is symmetric

- directed graph:



You can represent a graph via:

① adjacency matrix $M_G = \begin{pmatrix} & \cdots & j \\ i & \cdots & m_{ij} \\ & \cdots & \end{pmatrix} \in \{0,1\}^{n \times n}$

matrix

$$m_{ij} = 1 \Leftrightarrow (i,j) \in E$$

② adjacency list

$$L_G = 1 \mapsto 3, 7, 5, 11$$

$$3 \mapsto 2, 5, 7$$

⋮

Tradeoffs between representations:

	space	$(u,v) \in E?$	neighbors(u)
matrix	$O(V ^2)$	$O(1)$	$O(V)$
list	$O(E)$	$O(\text{degree})$	$O(\text{degree})$

Today we study GRAPH CONNECTIVITY. (E.g. which parts of G are reachable from $v \in V$?)

The Explore Procedure

Consider this recursive exploration procedure:

`explore (G, v)`:

- $\text{visited}[v] := \text{true}$
- for each $(v, u) \in E$.

| if not $\text{visited}[u]$: `explore (G, u)`

claim: visits every vertex reachable from v

proof: Suppose there exists $a \in V$ that is reachable from v but is NOT visited.

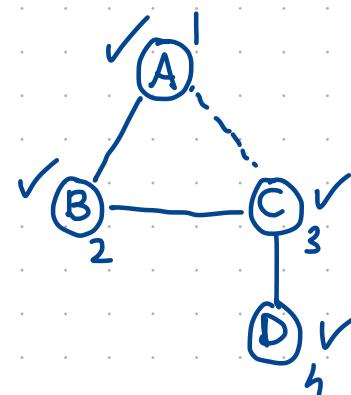
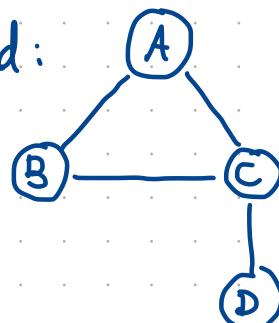
Look along any path from v to a :  A horizontal path from node v to node a , with nodes w and z in between. Node v is white. Nodes w and z are green, indicating they are visited.

Let z be the first unvisited node, and w the (visited) one before it.

When $\text{explore}(G, w)$ was run, it would have run $\text{explore}(G, z)$.

This is a contradiction so a was visited. \blacksquare

Ex on undirected:



Depth-First Search (DFS) [undirected case]

To visit the whole graph G we use:

- DFS(G):
1. For $v \in V$: $\text{visited}[v] = \text{false}$
 2. For $v \in V$: if not $\text{visited}[v]$ then $\text{explore}(G, v)$

claim: DFS runs in time $O(|V| + |E|)$

Recursion would yield a sloppy bound.

DFS is recursive not due to Divide & Conquer but due to stack.

A more precise accounting is:

- init vector of vertices is $O(|V|)$
- go over each vertex ($O(|V|)$) and maybe explore it

Observation: an undirected edge $(i, j) \in E$ is examined twice [in $\text{explore}(G, i)$ and $\text{explore}(G, j)$]

$\Rightarrow O(|E|)$ work in explorations

Connectivity [undirected case]

A graph is **connected** if every pair of vertices are connected by a path.

The **connected components** are the subgraphs induced by this equivalence relation.

Q: how to find connected components?

CC(G):

1. For $v \in V$: $\text{visited}[v] := \text{false}$

$\text{ccnum}[v] := 1$

2. $\text{cc} := 0$

3. For $v \in V$:

if not $\text{visited}[v]$:

$\text{cc} := \text{cc} + 1$

$\text{explore}(G, v)$

explore(G, v):

- $\text{visited}[v] := \text{true}$

- $\text{ccnum}[v] := \text{cc}$

- for each $(v, u) \in E$.

- If not $\text{visited}[u]$: **explore**(G, u)

A connected component consists of vertices with the same ccnum value.

(They are the vertices within the same tree in the forest of trees induced by exploration.)

The running time is $O(|V| + |E|)$,

Tracking time in DFS

Maintain global clock & exploration, marking for each vertex the first discovery and final departure.

DFS(G):

1. For $v \in V$: $\text{visited}[v] := \text{false}$

$\text{pre}[v] := \perp$, $\text{post}[v] := \perp$

2. $\text{clock} := 1$

3. For $v \in V$:

if not $\text{visited}[v]$ then $\text{explore}(G, v)$

$\text{explore}(G, v)$:

- $\text{visited}[v] := \text{true}$

- $\text{pre}[v] := \text{clock}$

- $\text{clock}++$

- for each $(v, u) \in E$.

If not $\text{visited}[u]$: $\text{explore}(G, u)$

- $\text{post}[v] := \text{clock}$

- $\text{clock}++$

For any vertex $v \in V$: $v \mapsto [\text{pre}[v], \text{post}[v]]$



interval on the stack (FILO)

Pre and post numbers determine edge types

Any two intervals are disjoint or nested (like valid parentheses).

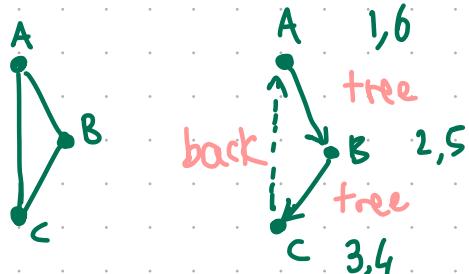
UNDIRECTED CASE

$$\begin{array}{c} ((u,v) \in E \\ \text{iff} \\ (v,u) \in E) \end{array}$$

If $(u,v) \in E$:

- tree edge $\begin{bmatrix} & [&] &] \\ u & v & v & u \end{bmatrix}$
part of DFS forest

- back edge $\begin{bmatrix} \dots & [&] &] \\ v & u & u & v \end{bmatrix}$
to ancestor



DIRECTED CASE

If $(u,v) \in E$:

- tree edge $\begin{bmatrix} & [&] &] \\ u & v & v & u \end{bmatrix}$
part of DFS forest

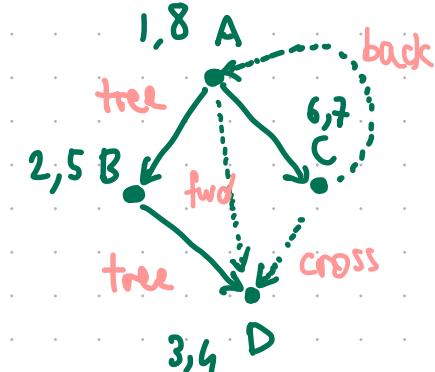
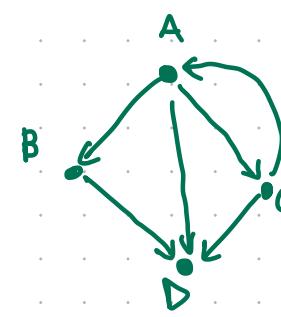
- forward edge same as above
to non-child descendant

- back edge $\begin{bmatrix} & [&] &] \\ v & u & u & v \end{bmatrix}$
to ancestor

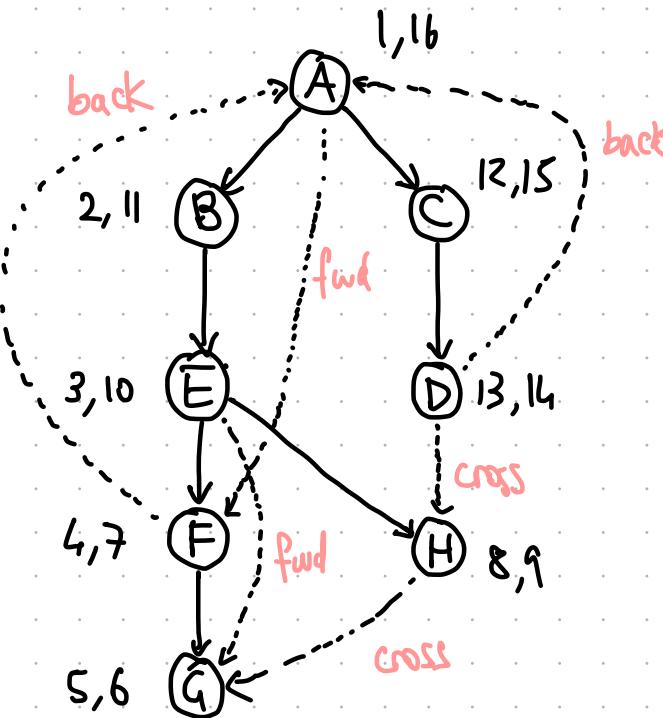
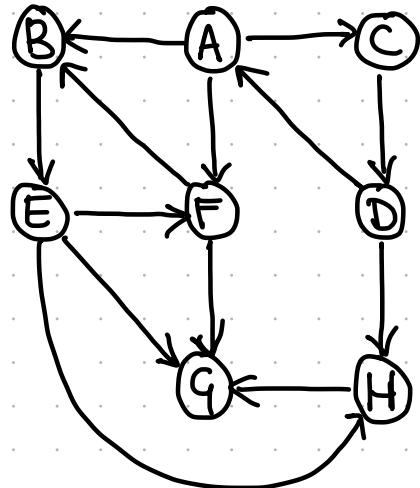
- cross edge $\begin{bmatrix} & [&] & [&] \\ v & v & u & u \end{bmatrix}$
to already post-visited

This is impossible:

$$\begin{bmatrix} & [&] & [&] \\ u & u & v & v \end{bmatrix}$$



Example of directed graph



What are pre and post numbers useful for?

To begin, we recover an algorithm for finding connected components in undirected graphs:

$CC(G)$:= "Run DFS and then the connected components are according to base level parentheses."

Moreover, we can use them for many other tasks, as we see next (and next lecture).

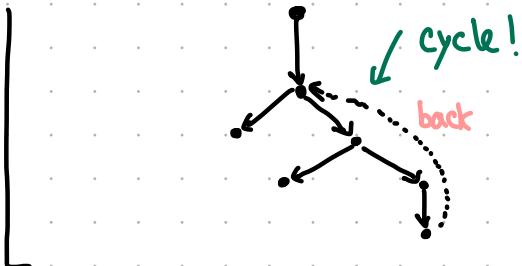
Directed Acyclic Graphs

Here **acyclic** means without cycles.

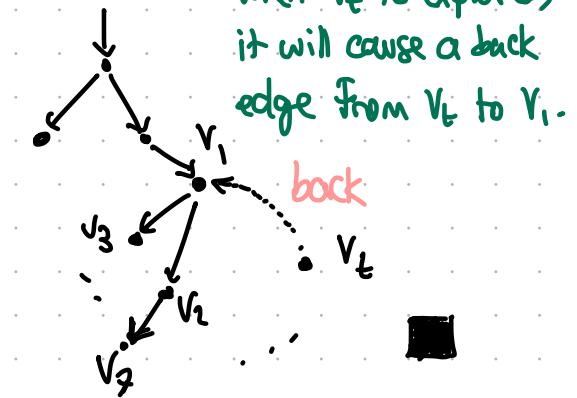
They are useful to model causalities, hierarchies, temporal dependencies, ...

claim: G is acyclic \Leftrightarrow no back edges in $\text{DFS}(G)$

proof: ① back edge \rightarrow cycle ② cycle \rightarrow back edge



Say G has cycle v_1, \dots, v_t ,
and WLOG v_1 is visited first
when running $\text{DFS}(G)$. Then:



The claim directly leads to the following algorithm:

Is DAG(G): 1. Run $\text{DFS}(G)$ to collect pre, post numbers.

2. For each $(u,v) \in E$, if (u,v) is a back edge then output NO.

3. Output YES.

} can inline
in $\text{DFS}(G)$

$$\text{post}[v] > \text{post}[u]$$

The running time is $O(|V| + |E|)$.