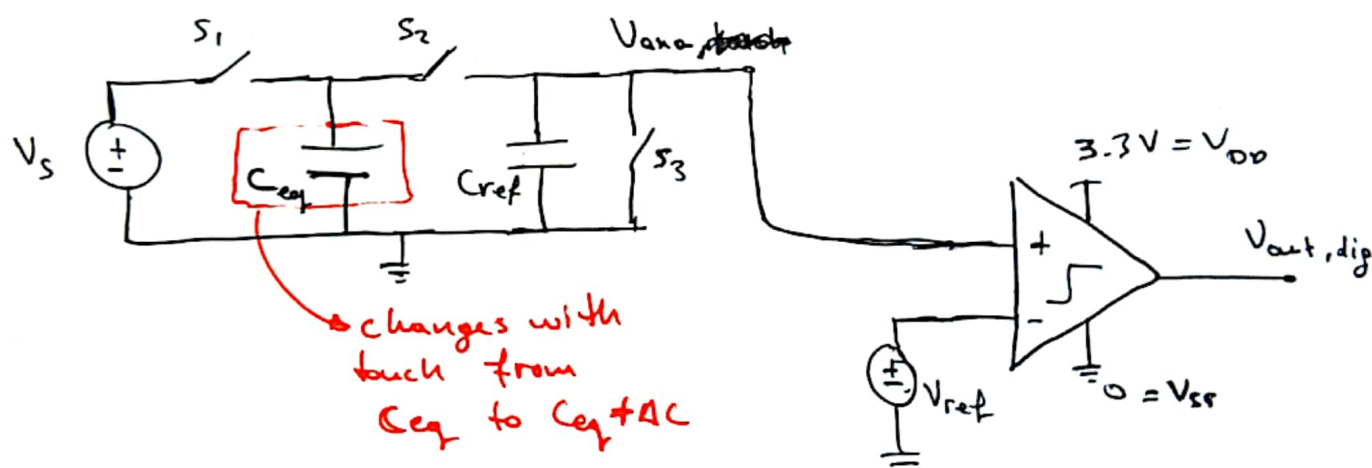


Last Time: * Built Capacitive Touchscreen } Note 17, 17B
* Charge Sharing

Today: * Capacitive Touchscreen Wrap-Up } Note 18
* Audio System (DAC example)
* Intro to Negative Feedback
* Golden Rules
* VFB Examples

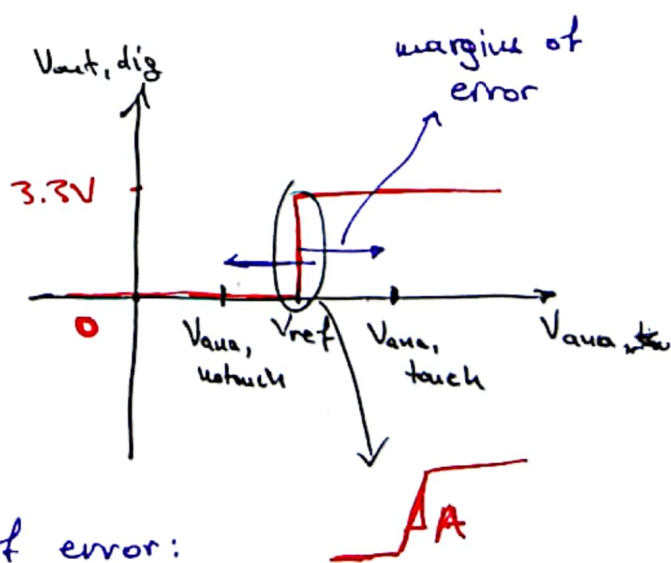
Announcements: HW 4B is out (due Sunday)



$$V_{ana, no\ touch} = \frac{C_{eq}}{C_{eq} + C_{ref}} V_s$$

$$V_{ana, touch} = \frac{C_{eq} + \Delta C}{C_{eq} + \Delta C + C_{ref}} \cdot V_s$$

$$V_{ana, touch} > V_{ana, no\ touch}$$



To maximize the margin of error:

$$V_{ref} = \frac{V_{ana, touch} + V_{ana, no\ touch}}{2}$$

A, refers to the gain of the op-amp as will be introduced in p3 of the notes

(p2)

• How to choose C_{ref} ?

Assume $C_{eq} = 1F$, $\Delta C = 1F$, $V_s = 1V$

If $C_{ref} = 0.01F$:

$$V_{ana, no touch} = \frac{C_{eq}}{C_{eq} + C_{ref}} V_s = \frac{1}{1 + 0.01} \cdot 1 = 0.99 \text{ V} \quad \left. \vphantom{\frac{1}{1 + 0.01}} \right\} 0.05 \text{ V}$$

$$V_{ana, touch} = \frac{C_{eq} + \Delta C}{C_{eq} + \Delta C + C_{ref}} V_s = \frac{2}{2 + 0.01} = 0.995 \text{ V}$$

If $C_{ref} = 1000F$:

$$V_{ana, no touch} = \frac{C_{eq}}{C_{eq} + C_{ref}} V_s = 0.001 \text{ V} \quad \left. \vphantom{\frac{C_{eq}}{C_{eq} + C_{ref}}} \right\} \Delta V = 0.01 \text{ V}$$

$$V_{ana, touch} = \frac{C_{eq} + \Delta C}{C_{eq} + \Delta C + C_{ref}} \cdot V_s = 0.002 \text{ V}$$

If $C_{ref} = 1F$:

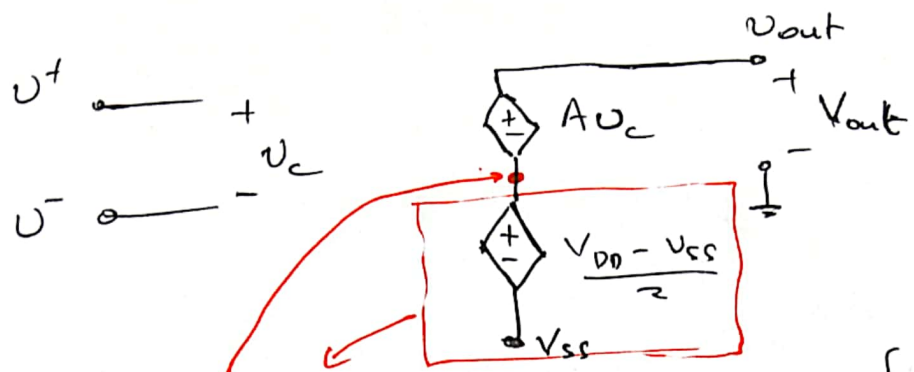
$$V_{ana, no touch} = \frac{1}{1 + 1} \cdot 1 = \frac{1}{2} \text{ V} \quad \left. \vphantom{\frac{1}{1 + 1}} \right\} 0.16 \text{ V}$$

$$V_{ana, touch} = \frac{1 + 1}{1 + 1 + 1} \cdot 1 = \frac{2}{3} \text{ V}$$

→ C_{ref} , C_{eq} and ΔC have to be of approximately the same value to have a big ΔV → big error margin!

P3

Op - Amp Circuit Model

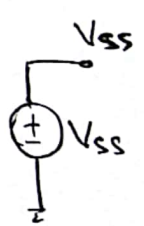
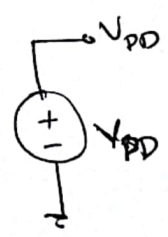
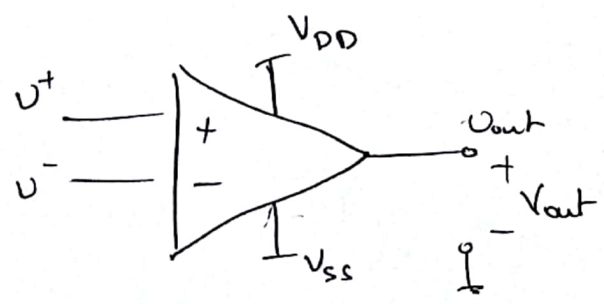


this sets that node in-between V_{DD} and V_{SS}

V_{out} cannot be higher than V_{DD} or lower than V_{SS} !

$$V_{out} = \begin{cases} V_{DD}, & \text{if } U^* > V_{DD} \\ V_{SS} + \frac{V_{DD} - V_{SS}}{2} + A \cdot u_c, & \text{if } V_{SS} < U^* < V_{DD} \\ V_{SS}, & \text{if } U^* < V_{SS} \end{cases}$$

Circuit Symbol



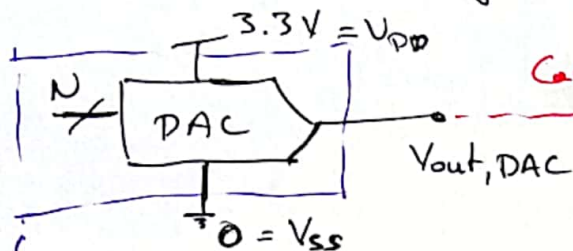
Supply connections are usually not drawn to avoid cramming the circuit diagram but they are in reality coming from independent voltage sources!

P4

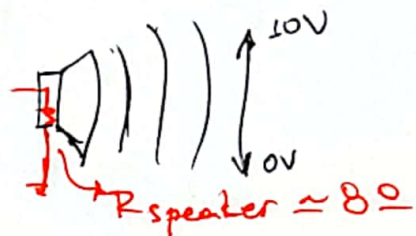
Audio System - "JGA Boombox"

Digital to Analog Converter (DAC)

Converts a digital-binary value to an analog voltage

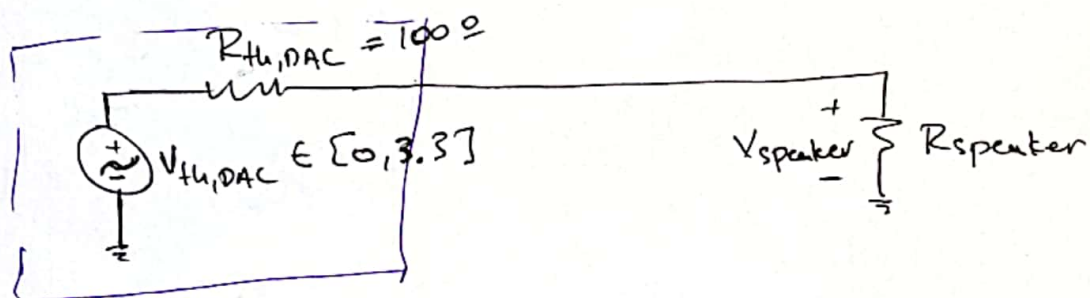


Can I directly connect?



$$V_{out, DAC} \in [0, 3.3V]$$

→ Thevenin Equivalent



→ Voltage Divider

$$V_{speaker} = \frac{R_{speaker}}{R_{speaker} + R_{th, DAC}} \cdot V_{th, DAC}$$

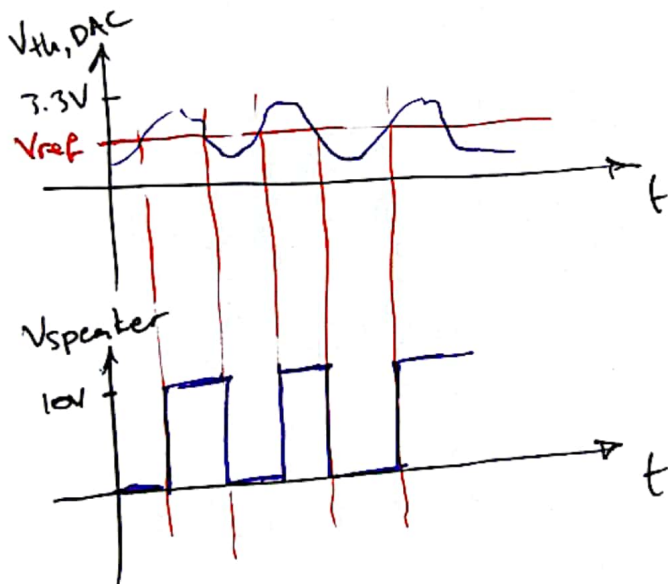
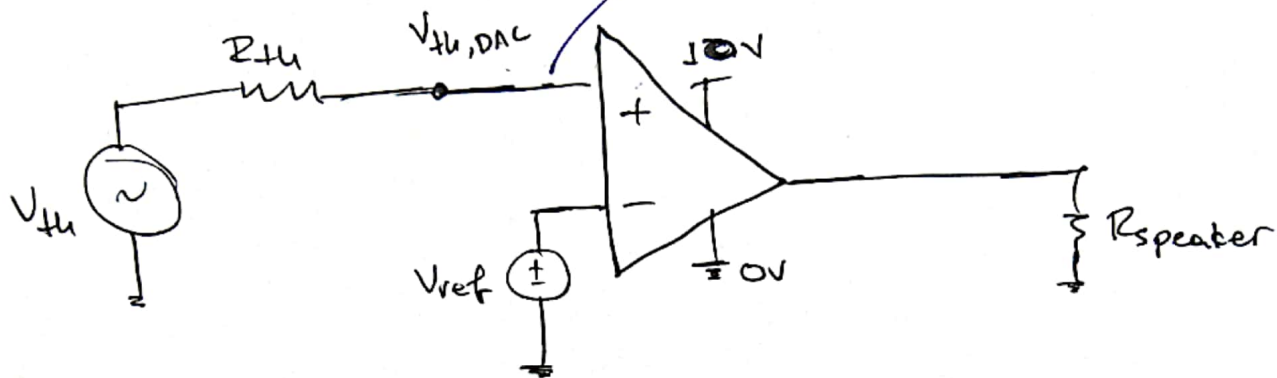
$$= \frac{8\Omega}{8\Omega + 100\Omega} \cdot V_{th, DAC} \approx 0.1 V_{th, DAC}$$

Loading Effect!



(P5)

- Goals:
- 1) Isolate R_{speaker} from Thevenin Equivalent of the DAC (get rid of Loading Effect) ✓
 - 2) Amplify by a well-known, stable value of 3 ✗



(Beethoven)

(Iron-Maiden)

We need to "tame" the op-amp.

To do this we will introduce Negative Feedback.

⑥ Negative Feedback (NFB)

→ Goal: get a certain gain (e.g. 3)

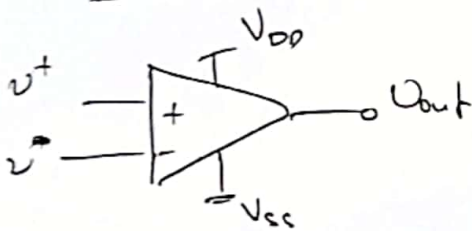
Have: op-amp with large, uncertain gain ($10^3 - 10^7$)

* Key Idea of NFB is best understood using block diagrams.

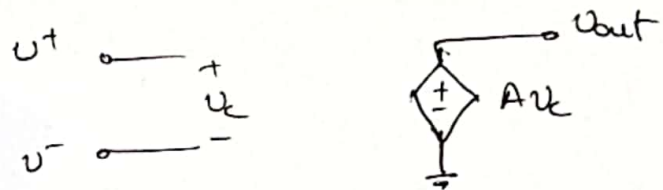
↙
A collection of drawings that indicate how various functions operate on quantities of interest.

Op-Amp

Circuit - Symbol

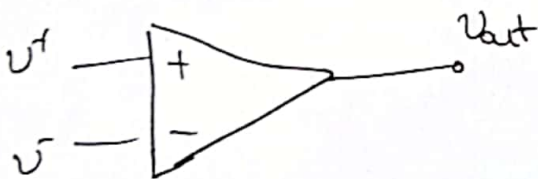


Circuit Model

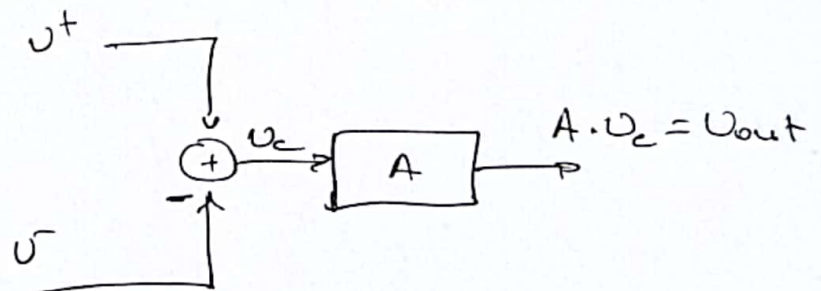


Assume $V_{DD} = -V_{SS}$

↙
* Simplified Symbol



⇒ Block Diagram

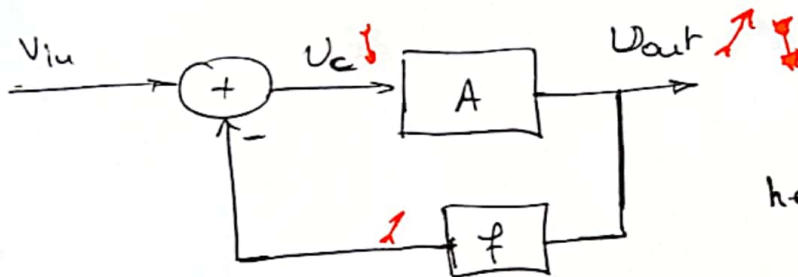


P7

Goal: $V_{out} = 3V_{in}$

NFB: Measure $\frac{V_{out}}{3}$ and compare it against V_{in} .

Block Diagram:



here $f = \frac{1}{3}$

$$\left. \begin{aligned} V_c &= V_{in} - f \cdot V_{out} \\ V_{out} &= A \cdot V_c \end{aligned} \right\} \Rightarrow V_{out} = A(V_{in} - f V_{out})$$

$$\Rightarrow V_{out} + A f V_{out} = A \cdot V_{in}$$

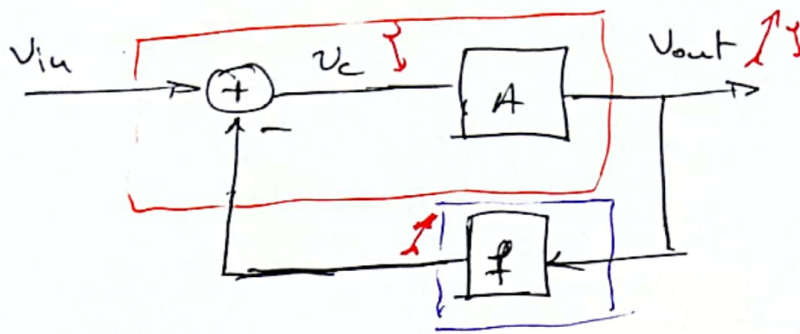
$$\Rightarrow \boxed{V_{out} = \frac{A}{1 + A f} \cdot V_{in}}$$

$$\lim_{A \rightarrow \infty} V_{out} = \lim_{A \rightarrow \infty} \frac{A}{1 + A f} \cdot V_{in} = \frac{\cancel{A}}{\cancel{A} \cdot f} \cdot V_{in} = \frac{V_{in}}{f}$$

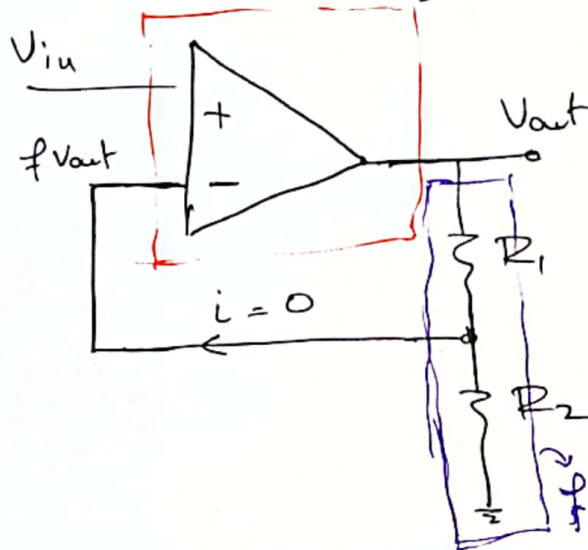
$$\Rightarrow \boxed{V_{out} = \frac{V_{in}}{f}} \text{ as } A \rightarrow \infty$$



PO



Circuit Implementation of the block diagram



$$\text{If } R_1 = 2R_2 \Rightarrow$$

$$V_- = \frac{R_2}{R_1 + R_2} \cdot V_{out}$$

$$= \frac{1}{3} \cdot V_{out}$$

$$\boxed{f = \frac{1}{3} = \frac{R_2}{R_1 + R_2}}$$

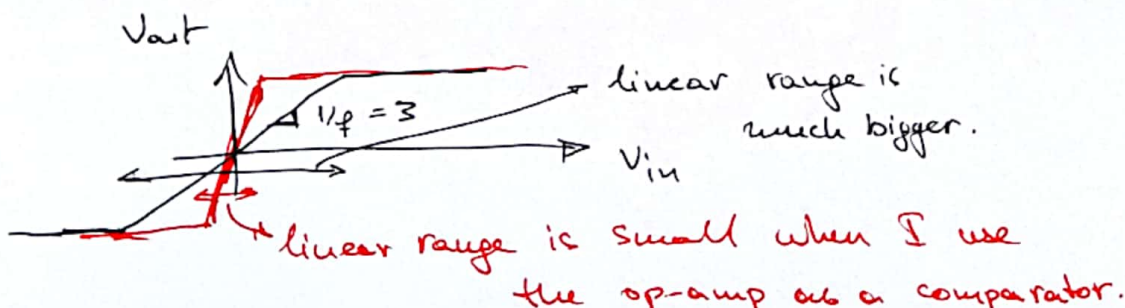
Goal:



Based on: $V_{out} = \frac{V_{in}}{f} \Rightarrow V_{out} = \frac{R_1 + R_2}{R_2} \cdot V_{in}$

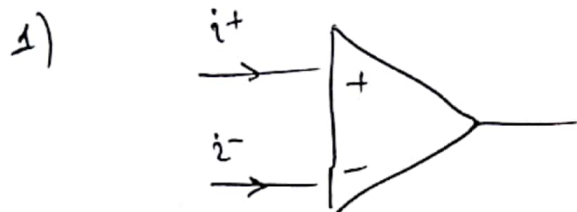
$$\Rightarrow V_{out} = \left(1 + \frac{R_1}{R_2}\right) V_{in}$$

NON-INVERTING AMPLIFIER.



99

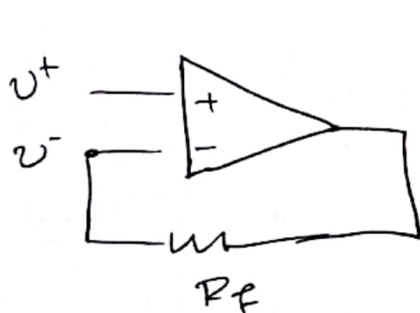
Golden Rules



$$\boxed{i^+ = i^- = 0}$$

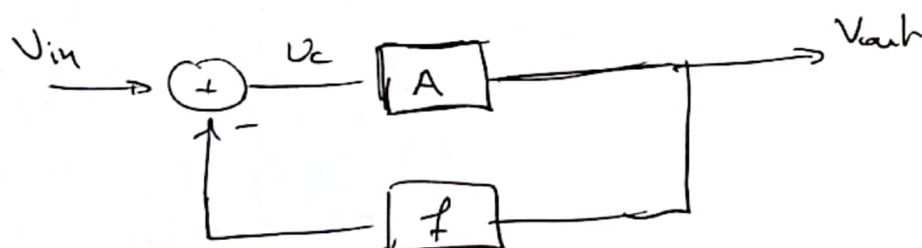
Always

2) Op-Amp in NFB with $A \rightarrow \infty$:



$$\boxed{v^+ = v^-}$$

Proof: $v^+ - v^- = v_c$



$$\begin{aligned} v_c &= V_{in} - f \cdot V_{out} \\ &= V_{in} - A \cdot f \cdot v_c \end{aligned}$$

$$\Rightarrow v_c (1 + A f) = V_{in}$$

$$\Rightarrow v_c = \frac{1}{1 + A f} \cdot V_{in} \xrightarrow{A \rightarrow \infty} v_c = 0$$

$$\Rightarrow v_+ - v_- = 0$$

$$\Rightarrow \boxed{v_+ = v_-} \quad \text{G.R. \#2}$$

✓