1)

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| --- | --- | --- |
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| Fix(1,16,8) | Fix(1,18,8) | SIGNAL In1\_signed : signed(15 DOWNTO 0); -- sfix16\_En8  SIGNAL Data\_Type\_Conversion\_out1: signed(17 DOWNTO 0); -- sfix18\_En8  Data\_Type\_Conversion\_out1 <= resize(In1\_signed, 18); |
| Fix(1,16,8) | Fix(1,16,10) | Data\_Type\_Conversion1\_out1 <= In2\_signed(13 DOWNTO 0) & '0' & '0'; |
| Fix(0,16,8) | Fix(0,18,8) |  |
| Fix(1,16,8) | Fix(1,16,10) |  |
| Fix(1,16,8) | Fix(1,18,8) | Data\_Type\_Conversion1\_out1 <= In2\_signed & '0' & '0'; |
|  |  |  |
|  |  |  |

2)